

وزارة التعليم العالي والبحث العلمي
جهاز الإشراف والتقويم العلمي
دائرة ضمان الجودة والاعتماد الأكاديمي
قسم الاعتماد



دليل وصف البرنامج الأكاديمي والمقرر الدراسي

المقدمة:

يُعد البرنامج التعليمي بمثابة حزمة منسقة ومنظمة من المقررات الدراسية التي تشتمل على إجراءات وخبرات تنظم بشكل مفردات دراسية الغرض الأساس منها بناء وصقل مهارات الخريجين مما يجعلهم مؤهلين لتلبية متطلبات سوق العمل يتم مراجعته وتقييمه سنوياً عبر إجراءات وبرامج التدقيق الداخلي أو الخارجي مثل برنامج الممتحن الخارجي.

يقدم وصف البرنامج الأكاديمي ملخص موجز للسمات الرئيسة للبرنامج ومقرراته مبيناً المهارات التي يتم العمل على اكسابها للطلبة مبنية على وفق اهداف البرنامج الأكاديمي وتتجلى أهمية هذا الوصف لكونه يمثل الحجر الأساس في الحصول على الاعتماد البرامجي ويشترك في كتابته الملاكات التدريسية بإشراف اللجان العلمية في الأقسام العلمية.

ويتضمن هذا الدليل بنسخته الثانية وصفاً للبرنامج الأكاديمي بعد تحديث مفردات وفقرات الدليل السابق في ضوء مستجدات وتطورات النظام التعليمي في العراق والذي تضمن وصف البرنامج الأكاديمي بشكلها التقليدي نظام (سنوي، فصلي) فضلاً عن اعتماد وصف البرنامج الأكاديمي المعمم بموجب كتاب دائرة الدراسات ت م ٢٩٠٦/٣ في ٢٠٢٣/٥/٣ فيما يخص البرامج التي تعتمد مسار بولونيا أساساً لعملها.

وفي هذا المجال لا يسعنا إلا أن نؤكد على أهمية كتابة وصف البرامج الأكاديمية والمقررات الدراسية لضمان حسن سير العملية التعليمية.

مفاهيم ومصطلحات:

وصف البرنامج الأكاديمي: يوفر وصف البرنامج الأكاديمي إيجازاً مقتضباً لرؤيته ورسالته وأهدافه متضمناً وصفاً دقيقاً لمخرجات التعلم المستهدفة على وفق استراتيجيات تعلم محددة.

وصف المقرر: يوفر إيجازاً مقتضياً لأهم خصائص المقرر ومخرجات التعلم المتوقعة من الطالب تحقيقها مبرهنأ عما إذا كان قد حقق الاستفادة القصوى من فرص التعلم المتاحة. ويكون مشتق من وصف البرنامج.

رؤية البرنامج: صورة طموحة لمستقبل البرنامج الأكاديمي ليكون برنامجاً متطوراً وملهماً ومحفزاً وواقعياً وقابلاً للتطبيق.

رسالة البرنامج: توضح الأهداف والأنشطة اللازمة لتحقيقها بشكل موجز كما يحدد مسارات تطور البرنامج واتجاهاته.

أهداف البرنامج: هي عبارات تصف ما ينوي البرنامج الأكاديمي تحقيقه خلال فترة زمنية محددة وتكون قابلة للقياس والملاحظة.

هيكلية المنهج: كافة المقررات الدراسية / المواد الدراسية التي يتضمنها البرنامج الأكاديمي على وفق نظام التعلم المعتمد (فصلي، سنوي، مسار بولونيا) سواء كانت متطلب (وزارة، جامعة، كلية وقسم علمي) مع عدد الوحدات الدراسية.

مخرجات التعلم: مجموعة متوافقة من المعارف والمهارات والقيم التي اكتسبها الطالب بعد انتهاء البرنامج الأكاديمي بنجاح ويجب أن يُحدد مخرجات التعلم لكل مقرر بالشكل الذي يحقق أهداف البرنامج.

استراتيجيات التعليم والتعلم: بأنها الاستراتيجيات المستخدمة من قبل عضو هيئة التدريس لتطوير تعليم وتعلم الطالب وهي خطط يتم إتباعها للوصول إلى أهداف التعلم. أي تصف جميع الأنشطة الصفية واللاصفية لتحقيق نتائج التعلم للبرنامج.

نموذج وصف البرنامج الأكاديمي

اسم الجامعة: الجامعة التقنية الشمالية
الكلية/ المعهد: الكلية التقنية الهندسية للحاسوب والدكاء الاصطناعي
القسم العلمي: هندسة تقنيات الحاسوب
اسم البرنامج الأكاديمي أو المهني: بكالوريوس هندسة تقنيات الحاسوب
اسم الشهادة النهائية: بكالوريوس هندسة تقنيات الحاسوب
النظام الدراسي: مقررات و بولونيا
تاريخ اعداد الوصف: 2026/6/25
تاريخ ملء الملف: 2026/6/25


التوقيع:
اسم المعاون العلمي: د. علي قاسم سعيد
التاريخ: 2026/6/30


التوقيع:
اسم رئيس القسم: أ.م.د. عماد احمد محمد
التاريخ: 2026 / 6 / 30

دقق الملف من قبل
شعبة ضمان الجودة والأداء الجامعي
اسم مدير شعبة ضمان الجودة والأداء الجامعي: نور قحطان يونس

التاريخ 2026 / 6 / 30
التوقيع 


أ.م.د. عمر ابراهيم دلال باشي
مصادقة السيد العميد



١. رؤية البرنامج

يسعى قسم هندسة تقنيات الحاسوب ليكون من اهم الأقسام المتميزة علمياً وأن يكون القسم مبدعاً وريادياً وفاعلاً ضمن اقسام الكلية في مجال هندسة تقنيات الحاسوب لجعله صرحاً علمياً يرفع من جودة التعليم العالي والبحث الاكاديمي باستخدام تقنيات متقدمة ليساهم في تطوير وخدمة المجتمع المحلي والاقليمي بوساطة رفده بالكوادر الكفوءة والمؤهلة تأهيلاً عالياً في اكتساب المعرفة واستخدام أحدث التقنيات العلمية الحديثة في ظل وجود سوق تنافسية مفتوحة كما يطمح الى تطوير البحث العلمي بحيث يساهم في رفع فعالية التعليم.

٢. رسالة البرنامج

يسهم قسم الحاسوب في اداء الرسالة الأكاديمية التي تطلع بها الكلية كما يقوم بأعداد مهندسين تقنيين اكفاء قادرين على تلبية سوق العمل المحلية في مجالي تقنيات هندسة الشبكات والاتصالات وتقنيات هندسة الالكترونيات ليتمكن المهندس التقني الربط بين الاساس النظري والجانب العلمي والتطبيقي للبرامج لكي ينسجم مع معايير الجودة ويكونون قادرين على تلبية احتياجات سوق العمل الحكومي والمختلط والخاص، كما تتبنى نمذجة مبادئ الحاسوب والرياضيات وصولاً الى التكامل المعرفي والارتقاء بالطالب الى مستوى الطموح الذي يخدم المجتمع العراقي كما يطمح للوصول الى العالمية.

٣. اهداف البرنامج

1. اعداد كوادر من المهندسين التقنيين ذوو مستوى عالي من الفهم والمعرفة والاعداد النفسي قادرة على التعامل مع المنظومات الالكترونية ومنظومات التحكم باستخدام الحاسوب فضلاً عن الاتصالات وشبكات الحاسوب وجودة التنفيذ في مجال هندسة تقنيات الحاسوب.

2. التطوير الدائم للبرامج الدراسية بوساطة التعاون مع الاقسام المناظرة لتلائم متطلبات برامج التنمية المحلية واحتياجات سوق العمل وتوفير بيئة ملائمة للتدريس النظري والعملي باستخدام أحدث الوسائل التعليمية والاجهزة واعتماد مقياس الجودة والاعتماد الأكاديمي.
3. اكساب الطالب المعارف المتعلقة بتطوير المشاريع البرمجية الكبيرة والمتوسطة في الوقت المحدد وباتجاه الاهداف الموضوعية.
4. يتمكن الخريج تقييم الحلول البديلة للمشاكل التي تصادفه في عمله والبت في امكانية استخدام تكنولوجيا المعلومات في تطبيق الحل.
5. تفعيل البرامج التي تصب في مصلحة الاختصاص والمدعومة من الراعيين الرسميين على مستوى العالم بحيث يتم تسليح المهندس الخريج بشهادات عالمية بجانب شهادته الاكاديمية.
6. فهم اسباب وكيفية التأثير التكنولوجي على محيط العمل.
7. اكتساب الاسس التعليمية السليمة للتفاعل مع التطورات المستقبلية في مجال الحاسوب والاتصالات.
8. اجراء البحوث العلمية البحتة والتطبيقية لمواكبة التطور العلمي.
9. مد الجسور مع المجتمع بوساطة اقامة الدورات العلمية والندوات والورش من اجل خدمته.
10. التنمية المستمرة لقدرات اعضاء هيئة التدريس والفنيين والاداريين في القسم بوساطة المشاركة في الندوات والمؤتمرات وورش العمل التي يقيمها القسم أو الاقسام المناظرة في الجامعات المحلية.
11. اعداد وتأهيل المختصين لتلبية متطلبات سوق العمل بقطاعيه العام والخاص في مجال الحاسوب بوساطة التنوع في طرق التعلم والتعليم وتدريب الطلاب على تطبيق المعارف والمهارات المكتسبة لحل مشاكل واقعية لضمان المساهمة الفعالة في خدمة المجتمع بعد التخرج وفق خطة ورؤية مستقبلية لتطوير وتوسيع دائرة المشاركة المجتمعية.
12. اعتماد نظرة واقعية فعالة لتطوير امكانيات القسم في اجراء البحوث والدراسات وتهيئة مستلزمات الدراسات العليا ليتوافق مع معايير الجودة في وزارة التعليم العالي والبحث العلمي وخدمة المجتمع من

خلال خريجين مزودين بمختلف المعارف التي تؤهلهم للتميز في مجال عملهم وقادرين على التكيف مع مختلف ظروف العمل ومواكبة التطورات المتسارعة في مجال التخصص مع احترام اخلاقيات المهنة.

13. اعداد القيادات العلمية المتخصصة بوساطة برنامج الدراسات العليا لرفد المجتمع بالمؤهلات العلمية والتقنية.

٤. الاعتماد البرامجي
القسم حاصل على الاعتماد البرامجي

٥. المؤثرات الخارجية الأخرى
لا يوجد

٦. هيكلية البرنامج				
هيكل البرنامج	عدد المقررات	وحدة دراسية	النسبة المئوية	ملاحظات *
متطلبات المؤسسة	٦	١٢	٥	مقرر اساسي
متطلبات الكلية	٤	١٨	٨	
متطلبات القسم	٤٦	٢١٠	٨٧	
التدريب الصيفي	٢	—	—	
أخرى				

* ممكن ان تتضمن الملاحظات فيما اذا كان المقرر أساسي او اختياري .

٧. وصف البرنامج				
الساعات المعتمدة		اسم المقرر أو المساق	رمز المقرر أو المساق	السنة / المستوى
عملي	نظري			
٢	٢	منطق رقمي	CTE100	الاول
٢	٢	مبادئ الهندسة الكهربائية	CTE101	الاول
٢	٢	تركيب الحاسوب	CTE102	الاول
٢	١	رسم هندسي	CTE103	الاول
٠	٣	رياضيات	TECCA100	الاول
٠	٢	ديمقراطية وحقوق انسان	NTU100	الاول
٠	٢	لغة انكليزية	NTU101	الاول
٢	٢	دوائر منطقية	CTE104	الاول
٠	٣	رياضيات هندسية	CTE105	الاول
٢	٢	دوائر كهربائية	CTE106	الاول
٢	٠	ورشة هندسية	CTE107	الاول
٢	٢	أساسيات البرمجة	CTE108	الاول
٢	١	مبادئ الحاسوب	NTU102	الاول
٠	٢	اللغة العربية	NTU103	الاول
٢	٢	المعالجات الدقيقة	CTE200	الثاني
٢	٢	اساسيات الالكترونيك التناظري	CTE201	الثاني
٢	٢	البرمجة الشيئية	CTE202	الثاني
٠	٣	الرياضيات التطبيقية	CTE203	الثاني
٢	٢	هياكل بيانات	CTE204	الثاني
٢	٢	القياسات والمتحسسات	CTE205	الثاني
٠	٢	جرائم حزب البعث البائد	NTU200	الثاني
٢	٢	معمارية الحاسوب	CTE206	الثاني
٢	٢	دوائر الكترونية	CTE207	الثاني

٢	٢	اسس الاتصالات	CTE208	الثاني
٢	٢	تطبيقات الحاسوب	CTE209	الثاني
٢	٢	تصميم مواقع الكترونية	CTE210	الثاني
٥	٠	التدريب الصيفي ١	CTE211	الثاني
٠	٢	اللغة العربية	NTU203	الثاني
٢	٢	هندسة السيطرة	CTE300	الثالث
٢	٢	الشرائح الداعمة للمعالج	CTE301	الثالث
٢	٢	معالجة اشارة رقمية	CTE302	الثالث
٢	٢	تحليلات هندسية	CTE303	الثالث
٢	٢	اساسيات الاتصالات الرقمية	CTE304	الثالث
٢	٢	اسس شبكات الحاسوب	CTE305	الثالث
٢	٢	مسيطرات	CTE306	الثالث
٢	٢	انظمة تشغيل	CTE307	الثالث
٢	٢	اشارات ونظم	CTE308	الثالث
٢	٢	شبكة المتحسسات اللاسلكية وانترنت الاشياء	CTE309	الثالث
٢	٢	انظمة اتصالات رقمية	CTE310	الثالث
٢	٢	انظمة شبكات الحاسوب	CTE311	الثالث
٥	٠	التدريب الصيفي ٢	CTE312	الثالث
٠	٢	لغة انكليزية ٤	NTU401	الرابع
٢	٢	إدارة	TEMO402	الرابع
٤	٠	مشروع ١	TEMO403	الرابع
٤	٠	مشروع ٢	TEMO414	الرابع
٢	٢	نظرية المعلومات	CCT404	الرابع
٢	٢	الاتصالات الرقمية ٢	CCT405	الرابع
٢	٢	معالجة الاشارة الرقمية	CCT406	الرابع
٢	٢	ذكاء اصطناعي ١	CCT407	الرابع

٢	٢	الترميز	CCT408	الرابع
٢	٢	امنية الحاسبات	CCT409	الرابع
٢	٢	معالج دقيق ٢	CCT410	الرابع
٢	٢	ذكاء اصطناعي ٢	CCT411	الرابع

٨. مخرجات التعلم المتوقعة للبرنامج
أ- المعرفة
اهم مخرجات التعلم لبرنامج هندسة تقنيات الحاسوب في مجال المعرفة هي: ١- أن يكون الطالب قادرًا على الالمام بالمكونات المادية والبرمجة للحاسوب ٢- أن يكون الطالب قادرًا على فهم وتطبيق المفاهيم الأساسية في الرياضيات والعلوم التطبيقية التي تدعم هندسة الحاسوب ٣- أن يكون الطالب قادرًا على تصميم الدوائر المنطقية والمتكاملة ٤- أن يكون الطالب قادرًا على تحديد مكونات وطبقات شبكات الحاسوب وتحليل البروتوكولات الخاصة بها ٥- أن يكون الطالب قادرًا على ادارة الموارد المادية في الحاسوب كالذاكرة وانظمة التشغيل ٦- أن يكون الطالب قادرًا على استيعاب منهجيات تطوير البرمجيات وهندستها
ب-المهارات
اهم مخرجات التعلم لبرنامج هندسة تقنيات الحاسوب في مجال المعرفة هي: ١- أن يكون الطالب قادرًا على صيانة وتجميع اجزاء الحاسوب ٢- أن يكون الطالب قادرًا على ربط شبكات الحاسوب بمختلف انواعها ٣- أن يكون الطالب قادرًا على تصميم المواقع الالكترونية ٤- أن يكون الطالب قادرًا على حماية المنظومات من البرامج الضارة
ج- القيم
اهم مخرجات التعلم لبرنامج هندسة تقنيات الحاسوب في مجال القيم هي: ١- أن يكون الطالب قادرًا على معرفة اخلاقيات مهنة الهندسة ٢- أن يكون الطالب قادرًا على التواصل مع الاخرين من خلال المجموعات البحثية ٣- أن يكون الطالب قادرًا على تحمل المسؤولية ٤- أن يكون الطالب قادرًا على الالتزام بمعايير الجودة والدقة

٩. استراتيجيات التعليم والتعلم
١- التعليم النظري من خلال المحاضرات النظرية في قاعة المحاضرات ٢- التعليم العملي من خلال المحاضرات العملية فب المختبرات ٣- التعليم الميداني من خلال التدريب الصيفي للطلبة

١٠. طرائق التقييم
الامتحانات الأسبوعية والشهرية واليومية وامتحان نهاية الفصل

١١. الهيئة التدريسية					
أعضاء هيئة التدريس (يذكر جميع التدريسيين في القسم العلمي مع المحاضرين الخارجين والداخلين)					
المرتبة العلمية		التخصص		المتطلبات/المهارات الخاصة (ان وجدت)	
				اعداد الهيئة التدريسية	
		عام	خاص	ملاك	محاضر
أ.م.د. عماد احمد محمد		هندسة اتصالات	شبكات حاسبات	ملاك	
م. نجوان زهير ويسى		علوم حاسوب	امنية معلومات	ملاك	
أ.م.د. احمد فالح محمود		هندسة كهرباء	شبكات حاسبات	ملاك	
أ.م.د. زياد سعيد محمد		هندسة كهرباء	حالة صلبة	ملاك	
أ.م.د. عيشة ابراهيم محمد		لغة عربية	لغة عربية	ملاك	
أ.م.د. احمد خزعل يونس		هندسة حاسبات	هندسة حاسبات	ملاك	
م.د. ثبات فخري يونس احمد		هندسة كهرباء	الالكترونيك	ملاك	

م. حكم مروان زيدان قدوري	هندسة حاسبات	هندسة حاسبات		ملاك	
م. زيد غانم محمد عبوش	هندسة تقنيات الحاسوب	هندسة تقنيات الحاسوب		ملاك	
نقاء لقمان محمد حمو	هندسة تقنيات الحاسوب	هندسة تقنيات الحاسوب		ملاك	
م.م. عمر زياد طارق	هندسة حاسبات	هندسة حاسبات		ملاك	
م.م. ازهار وليد طلب	هندسة تقنيات الحاسوب	هندسة تقنيات الحاسوب		ملاك	
م.م. زيد عبدالستار عبدالرزاق	هندسة حاسبات	هندسة حاسبات		ملاك	
م.م. احمد هاشم احمد عبد الله	هندسة كهرباء	قدرة ومكائن		ملاك	
م.م. ياسر مصلح عبدال	هندسة تقنيات الحاسوب	هندسة تقنيات الحاسوب		ملاك	
م.م. نور سعدالله عناد	هندسة اتصالات	هندسة اتصالات		ملاك	
م.د. ضياء حسين حماد	هندسة حاسبات	هندسة حاسبات		ملاك	
م.م. اريج محمود اسعد	هندسة تقنيات الحاسوب	هندسة تقنيات الحاسوب		ملاك	
لباب حارث سامي	هندسة تقنيات الحاسوب	هندسة تقنيات الحاسوب		ملاك	
م.م. رشا بشار محمد بشير	هندسة اتصالات	هندسة اتصالات		ملاك	
م.م. ضحى عبد المنعم محمد	هندسة كهرباء	الالكترونيك		ملاك	

التطوير المهني
توجيه أعضاء هيئة التدريس الجدد
١. تطوير برنامج توجيهي لتعريف أعضاء هيئة التدريس الجدد بالمؤسسة وسياساتها وإجراءاتها. ٢. إقران أعضاء هيئة التدريس الجدد بمرشدين ذوي خبرة لتقديم التوجيه والدعم والمشورة ٣. تنظيم ورش عمل تدريبية منتظمة حول مناهج التدريس والتقييم وتقنيات البحث ٤. إنشاء آلية ردود فعل لأعضاء هيئة التدريس الجدد لتلقي ردود فعل بناءة حول أدائهم ومجالات التحسين ٥. تشجيع المشاركة في المؤتمرات والندوات ومشاريع البحث لتعزيز النمو المهني
التطوير المهني لأعضاء هيئة التدريس
١. تقييم مهاراتهم أعضاء هيئة التدريس لدراسة احتياجاتهم التعليمية والفنية. ٢. تقديم دورات تدريبية للمساعدة في تطوير مهارات أعضاء هيئة التدريس في مجالات مثل التعليم المتقدم وتقنيات التعليم والتقييم الدقيق. ٣. تشجيع العمل الجماعي بين أعضاء هيئة التدريس لتبادل الخبرات والمعرفة. ٤. تقديم تحديثات مستمرة حول أحدث ما توصل إليه العلم في مجالات التعليم وتقنيات التعليم. ٥. تقديم الدعم المستمر لأعضاء هيئة التدريس للمساعدة في حل المشكلات والتحديات التي يواجهونها. ٦. إجراء تقييمات دورية لمراجعة التطوير المهني لأعضاء هيئة التدريس وتحديد المزايا والعيوب

١٢. معيار القبول
المعايير المعتمدة في القبول المركزي الخاصة بوزارة التعليم العالي والبحث العلمي

١٣. أهم مصادر المعلومات عن البرنامج
١. موقع الجامعة الإلكتروني. ٢. موقع القسم. ٣. ملفات الوصف الأكاديمي ومواصفات البرنامج. ٤. نموذج مراجعة البرنامج الأكاديمي

١٤. خطة تطوير البرنامج

١. تحديث البرمجيات الخاصة بالمختبرات

٢. استخدام المصادر الحديثة -

مخطط مهارات البرنامج												
مخرجات التعلم المطلوبة من البرنامج												
القيم				المهارات				المعرفة				السنة / المستوى
ج4	ج3	ج2	ج1	ب4	ب3	ب2	ب1	أ4	أ3	أ2	أ1	اساسي أم اختياري
	*			*		*	*	*	*			المستوى الرابع
	*			*	*	*	*	*	*			نظرية المعلومات CCT404
	*			*	*	*	*	*	*			الاتصالات الرقمية ٢ CCT405
	*			*			*	*	*	*	*	معالجة الاشارة الرقمية CCT406
	*			*	*	*		*	*			ذكاء اصطناعي ١ CCT407
	*				*		*	*	*	*	*	الترميز CCT408
						*	*	*	*			امنية الحاسبات CCT409
				*	*	*	*	*	*	*	*	معالج دقيق ٢ CCT410
				*	*	*		*	*			ذكاء اصطناعي ٢ CCT411
		*										لغة انكليزية ٤ NTU401

*	*	*	*										إدارة	TEMO402	
*	*	*	*										مشروع ١	TEMO403	
*	*	*	*										مشروع ٢	TEMO414	



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Digital Logic		Module Delivery	
Module Type	Core		☒ Theory ☒ Lecture ☒ Lab ☒ Tutorial ☒ Practical ☒ Seminar	
Module Code	CTE100			
ECTS Credits	7			
SWL (hr/sem)	175			
Module Level	UGx11 1	Semester of Delivery		1
Administering Department	Department of Computer Techniques Engineering		College	Northern Technical University Engineering Technical College/Mosul
Module Leader	Khalis A. Mohammed		e-mail	Khalis_am@ntu.edu.iq
Module Leader's Acad. Title	Lecturer		Module Leader's Qualification	M.Sc.
Module Tutor	None		e-mail	None
Peer Reviewer Name	None		e-mail	None
Review Committee Approval	21/06/2023		Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	- To learn the basic techniques and methodologies for designing and analyzing digital systems and how to apply these techniques to build specific circuits. - Define the problem (Inputs and Outputs), write its functions - Implement functions using Combinational digital circuit. - Minimize functions using any type of minimizing algorithms (Boolean algebra, Karnaugh-Map or Tabulation Method). - Have knowledge in analyzing and designing procedures of Combinational digital circuits.

Module Learning Outcomes مخرجات التعلم للمادة الدراسية	1. Learning about the different number systems. 2. Learning the arithmetic operations related to different number systems. 3. Learning the different logic gates of computer system and their work. 4. Ability to design, simplify and implement different logical and arithmetic circuits that considered the basic of digital system. 5. Ability to design, simplify and implement different sequential circuits, counters and shift registers.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: Part 1 – Numbers Systems, Operations, and Codes Different Number Systems, Data representation (integer and fraction) using different number systems. Conversion Between Different Numbers Systems. Arithmetic operations using different number systems, and Digital Codes (BCD, Parity, Gray, Excess-3 etc.) [14 hrs] Part 2- Logic Gates The Inverter (NOT Gate), AND Gate, OR Gate, NAND Gate, NOR Gate, the Exclusive-OR Gate and Exclusive-NOR Gates. [12 hrs] Part 3 Boolean Algebra and Logic Simplification Boolean Operations and Expressions, Laws and Rules of Boolean Algebra, Simplification Using Boolean Algebra, DE Morgan's theorems, The Karnaugh Map (1, 2 , 3 and 4 variables) , SOP and POS Minimization. [16 hrs] Part 4 Combinational Logic Analysis Basic Combinational Logic Circuits, Implementing Combinational Logic, Combinational Logic Using NAND and NOR Gates, Logic Circuit Operation with Pulse Waveform Inputs. [10 hrs] - Revision problem classes [6 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	74	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعيا	4.93
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	101	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعيا	6.73
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	175		

Module Evaluation

تقييم المادة الدراسية

As		Time/ Number	Weight % (Marks for each item)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	3	5% (10)	3,6,10	LO #2, 4 and 5
	Projects	3	5% (10)	2,5,11	All

	Lab.	1	10% (60)	12	LO # 1-4
	Reports	6	10% (10)	Continuous	All
	Assignments	3	5% (10)	4,7,12	LO #3, 4 and 5
	Seminars	3	5% (10)	2,5,11	All
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-4
	Final Exam	3 hr	50% (50)	15	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	1- Numbers Systems, Operations, and Codes: Decimal Numbers, Binary numbers.
Week 2	1- Numbers Systems, Operations, and Codes: Hexadecimal Numbers, Octal numbers.
Week 3	1- Numbers Systems, Operations, and Codes: Data representation (integer and fraction) using different number systems. Conversion Between Different Numbers Systems .
Week 4	1- Numbers Systems, Operations, and Codes: Arithmetic operations using 9's and 10's Complements of Decimal Numbers. Arithmetic operations using 1's and 2's Complements of Binary Numbers.
Week 5	1- Numbers Systems, Operations, and Codes: Signed Numbers, Arithmetic Operations with Signed Numbers.
Week 6	1- Numbers Systems, Operations, and Codes: Digital Codes (BCD, Excess-3, Parity, Gray etc.).
Week 7	2- Logic Gates: The Inverter (NOT Gate), The AND Gate, The OR Gate.
Week 8	2- Logic Gates: NAND Gate, NOR Gate, Exclusive-OR Gate and Exclusive-NOR Gates.
Week 9	3- Boolean Algebra and Logic Simplification: Boolean Operations and Expressions.
Week10	3- Boolean Algebra and Logic Simplification: Laws and Rules of Boolean Algebra.
Week11	3- Boolean Algebra and Logic Simplification Simplification Using Boolean Algebra. DeMorgan's theorems.
Week12	3- Boolean Algebra and Logic Simplification The Karnaugh Map (1, 2 , 3 and 4 variables) , SOP and POS Minimization.
Week13	4- Combinational Logic Analysis: Basic Combinational Logic Circuits. Implementing Combinational Logic.
Week14	4- Combinational Logic Analysis: Combinational Logic Using NAND and NOR Gates. Logic Circuit Operation with Pulse Waveform Inputs.
Week15	Final Exam

Delivery Plan (Weekly Lab. Syllabus) المنهاج الاسبوعي للمختبر	
Week	Material Covered
Week 1	Lab 1: Introduction to digital laboratory kit operation
Week 2	Lab 2: Logic Gates (AND, OR, NOT, NAND, NOR).
Week 3	Lab 3: Logic Gates (XOR, XNOR).
Week 4	Lab 4: Design of (AND, OR, NOT) gates Using NAND gates.

Week 5	Lab 5: Design of (AND, OR, NOT) gates Using NOR gates.
Week 6	Lab 6: Implementation of logic circuits using NAND-gate only.
Week 7	Lab 7: Implementation of logic circuits using NOR-gate only.
Week 8	Lab 8: Implementation of DeMorgan theory, 1 st Law
Week 9	Lab 9: Implementation of DeMorgan theory, 2 nd Law
Week 10	Lab 10: Design of a combinational logic circuits . Part 1
Week 11	Lab 11: Design of a combinational logic circuits. Part 2
Week 12	Lab 12: Realization of Boolean equation. Part 1
Week 13	Lab 13: Realization of Boolean equation. Part 2
Week 14	Lab 14: Review

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	Thomas L. Floyd, Digital Fundamentals, 11th Edition, Pearson Education 2015	Yes
Recommended Texts	M. Morris Mano, Michael D. Ciletti, Digital Design, 5th edition, Pearson Education 2013	No
Websites	Digital Systems: From Logic Gates to Processors: https://www.coursera.org/learn/digital-systems	

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	<u>fundamentals of electrical engineering</u>		Module Delivery	
Module Type	<u>Core</u>		✓ Theory ✓ Lecture ✓ Lab ✓ Tutorial ✓ Practical ✓ Seminar	
Module Code	<u>CTE101</u>			
ECTS Credits	<u>7</u>			
SWL (hr/sem)	<u>175</u>			
Module Level	UGx11 1	Semester of Delivery		1
Administering Department	<u>Department of Computer Techniques Engineering</u>		College	<u>Northern Technical University Engineering Technical College/Mosul</u>
Module Leader	Maysaloon Abed Qasim		e-mail	Maysloon.alhashim@ntu.edu.iq
Module Leader's Acad. Title		Lecturer	Module Leader's Qualification	PhD
Module Tutor	None		e-mail	None
Peer Reviewer Name		None	e-mail	None
Review Committee Approval		21/06/2023	Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	1
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	This course focuses on direct current (DC) circuit analysis and aims to: 1-Understanding of the fundamental laws and elements of electrical circuits design and application. 2-Understand the techniques to analyze different circuit configuration 3- Analyze resistive circuits and laws/theorems including Kirchhoff's Superposition, Thevenin's, Nortons, and Maximum Power Transfer. 4- Develop students computational skills.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	Having successfully completed the course, students will be able to: 1- Know the various types of electric circuits. 2-Know the Elements of electric circuits and their roles 3-Apply different techniques to analyze electric circuits. 4-Solve Problem of different electric circuits 5-Compare the application of different type of electric circuits.

	6-Appreciate the importance of electric circuit elements. 7-Compare and contrast the operation of different types of electrical elements. 8-Derive equations related to the circuit's performance and design. 9-Identify different types of electrical elements and their applications.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: <u>Part A – General Electric System.</u> Constituent parts of an electrical system (source, load, communication & control), Current flow in a circuit, Electromotive force and potential difference, Electrical units. Ohm's law, Resistors, Resistivity, Temperature rise & Temperature coefficient of resistance, Voltage & Current sources [8 hrs] <u>Part B DC circuits.</u> Series circuits, Parallel circuits. Kirchhoff's laws. Power and energy [14 hrs] <u>Part C Network Theorems</u> . Star-delta & delta-star transformation. Sources transformations Mesh analysis. Nodal analysis. Superposition theorem. Thevenin's theorem. Norton's theorem. Maximum power transfer theorem. [32 hrs] - Revision problem classes [4 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	1- Hands-on Experiments: Engage students in practical experiments to deepen their understanding of circuits. 2- Simulation Software: Use circuit simulation software for virtual circuit design and analysis. 3- Problem-solving Exercises: Include various problem-solving exercises to apply circuit analysis techniques. 4- Group Projects: Assign collaborative projects for circuit design and construction. 5- Real-world Applications: Discuss practical applications of circuits in different devices and systems. 5- Interactive Discussions: Encourage student participation and critical thinking through open-ended questions. 6- Conceptual Understanding: Focus on intuitive understanding alongside mathematical analysis. 7- Assessment Variety: Use diverse assessment methods to gauge student understanding. 8- Office Hours and Support: Offer individualized assistance through office hours or online support.
-------------------	---

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	58	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	117	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	7
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	175		

Module Evaluation

تقييم المادة الدراسية

As	Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Quizzes	8	10% (10)	5, 10	LO #1, 2, 10 and 11

Formative assessment	Assignments	8	10% (10)	2, 12	LO # 3, 4, 6 and 7
	Projects / Lab.	15	10% (10)	Continuous	All
	Report	8	10% (10)	13	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week	Material Covered
Week 1	Constituent parts of an electrical system, Current flow in a circuit, Electromotive force and potential difference, Electrical units.
Week 2	Ohm's law, Resistors, Resistivity, Temperature rise & Temperature coefficient of resistance, Voltage & Current sources
Week 3	Series circuits , Parallel circuits.
Week 4	Kirchhoff's laws.
Week 5	Power and energy.
Week 6	Star-delta & delta-star transformation
Week 7	Sources transformations
Week 8	Mesh analysis.
Week 9	Nodal analysis.
Week 10	Superposition theorem.
Week 11	Thevenin's theorem
Week 12	Norton's theorem
Week 13	Maximum power transfer theorem.
Week 14	Reciprocity theorem
Week 15	Final Exam

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	<u>Lab 1: Connection of resistances in series and parallel.</u>
Week 2	<u>Lab 2: Verification of Ohm's law using hardware .</u>
Week 3	<u>Lab 3: Verification of Ohm's law using digital simulation.</u>
Week 4	<u>Lab 4: Verification of Kirchhoff's current law and Voltage law using hardware.</u>
Week 5	<u>Lab 5: Verification of Kirchhoff's current law and Voltage law using digital simulation.</u>
Week 6	<u>Lab 6: Determination of mesh currents using hardware.</u>
Week 7	<u>Lab 7: Determination of mesh currents using digital simulation.</u>
Week 8	<u>Lab 8: Measurement of nodal voltages using hardware and digital simulation.</u>
Week 9	<u>Lab 9: Verification of superposition theorem using hardware .</u>
Week 10	<u>Lab 10: Verification of superposition theorem using digital simulation.</u>
Week 11	<u>Lab 11: Verification of Thevenin's theorem using hardware.</u>
Week 12	<u>Lab 12: Verification of Thevenin's theorem using hardware.</u>

Week 13	<u>Lab 13: Verification of Norton's using hardware.</u>
Week 14	<u>Lab 14: Verification of Norton's using digital simulation.</u>

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	Charles K. Alexander, Matthew N.O. Sdiku Fundamentals of Electrical Engineering, 4th Edition, 2009	Yes
Recommended Texts	Tony R. Kuphaldt, Lessons In Electric Circuits, Volume I - DC 5th edition, Pearson Education 2002	No
Websites	<u>Direct Current (DC)</u> https://www.allaboutcircuits.com/textbook/direct-current/	

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Ministry of Higher Education and
Scientific Research - Iraq
Northern Technical University
Engineering Technical College/Mosul
Department of Computer Technology Engineering



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information					
معلومات المادة الدراسية					
Module Title	COMPUTER ORGANIZATION			Module Delivery	
Module Type	Core			✓ Theory ✓ Lecture ✓ Lab ✓ Tutorial ✓ Practical ✓ Seminar	
Module Code	CTE102				
ECTS Credits	4				
SWL (hr/sem)	100				
Module Level	UGx11 1		Semester of Delivery		1
Administering Department	Department of Computer Technology Engineering		College	Northern Technical University Engineering Technical College/Mosul	
Module Leader	Mohammed G. Ayoub		e-mail	Mohammed.ghanim@ntu.edu.iq	
Module Leader's Acad. Title	Lecturer		Module Leader's Qualification	M.Sc.	
Module Tutor	None		e-mail	None	
Peer Reviewer Name	None		e-mail	None	
Review Committee Approval	14/06/2023		Version Number	1.0	

Relation With Other Modules العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	At the end of this course, following learning objectives are expected to be achieved: -To understand principles of computer organization and the basic architectural concepts. -To understand the structure, function and characteristics of computer systems. -To understand how the various components of Computer Systems fit together and interact. -To explain the function of each element of a memory hierarchy.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	6. Understand the basic concepts and structure of computers. 7. Understand the main architectures of computer systems. 8. Learn the concept of memory hierarchy. 9. Understand the architecture and functionality of memory and storage in the computer systems. 10. Understand the theory and architecture of central processing unit.

	11. Understand the architecture and functionality of I/O units. 12. To be familiar with fundamental programming concepts and methodology. 13. Understand the theory and architecture of Intel Microprocessors.
Indicative Contents المحتويات الإرشادية	1. Introduction to Computer Systems: [2 hr.] a. Overview of Computer Characteristics b. Introduction to Software and Hardware c. Comparison of different Computers Categories 2. Introduction to Computer Architecture: [2 hr.] a. Von Neumann and Harvard architectures b. Advantages and disadvantages of both architectures 3. The Memory Hierarchy: [4 hr.] a. Overview of memory hierarchy in computer systems b. Comparison of different memory types such as Registers, Caches, Main Memory c. Calculation of Average Memory Access Time d. Overview of Cache Miss and Cache Hit 4. Types of CPU Register and their Functions: [2 hr.] a. Operations of CPU Registers b. Types and Functions of CPU Registers 5. Computer Bus Types and Functions: [2 hr.] a. Data Bus, Address Bus, Control Bus b. Internal and External Buses 6. Semiconductor Memory Types & Technologies: [6 hr.] a. Memory Array, Capacity and Addressing b. Read and Write Operations in Memory c. SRAM, DRAM, RAM Family, ROM Family d. Flash Memory, Magnetic Storage, e. Optical Storage and Cloud Storage System 7. Basic Operation of Processors: [2 hr.] a. Fetch/Execute Cycle b. Pipelining and Processor Elements 8. Levels of Programming Languages: [2 hr.] a. Assembly Language and Machine Language 9. Introduction to the Intel Microprocessors: [6 hr.] a. Pre-Pentium Intel Process b. 4004,8080/8085,8086/8086 80386,80486 and Multicore
Learning and Teaching Strategies استراتيجيات التعلم والتعليم	
Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.

--	--

Student Workload (SWL) الحمل الدراسي للطالب			
Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	58	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعيا	4.2
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	42	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعيا	3
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	100		

Module Evaluation تقييم المادة الدراسية					
As		Time/ Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (15)	3, 6, 9, 12	LO #1-2, 3-5, 6-8 and 10-11
	Assignments	2	5% (10)	2, 5, 11, 14	LO # 1, 3, 7-9 and 12-13
	Lab.	1	10% (30)	Continuous	All
	Report	6	10% (10)	Continuous	All
	Seminar	1	5% (10)	10	LO 1-8
Summative assessment	Midterm Exam	2 hr.	10% (10)	7	LO # 1-7
	Final Exam	3 hr.	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	Introduction to Computer Systems
Week 2	Introduction to Computer Architecture
Week 3	The Memory Hierarchy
Week 4	Average Memory Access Time (AMAT)
Week 5	Types of CPU Register and their Functions
Week 6	Computer Bus Types and Functions
Week 7	Basics of Semiconductor Memory Types & Technologies Parts I
Week 8	Basics of Semiconductor Memory Types & Technologies Parts II
Week 9	Basics of Semiconductor Memory Types & Technologies Parts III
Week 10	Basic Operation of Processors
Week 11	Levels of Programming Languages
Week 12	Introduction to the Intel Microprocessors Parts I
Week 13	Introduction to the Intel Microprocessors Parts II
Week 14	Introduction to the Intel Microprocessors Parts III
Week 15	Final Exam

Delivery Plan (Weekly Lab. Syllabus) المنهاج الاسبوعي للمختبر
--

Week	Material Covered
Week 1	Lab 1: Introduction to Computer System Parts
Week 2	Lab 2: Peripherals Devices
Week 3	Lab 3: Computer Monitors
Week 4	Lab 4: Computer Cables
Week 5	Lab 5: Types of Microprocessors
Week 6	Lab 6: Types of Memory in Computer System
Week 7	Lab 7: Storage in Computer System
Week 8	Lab 8: Motherboards and Graphics Card
Week 9	Lab 9: Types of Computer Ports
Week 10	Lab 10: Computer Software Part I
Week 11	Lab 11: Computer Software Part II
Week 12	Lab 12: Programming Languages
Week 13	Lab 13: Computer Networks
Week 14	Lab 14: Review

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Recommended Texts	1. 1- Computer Architecture & Organisation by Atul. P. Godse, Deepali. A. Godse. Publisher: Technical Publication 2019. 2. 2- Computer Systems Architecture by Yadin, Aharon. Publisher: Taylor & Francis Group, Year: 2016.	No
Websites	https://www.coursera.org , https://www.udemy.com	

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



MODULE DESCRIPTOR FORM

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Engineering Drawing		Module Delivery	
Module Type	Core		✓ Theory	
Module Code	CTE103		✓	✓ Lecture
ECTS Credits	3		✓	✓ Lab
SWL (hr/sem)	75		✓	✓ Tutorial
			✓	✓ Practical
			✓	✓ Seminar
Module Level		UGx11 1	Semester of Delivery	1
Administering Department		Department of Computer Techniques Engineering	College	Engineering Technical College
Module Leader	Naqaa L. Mohammed		e-mail	
Module Leader's Acad. Title		Lecturer	Module Leader's Qualification	M.Sc.
Module Tutor	None		e-mail	Naqaa_alhamo@ntu.edu.iq
Peer Reviewer Name		None	e-mail	None
Review Committee Approval		12/06/2023	Version Number	1.0

Relation with Other Modules

العلاقة مع المواد الدراسية الأخرى

Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents

أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

Module Aims أهداف المادة الدراسية	1. Define engineering drawing material, its uses and Engineering drawing tools 2. Introduction to Engineering drawing through AutoCAD software 3. Developing the student's mental and abilities in drawing simple and complex shapes
--------------------------------------	--

	4. Decomposes 3D shapes into binary projections
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	14. Learning types of engineering lines and their uses and how to draw 15. Drawing geometric shapes such as square, rectangular, parallelogram and circle 16. Learning dimensions in engineering drawing and how to put them on the drawing 17. Learning Fundamentals of projection in engineering drawing 18. Ability of drawing an anthropomorphic shape
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: • <u>Part A – AutoCAD interface</u> Setup, save, limits, grid, object snap and ortho mode [3 hrs.] • <u>Part B- Coordinate method</u> Direct distance method, Absolute coordinate, Relative coordinate, Polar coordinate[3hrs] • <u>Part C Draw menu</u> Line, polyline, rectangle, arc, circle, ellipse and hatch [12hrs] • <u>Part D Modify and Properties menu</u> Copy, move, offset, erase, extend, trim and array, line shape and line size [9 hrs.] • <u>Part D Projection</u> Front, side and top ortho projections [6 hrs.] • <u>Part E stereoscopic shapes</u> Method for drawing stereoscopic shapes[6hrs] • Revision problem classes [8 hrs.]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	45	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	3
Unstructured SWL (h/sem)	30	Unstructured SWL (h/w)	2

الحمل الدراسي غير المنتظم للطالب خلال الفصل	الحمل الدراسي غير المنتظم للطالب أسبوعيا
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	75

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	3	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	3	5% (10)	2, 12	LO # 3, 4, 6 and 7
	Projects / Lab.	1	5% (10)	Continuous	All
	Report	0	0% (0)	0	0
Summative assessment	Midterm Exam	2 hr	30% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	-Get a quick introduction to AutoCAD -Drawing Setup in AutoCAD -Use precision drawing tools such as Grid, Object Snap, and Limits to create accurate measurements in drawings.
Week 2	Coordinate method (Direct distance method, Absolute coordinate, Relative coordinate, Polar coordinate)
Week 3	Draw menu (line, poly line, polygon, rectangle).
Week 4	Drawing objects of Pentagonal, hexagonal and octagonal shapes
Week 5	Draw menu (arc, circle, ellipse, point and text).
Week 6	Draw several shapes containing circles and texts
Week 7	Modify menu (erase, copy, mirror, move offset,)
Week 8	Modify menu (rotate, trim, extend, explode)
Week 9	Properties and Layers in AutoCAD and dimension
Week 10	Orthographic projection
Week 11	Draw the three projection(front, side and top) of some shapes
Week 12	Basics of drawing stereoscopic shapes
Week 13	Draw stereoscopic shape
Week 14	Printing the graphic
Week 15	Preparatory Week
Week 16	Final Exam

Delivery Plan (Weekly Lab. Syllabus) المنهاج الاسبوعي للمختبر	
Week	Material Covered
Week 1	Lab 1: Definition of AutoCAD interface
Week 2	Lab 2: Applications of Coordinate method

Week 3	<u>Lab 3: Draw figures of lines, polygons and rectangle</u>
Week 4	<u>Lab 4: Drawing objects of Pentagonal, hexagonal and octagonal shapes</u>
Week 5	<u>Lab 5: Drawing figures of circles and ellipse</u>
Week 6	<u>Lab 6: Draw several shapes containing circles and texts</u>
Week 7	<u>Lab 7: Applications of some order in modify menu</u>
Week 8	<u>Lab 8: Applications of other order in modify menu</u>
Week 9	<u>Lab 9: Practicing of using layers</u>
Week 10	<u>Lab 10: Practicing of projection of simple figure</u>
Week 11	<u>Lab 11: Draw three projection of figure</u>
Week 12	<u>Lab 12: Practicing of drawing stereoscopic shapes</u>
Week 13	<u>Lab 13: Draw stereoscopic shape</u>
Week 14	<u>Lab 14: Practicing of Printing the graphic</u>

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	AutoCAD 2017 2D Fundamentals Randy H. Shih © Tutorial First Level by Randy H. Shih	No
Recommended Texts	Introduction to AutoCAD 2011 2D and 3D Design, Alf Yarwood	No
Websites	<u>https://youtu.be/XFO8VQT731M Introduction to AutoCad 2017 Tutorial series</u>	

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Human rights and Democracy		Module Delivery	
Module Type	Supplement		✓ Theory ✓ Lecture Lab Tutorial Practical ✓ Seminar	
Module Code	NTU100			
ECTS Credits	2			
SWL (hr/sem)	50			
Module Level	UGx11 1	Semester of Delivery		1
Administering Department	Department of Computer Techniques Engineering		College	Northern Technical University Engineering Technical College/Mosul
Module Leader	Dr. Eesha I. Mohammed		e-mail	aysha.ibrahim@ntu.edu.iq
Module Leader's Acad. Title		Assist Prof.	Module Leader's Qualification PHD	
Module Tutor	None		e-mail	None
Peer Reviewer Name		None	e-mail	None
Review Committee Approval		21/06/2023	Version Number 1.0	

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None		Semester
Co-requisites module	None		Semester

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	تهدف الديمقراطية وحقوق الإنسان للحفاظ على كرامة الفرد وحقوقه الأساسية وتعزيزها كما تحقيق العدالة الاجتماعية وتشجيع التنمية الاقتصادية والاجتماعية للمجتمع وتماسكه فضلاً عن توطيد الأمن الوطني وإرساء مناخ مؤات للسلام الدولي وذلك لأن حقوق الإنسان والديمقراطية مرجعاً أساسياً للجميع لحماية حقوق الإنسان؛ وهي توفر بيئة لحماية حقوق الإنسان وإعمالها إعمالاً فعلياً. واليوم، بعد مضي فترة على تحقيق الديمقراطية في مختلف أنحاء العالم، يبدو أن العديد من النظم الديمقراطية تتراجع. ويظهر أن بعض الحكومات تعتمد إضعاف إجراءات عمليات تحقق مستقلة بشأن سلطاتها، والقضاء على أي نقد، وتفكيك الرقابة الديمقراطية وضمان حكمها لمدة طويلة، مع أثر سلبي على حقوق الشعب.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	١ - فهم ومعرفة وأدراك حقوقه التي أقرها الله له وللبشر جميعاً وبالتالي فهي هبة وليس مكسب من أحد ولا يحق لأي شخص انتزاعها. ٢ - يعبر الطالب بأسلوبه الخاص عن هذه الحقوق ويدافع عنها. ٣ - تحليل الظواهر واعطاء التفسيرات لما يحدث امامه من انتهاك لحقوق الإنسان وحرياته من خلال تحديد اوجه النقص او الثغرات الموجودة في ضوء المعلومات المتوفرة لديه

	٤- فهم اهم النظم السياسية والتي تعد ضمانه لحقوق الانسان وحرياته السياسية ومحاولة تطبيقه على ارض الواقع الا وهو النظام الديمقراطي.
Indicative Contents المحتويات الإرشادية	❖ حقوق الانسان في التاريخ المعاصر والحديث: الاعتراف الدولي بحقوق الانسان منذ الحرب العالمية الأولى وعصبة الامم المتحدة (٤ ساعات) ❖ حقوق الانسان، تعريفها، اهدافها وحقوق الانسان في الحضارات القديمة وخصوصا حضارة وادي الرافدين (٦ ساعات) ضمانات واحترام وحماية حقوق الانسان على الصعيد الدولي: - دور الأمم المتحدة ووكالاتها المتخصصة في توفير الضمانات - دور المنظمات الإقليمية (الجامعة العربية، الاتحاد الأوروبي، الاتحاد الأفريقي، منظمة الدول الأمريكية، منظمة آسيان) ❖ دور المنظمات الدولية الإقليمية غير الحكومية والرأي العام في احترام وحماية حقوق الانسان (١٢ ساعة) المشاكل والمعوقات ونقاشات الطلبة (٦) ❖ (ساعات)

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	٤-العصف الذهني ١-استراتيجية التفكير حسب قدرة الطالب ٢-استراتيجية مهارة التفكير العالية ٣-استراتيجية التفكير الناقد في التعلم
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	28	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	2
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	22	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	1.5
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	50		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	0	0	Continuous	All
	Projects / Lab.	0	0		
	Report	4	10% (10)		
Summative assessment	Midterm Exam	2 hr	20% (20)	7	LO # 1-7
	Final Exam	3 hr	60% (60)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المناهج الأسبوعي النظري

Week	Material Covered
Week 1	حقوق الإنسان، تعريفها، أهدافها <u>حقوق الإنسان في الحضارات القديمة وخصوصاً حضارة وادي الرافدين</u>
Week 2	<u>حقوق الإنسان في الشرائع السماوية مع التركيز على حقوق الإنسان في الإسلام</u>
Week 3	<u>حقوق الإنسان في التاريخ المعاصر والحديث : الاعتراف الدولي بحقوق الإنسان منذ الحرب العالمية الأولى وعصبة الأمم المتحدة</u>
Week 4	<u>الاعتراف الإقليمي بحقوق الإنسان : الاتفاقية الأوروبية لحقوق الإنسان 1950 ، الاتفاقية الأمريكية لحقوق الإنسان 1969 ، الميثاق الأفريقي لحقوق الإنسان 1981 ، الميثاق العربي لحقوق الإنسان 1994</u>
Week 5	<u>حقوق الإنسان في التاريخ المعاصر والحديث : الاعتراف الدولي بحقوق الإنسان منذ الحرب العالمية الأولى وعصبة الأمم المتحدة</u>
Week 6	<u>حقوق الإنسان في الدساتير العراقية بين النظرية والواقع</u>
Week 7	<u>حقوق الإنسان الاقتصادية والاجتماعية والثقافية وحقوق الإنسان المدنية والسياسية</u>
Week 8	<u>حقوق الإنسان الحديثة : الحقائق في التنمية ، الحق في البيئة النظيفة ، الحق في التضامن ، الحق في الدين</u>
Week 9	ضمانات احترام وحماية حقوق الإنسان على الصعيد الوطني ، الضمانات في الدستور والقوانين <u>الضمانات في الرقابة الدستورية ، الضمانات في حرية الصحافة والرأي العام ، دور المنظمات غير الحكومية في احترام وحماية حقوق الإنسان</u>
Week 10	ضمانات واحترام وحماية حقوق الإنسان على الصعيد الدولي : - دور الأمم المتحدة ووكالاتها المتخصصة في توفير الضمانات - دور المنظمات الإقليمية (الجامعة العربية ، الاتحاد الأوروبي ، الاتحاد الأفريقي ، منظمة الدول الأمريكية ، منظمة آسيان) <u>دور المنظمات الدولية والإقليمية غير الحكومية والرأي العام في احترام وحماية حقوق الإنسان</u>
Week 11	<u>مصطلح الديمقراطية ، نشأته ، دلالاته ، تاريخ الديمقراطية</u>
Week 12	<u>الإسلام والديمقراطية ومساوئ الحكم الاستبدادي</u>
Week 13	<u>الانتقادات الموجهة للديمقراطية ، ومحاسن النظام الديمقراطي</u>
Week 14	<u>الأنظمة الديمقراطية في العالم / الديمقراطية في العالم الثالث / المشاكل التي تواجه البلدان العربية في التحول الديمقراطي</u>
Week 15	الامتحان النهائي

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	حقوق الإنسان والديمقراطية – المفاهيم والمركبات للدكتور سماح مهدي العلياوي والدكتور سلمان كاظم البيهادي	Yes
Recommended Texts	الديمقراطية وحقوق الإنسان في الإسلام للدكتور راشد الغنوشي	No
Websites	https://www.neelwafurat.com https://studies.aljazeera.net	

APPENDIX:

APPENDIX

GRADING SCHEME				
مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Ministry of Higher Education and
Scientific Research - Iraq
Northern Technical University
Engineering Technical College/Mosul
Department of Computer Techniques Engineering



Module Descriptor Form
نموذج وصف المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	English Language	Module Delivery	
Module Type	SUPPLEMENT	✓ Theory ✓ Lecture Lab Tutorial Practical ✓ Seminar	
Module Code	NTU101		
ECTS Credits	2		
SWL (hr/sem)	50		
Module Level	UGX11 1	Semester of Delivery	2
Administering Department	Department of Computer Techniques Engineering	College	Northern Technical University Engineering Technical College/Mosul
Module Leader	dr. Younis Anas Younis	e-mail	younis.alrozz@ntu.edu.iq
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	PhD.
Module Tutor	None	e-mail	None
Peer Reviewer Name	None	e-mail	None
Review Committee Approval	21/06/2023	Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes, and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Aims أهداف المادة الدراسية	To enable the learner to communicate effectively and appropriately in real life situation. To use English effectively for study purposes across the curriculum. To develop and integrate the use of the four language skills i.e. Reading, Listening, Speaking, and Writing. To revise and reinforce structure already learned.

Module Learning Outcomes مخرجات التعلم للمادة الدراسية	Students will heighten their awareness of the correct usage of English grammar in writing and speaking. Students will improve their speaking ability in English both in terms of fluency and comprehensibility. Students will give oral presentations and receive feedback on their performance. Students will increase their reading speed and comprehension of academic articles. Students will improve their reading fluency skills through extensive reading. Students will enlarge their vocabulary by keeping a vocabulary journal.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: Part 1 – Grammar [4 hrs] Tenses, Present tenses: Present Simple, Present Continuous Part 2 – Vocabulary [6 hrs] Irregular verbs, making connections, Nouns, verbs, and adjectives, Making negatives. Part 3 – Speaking [6 hrs] Information gap, people's lifestyles, comparing cities. Part 4 – Listening [4 hrs] Telling stories, Town survey, attitudes to shopping, comparing cities. Revision [2 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage student's participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials, and interesting sampling activities for the students.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	22	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	1
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	28	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	1.12
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	50		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	3, 7, 11, 14	LO #1, 2, 10 and 11
	Assignments	0	0	0	0
	Projects / Lab.	0	0	0	0
	Report	4	10% (10)	5, 6, 8, 10, 13	LO # 5, 8 and 12
Summative assessment	Midterm Exam	2 hr	20% (20)	7	LO # 1-7
	Final Exam	3 hr	60% (60)	15	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week	Material Covered
Week 1	Grammar: Tenses, Questions, Questions words Vocabulary: Using a bilingual dictionary, Parts of speech, and Words with more than one meaning. Everyday English: Social expressions.
Week 2	Reading: the many ways we communicate Speaking: Information gap Listening: Neighbors
Week 3	Grammar: Present tenses: Present Simple, Present Continuous, have/ have got Vocabulary: Describing countries, Collocation Everyday English: Making conversation
Week 4	Reading: three people talk about their experiences Speaking: people's lifestyles Listening: what annoys you about the people in your life?
Week 5	Grammar: Past tenses: Past Simple, Past Continuous Vocabulary: Irregular verbs, making connections, Nouns, verbs, and adjectives, Making negatives. Everyday English: Time expressions
Week 6	Reading: Newspaper stories Speaking: Telling stories Listening: A radio drama
Week 7	Grammar: Quantity, Articles Vocabulary: Buying things Everyday English: Prices and shopping
Week 8	Reading: 'The best shopping street in the world' Speaking: Town survey, attitudes to shopping Listening: Buying things
Week 9	Grammar: Verb patterns 1, Future intentions Vocabulary: Hot verbs Everyday English: How do you feel?
Week 10	Reading: Hollywood kids Speaking: Being a teenager Listening: You've got a friend
Week 11	Grammar: Comparative and superlative adjectives Vocabulary: Synonyms and antonyms Everyday English: Directions
Week 12	Reading: 'A Tale of two millionaires' Speaking: comparing cities Listening: Living in another country
Week 13	Grammar: Present Perfect and Past Simple Vocabulary: Past participles, Adverbs, Word pairs Everyday English: Short answers
Week 14	Review
Week 15	Final Exam

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	
Week 2	

Week 3	
Week 4	
Week 5	
Week 6	
Week 7	
Week 8	
Week 9	
Week 10	
Week 11	
Week 12	
Week 13	
Week 14	

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	New Headway Intermediate Students Book	No
Recommended Texts		
Websites	You can visit the course page at the following link: https://youtube.com/playlist?list=PLzOug2pV17x9JD3wR8mk5qst_1EQ1myF6	

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	Mathematics		Module Delivery
Module Type	Basic		☒ Theory ☒ Lecture ☒ Lab ☒ Tutorial ☒ Practical ☒ Seminar
Module Code	TECCAI100		
ECTS Credits	5		
SWL (hr/sem)	125		
Module Level	UGx11 1	Semester of Delivery	1
Administering Department	Department of Computer Techniques Engineering		College
			Northern Technical University Engineering Technical College/Mosul
Module Leader	Ayhan A. khaleel		e-mail
			Ay_ahmed@ntu.edu.iq
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	M.Sc.
Module Tutor	None	e-mail	None
Peer Reviewer Name	None	e-mail	None
Review Committee Approval	21/06/2023	Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	Mathematics is an important tool for understanding modern and scientific technologies, and the modern world today relies heavily on mathematics. Mathematics is also characterized by multiple benefits, including that it is an intellectual tool, a strong communication method, and it is in itself a way of thinking, through which the capabilities of individuals develop, and it helps us in advanced logical thinking. It also helps the student to understand the laws and issues needed for the purpose of solving simple and complex electrical circuits.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	19. Learning about the basic Matrix and Determinants 20. Learning the Algebraic functions, natural logarithm, the exponential function, trigonometric functions, inverse trigonometric functions and hyperbolic functions. 21. Learning the Derivatives formula and chain rule. 22. Learning the Integration, Indefinite and Definite Integral

Indicative Contents المحتويات الإرشادية	23. Learning the Integration method Indicative content includes the following: Part A – the basic Matrix and Determinants Matrix, properties, and operations, Determinants and properties of determinants Inverse of square matrix by determinants, Solving linear System equations using the inverse of the coefficient matrix and Cramer's rule. [13 hrs] Part B – Algebraic functions Review of natural logarithm, the exponential function, trigonometric functions, inverse trigonometric functions and hyperbolic functions. [10 hrs] Part C – Derivatives of natural logarithm, the exponential function, trigonometric functions, inverse trigonometric functions and hyperbolic functions. Applications of differentiation. [20 hrs] Part D: Review of Integration, Indefinite and Definite Integral, Integration method and Applications of integration, approximation(trapezoidal rule, Simpson's rule) Area between curves [10 hrs] Revision problem classes [6 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in the delivery of this unit is to encourage students to participate in exercises, while improving and expanding their mathematical reasoning skills.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	59	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعيا	3
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	66	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعيا	4.4
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	8	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	13	10% (10)	Continuous	All
	Projects / Lab.	0	0		
	Report	0	0		
	Midterm Exam	2 hr	20% (20)	7	LO # 1-7

Summative assessment	Final Exam	3 hr	60% (60)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المناهج الاسبوعي النظري	
Week	Material Covered
Week 1	Matrix, properties, and operations
Week 2	Determinants and properties of determinants Inverse of square matrix by determinants
Week 3	Solving linear System equations using the inverse of the coefficient matrix and <u>Cramer's rule</u>
Week 4	Algebraic functions
Week 5	Review of natural logarithm, the exponential function, trigonometric functions
Week 6	inverse trigonometric functions and hyperbolic functions
Week 7	Derivatives formula and chain rule.
Week 8	Derivatives of natural logarithm, the exponential function, trigonometric functions
Week 9	inverse trigonometric functions and hyperbolic functions.
Week 10	Applications of differentiation.
Week 11	Review of Integration, Indefinite and Definite Integral
Week 12	Integration method
Week 13	Integration method
Week 14	Applications of integration, approximation(trapezoidal rule, Simpson's rule) <u>Area between curves</u>
Week 15	Final Exam

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	Advance Engineering Mathematics, Alan Jeffrey, 2002	Yes
Recommended Texts	Calculus I, Paul Dawkins, 2007	No
Websites	https://tutorial.math.lamar.edu/Classes/CalcII/CalcII.aspx	

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors

Fail Group (0 – 49)	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



MODULE DESCRIPTOR FORM

نموذج وصف المادة الدراسية

Module Information					
معلومات المادة الدراسية					
Module Title	<u>Digital Circuits</u>		Module Delivery		
Module Type	<u>Core</u>		✓ Theory ✓ Lecture ✓ Lab ✓ Tutorial ✓ Practical ✓ Seminar		
Module Code	<u>CTE104</u>				
ECTS Credits	<u>6</u>				
SWL (hr/sem)	<u>150</u>				
Module Level	UGx11 1	Semester of Delivery		2	
Administering Department	<u>Department of Computer Techniques Engineering</u>		College	<u>Northern Technical University</u> ENGINEERING TECHNICAL COLLEGE/MOSUL	
Module Leader	Khalis A. Mohammed		e-mail	Khalis_am@ntu.edu.iq	
Module Leader's Acad. Title		Lecturer	Module Leader's Qualification		M.Sc.
Module Tutor	None		e-mail	None	
Peer Reviewer Name		None	e-mail	None	
Review Committee Approval		21/06/2023	Version Number		1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	<u>Digital Logic</u>	Semester	1
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	1. To learn the basic techniques and methodologies for designing and analyzing digital circuits such as Adder – subtractor circuits. 2. To learn the Decoder and Encoder circuits. 3. To learn the Comparator, Multiplexer and Demultiplexer circuits. 4. To learn and analysis sequential circuits such as flip-flop circuits and Registers. 5. To learn the types of counters.

Module Learning Outcomes مخرجات التعلم للمادة الدراسية	24. Ability to design Adder and Subtractor circuits. 25. Knowledge of designing encoder and decoder circuits. 26. Knowledge the Comparator, Multiplexer, Demultiplexer and places of use. 27. Learn how to design an asynchronous and synchronous counters.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: <u>Part 1 – Functions of Combinational Logic.</u> Half, Full and Parallel Binary Adders and Subtractors. 1's and 2's Complement Subtractor, 2's Complement Adder-Subtractor, BCD Adder, etc. Comparators, Decoders, Encoders, Multiplexers, Demultiplexer [14 hrs] <u>Part 2- Latches, Flip-Flops, and Timers.</u> Latches, Edge-Triggered Flip-Flops. Flip-Flop operating (R-S, T, J-K, D) [12 hrs] <u>Part 3 Counters</u> Synchronous Counters, Asynchronous Counters. Design of Counters. [26 hrs] <u>Part 4 Shift Registers</u> Basic Shift Register Operations: SISO, SIPO, PISO, PIPO, Bidirectional and special Types Shift Register. [10 hrs] - Revision problem classes [6 hrs]

Learning and Teaching Strategies استراتيجيات التعلم والتعليم	
Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.

Student Workload (SWL) الحمل الدراسي للطالب			
Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	61	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	89	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	6
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	150		

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	3	5% (10)	4,6,10	LO # 2, 10 and 11
	Assignments	3	5% (10)	2, 5,8	LO # 3, 4 and 7
	Lab.(mid pract.)	2	10%(30)	Continuous	All
	Projects	3	5% (10)	3,9,11	LO # 5, 8 and 10

Summative assessment	Report	6	10% (10)	Continuous	All
	Seminars	3	5% (10)	4,10,12	LO # 5, 8 and 10
	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	15	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	1- Functions of Combinational Logic. Basic Adders; Half and Full Adders.
Week 2	1- Functions of Combinational Logic. Basic Subtractors; Half and Full Subtractors.
Week 3	1- Functions of Combinational Logic. Parallel Binary Adders and Subtractors. 1's,2's Complement Subtractor, 2's Complement Adder-Subtractor, BCD Adder, etc.
Week 4	1- Functions of Combinational Logic. Comparators, Code converters.
Week 5	1- Functions of Combinational Logic. Decoders, Encoders.
Week 6	1- Functions of Combinational Logic. Multiplexers (Data Selectors), Demultiplexer.
Week 7	2- Latches, Flip-Flops, and Timers. Latches
Week 8	2- Latches, Flip-Flops, and Timers. Edge-Triggered Flip-Flops.
Week 9	2- Latches, Flip-Flops, and Timers. Flip-Flop operating (R-S, T, J-K ,D)
Week 10	3- Counters Synchronous Counters.
Week 11	3- Counters Asynchronous Counters.
Week 12	3- Counters Design of Counters.
Week 13	4- Shift Registers Basic Shift Register Operations. Serial In/Serial out Shift Registers. Serial In/Parallel out Shift Registers.

Week 14	4- Shift Registers Parallel In/Serial Out Shift Registers. Parallel In/parallel Out Shift Registers. Bidirectional Shift Registers.
Week 15	Final Exam

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	Lab 1: Half Binary Adder
Week 2	Lab 2: Full Binary Adder
Week 3	Lab 3: Half Binary Subtractor
Week 4	Lab 4: Full Binary Subtractor
Week 5	Lab 5: 2's Complement Adder-Subtractor
Week 6	Lab 6: Binary Comparator
Week 7	Lab 7: Digital Multiplexer
Week 8	Lab 8: DeMultiplexer.
Week 9	Lab 9: Decoders
Week 10	Lab 10: Encoders
Week 11	Lab 11: D Flip-Flop
Week 12	Lab 12: JK- Flip-Flop
Week 13	Lab 13: T- Flip-Flop
Week 14	Lab 14: Review

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	Thomas L. Floyd, Digital Fundamentals, 11th Edition, Pearson Education 2015	Yes
Recommended Texts	M. Morris Mano, Michael D. Ciletti, Digital Design, 5th edition, Pearson Education 2013	No
Websites	Digital Systems: From Logic Gates to Processors: https://www.coursera.org/learn/digital-systems	

APPENDIX:

GRADING SCHEME

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded

(0 – 49)	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	Engineering Mathematics		Module Delivery
Module Type	Basic		☒ Theory ☒ Lecture ☒ Lab ☒ Tutorial ☒ Practical ☒ Seminar
Module Code	CTE105		
ECTS Credits	5		
SWL (hr/sem)	125		
Module Level	UGx11 1	Semester of Delivery	2
Administering Department	Department of Computer Techniques Engineering		College
			Northern Technical University Engineering Technical College/Mosul
Module Leader	Ayhan A. khaleel		e-mail
			Ay_ahmed@ntu.edu.iq
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	M.Sc.
Module Tutor	None	e-mail	None
Peer Reviewer Name	None	e-mail	None
Review Committee Approval	21/06/2023	Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	Mathematics is an important tool for understanding modern and scientific technologies, and the modern world today relies heavily on mathematics. Mathematics is also characterized by multiple benefits, including that it is an intellectual tool, a strong communication method, and it is in itself a way of thinking, through which the capabilities of individuals develop, and it helps us in advanced logical thinking. It also Introduce students to mathematics through the laws and issues necessary for the purpose of assisting them in their studies in their field of specialization .
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	28. Learning about the complex numbers. 29. Learning the Functions of several variables. 30. Learning the Lines and planes in space, Tangent and normal in the plane 31. Learning the Triple integrals in rectangular coordinates 32. Double Integral in rectangular and polar form, Areas and volumes 33. Applications (Surface Area, Green's theorem and Stokes' theorem)

Indicative Contents المحتويات الإرشادية	Indicative content includes the following: ❖ <u>Complex Numbers</u>– For most students the assumptions I’ve made above about their exposure to complex numbers is the extent of their exposure. Problems tend to arise however because most instructors seem to assume that either students will see beyond this exposure in some later class or have already seen beyond this in some earlier class. Students are then suddenly expected to know more than basic arithmetic of complex numbers but often haven’t actually seen it anywhere and have to quickly pick it up on their own in order to survive in the class. [13 hrs] ❖ <u>Vector Fields</u> – In this section we introduce the concept of a vector field and give several examples of graphing them. We also revisit the gradient that we first saw a few chapters ago. Line Integrals – Part I – In this section we will start off with a quick review of parameterizing curves. This is a skill that will be required in a great many of the line integrals we evaluate and so needs to be understood. We will then formally define the first kind of line integral we will be looking at : line integrals with respect to arc length. Line Integrals – Part II – In this section we will continue looking at line integrals and define the second kind of line integral we’ll be looking at : line integrals with respect to x, y, and/or z. We also introduce an alternate form of notation for this kind of line integral that will be useful on occasion. Line Integrals of Vector Fields – In this section we will define the third type of line integrals we’ll be looking at : line integrals of vector fields. We will also see that this particular kind of line integral is related to special cases of the line integrals with respect to x, y and z. [20 hrs] ❖ <u>Part D: Multiple Integrals</u> - In this chapter will be looking at double integrals, i.e. integrating functions of two variables in which the independent variables are from two dimensional regions, and triple integrals, i.e. integrating functions of three variables in which the independent variables are from three dimensional regions. Included will be double integrals in polar coordinates and triple integrals in cylindrical and spherical coordinates and more generally change in variables in double and triple integrals.[20 hrs] ❖ Revision problem classes [6 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in the delivery of this unit is to encourage students to participate in exercises, while improving and expanding their mathematical reasoning skills.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	61	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	64	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4.2
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	20% (25)	5, 7, 9, 11	LO #1, 2, 10 and 11
	Assignments	8	16% (10)	Continuous	All
	Projects / Lab.	0	0		
	Report	1	4%(20)		
Summative assessment	Midterm Exam	2 hr	10% (20)	7	LO # 1-7
	Final Exam	3 hr	50% (60)	15	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	COMPLEX NUMBERS IN CARTESIAN COORDINATES AND POLAR FROM
Week 2	LINEAR ALGEBRA FOR COMPLEX NUMBER IN POLAR AND CARTESIAN EULER'S FORMULA.
Week 3	DEMOIVRE'S THEOREM TO FIND POWERS AND THE NTH ROOTS OF GIVEN COMPLEX NUMBERS
Week 4	Functions of several variables
Week 5	Partial differentiation and the chain rule
Week 6	Functions of a complex variable, Cauchy-Riemann equations
Week 7	Cartesian coordinates and vectors in space, Dot product and Cross product
Week 8	Lines and planes in space, Tangent and normal in the plane
Week 9	The two-dimensional Coordinate system, The three dimensional Coordinate .
Week 10	Directional derivatives, Gradient vectors
Week 11	Divergence, curl and the laplacian
Week 12	Double Integral in rectangular and polar form, Areas and volumes
Week 13	Triple integrals in rectangular coordinates
Week 14	Applications (Surface Area, Green's theorem and Stokes' theorem)
Week 15	Final Exam

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	Advance Engineering Mathematics, Alan Jeffrey, 2002	Yes
Recommended Texts	Calculus II & Calculus III, Paul Dawkins, 2007	No
Websites	https://tutorial.math.lamar.edu/Classes/CalcIII/CalcIII.aspx https://tutorial.math.lamar.edu/Classes/CalcII/CalcII.aspx	

APPENDIX:

GRADING SCHEME				
مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



MODULE DESCRIPTOR FORM

نموذج وصف المادة الدراسية

Module Information					
معلومات المادة الدراسية					
Module Title	<u>electrical Circuits</u>		Module Delivery		
Module Type	<u>Core</u>		✓ Theory ✓ Lecture ✓ Lab ✓ Tutorial ✓ Practical ✓ Seminar		
Module Code	<u>CTE106</u>				
ECTS Credits	<u>6</u>				
SWL (hr/sem)	<u>150</u>				
Module Level	UGx11 1			Semester of Delivery 2	
Administering Department	<u>Department of Computer Techniques Engineering</u>		College	<u>Northern Technical University</u> ENGINEERING TECHNICAL COLLEGE/MOSUL	
Module Leader	Maysaloon Abed Qasim		e-mail	Mayslooon.alhashim@ntu.edu.iq	
Module Leader's Acad. Title		Lecturer	Module Leader's Qualification		PHD
Module Tutor	None		e-mail	None	
Peer Reviewer Name		None	e-mail	None	
Review Committee Approval		21/06/2023	Version Number		1.0

Relation With Other Modules العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	<u>electrical Circuits</u>	Semester	2
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	1-Understand the fundamental concepts and principles of alternating current (AC) circuits. 2-Gain knowledge of the mathematical tools and techniques used to analyze AC circuits, including phasors, complex numbers, and impedance. 3-Develop the ability to solve AC circuit problems using circuit analysis techniques such as mesh analysis, nodal analysis, and Thevenin's theorem.. ect. 4-Learn how to calculate and analyze voltage and current phasors in AC circuits, including their amplitudes, phases, and frequency relationships.

	5-Explore the behavior and characteristics of AC circuit elements, such as resistors, capacitors, and inductors, and understand their roles in AC circuit analysis. 6-Investigate the concept of impedance in AC circuits and its relationship to resistance, reactance, and frequency. 7-Study the principles of AC power and power factor, including real power, reactive power, apparent power, and power factor correction. 8- Gain a comprehensive understanding of three-phase AC systems, including the generation, transmission, and distribution of power in three-phase circuits.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	1-Knowledge Acquisition: Students will acquire a comprehensive understanding of the fundamental concepts and principles of alternating current (AC) circuits. 2-Circuit Design and Analysis: Students will gain the ability to design and analyze AC circuits, applying their knowledge of impedance, power factor, and component characteristics. They will learn to calculate voltage and current magnitudes, phase differences, and power relationships in AC circuits. 3-Phasor Diagram Interpretation: Students will be able to construct and interpret phasor diagrams to visualize and analyze the behavior of voltages and currents in AC circuits. 4-Three-Phase Systems: Students will acquire understanding of three-phase AC systems, including balanced and unbalanced configurations. Laboratory Skills: Students will develop practical skills in using circuit simulation software and laboratory equipment to design, analyze, and verify the performance of AC circuits.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: <u>Part A – Inductance & Capacitance in Electric circuits.</u> <u>General concept of capacitance (charge and voltage, capacitors in series and parallel)</u> General concept of inductance (inductive and non-inductive circuits, capacitors in series and parallel) [4 hrs] <u>Part B Alternating Quantities.</u> Ac systems, waveforms, terms and definitions. Average and R.M.S values of current and voltage. [10 hrs] <u>Part C Single - phase of AC Circuits.</u> AC in resistive circuits, current and voltage in inductive circuits, current and voltage in capacitive circuits. Concept of complex impedance and admittance, AC series and parallel circuits. RL, RC and RLC circuit analysis and phasor representation. [12 hrs] <u>Part D Power in AC circuits.</u> Power in resistive circuits. power in inductive and capacitive circuits ,power in circuit with resistance and reactance. Power factor, its practical importance, improvement of power factor, measurement of power in a single – phase AC circuits. [16 hrs] <u>Part E Three – phase circuit analysis.</u> Basic concept and advantages of three – phase circuit. Phasor representation of star and delta connection. Phase and line quantities. Voltage and current computation in 3-phase balance and unbalance circuits. Real and Reactive power computation, measurement of power and power factor in 3-phase system. [12 hrs] - Revision problem classes [4 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	1-Conceptual Understanding: Explain the differences between AC and DC circuits, introduce the concept of impedance, reactance, and phasors, and highlight the significance of frequency and phase in AC circuits.
	2-Mathematical Foundations: Provide a solid mathematical foundation for AC circuits. Teach students the use of complex numbers and phasor notation to analyze AC circuits.
	3-Problem-Solving Skills: Dedicate adequate time to problem-solving exercises and examples.
	4-Laboratory Experiments: Incorporate laboratory experiments to reinforce theoretical concepts. Allow students to build and analyze AC circuits using oscilloscopes, function generators, and AC power sources.
	5-Simulation Tools: Introduce simulation software tools that allow students to simulate AC circuits and observe their behavior.
	6-Review and Assessment: Regularly review key concepts and provide formative assessments to gauge students' understanding. Offer constructive feedback on their performance to help them identify areas for improvement.

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	61	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	89	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	6
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	150		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	3	5% (10)	5,7,10	LO #1, 2, 10 and 11
	Assignments	3	5% (10)	2,9,12	LO # 3, 4, 6 and 7
	Lab.(mid pract.)	2	10%(30)	Continuous	All
	Projects	3	5% (10)	2,9,12	LO # 3, 4, 6 and 7
	Report	6	10% (10)	Continuous	All
	Seminars	3	5% (10)	3,10,13	LO # 3, 4, 6 and 7
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الأسبوعي النظري

Week	Material Covered
Week 1	<u>1- Inductance & Capacitance in Electric circuits.</u> <u>1-General concept of capacitance (charge and voltage, capacitors in series and parallel)</u>

	<u>2- General concept of inductance (inductive and non-inductive circuits, capacitors in series and parallel)</u>
Week 2	<u>2- Alternating Quantities.</u> <u>Ac systems, waveforms, terms and definitions.</u>
Week 3	<u>2- Alternating Quantities.</u> <u>Average and R.M.S values of current and voltage.</u>
Week 4	<u>2- Alternating Quantities.</u> <u>Phasor diagram</u>
Week 5	<u>3- Single - phase of AC Circuits.</u> <u>AC in resistive circuits , current and voltage in inductive circuits, current and voltage in capacitive circuits.</u>
Week 6	<u>3- Single - phase of AC Circuits.</u> <u>Concept of complex impedance and admittance , AC series and parallel circuits .</u>
Week 7	<u>3- Single - phase of AC Circuits.</u> <u>RL , RC and RLC circuit analysis and phasor representation.</u>
Week 8	<u>4- Power in AC circuits.</u> <u>Power in resistive circuits ,power in inductive and capacitive circuits ,power in circuit with resistance and reactance.</u>
Week 9	<u>4- Power in AC circuits.</u> <u>Power factor ,its practical importance , improvement of power factor , measurement of power in a single – phase AC circuits.</u>
Week 10	<u>5- Three – phase circuit analysis.</u> <u>Basic concept and advantages of three – phase circuit.</u>
Week 11	<u>5- Three – phase circuit analysis.</u> <u>Phasor representation of star and delta connection.</u>
Week 12	<u>5- Three – phase circuit analysis.</u> <u>Phase and line quantities.</u>
Week 13	<u>5- Three – phase circuit analysis.</u> <u>Voltage and current computation in 3-phase balance and unbalance circuits.</u>
Week 14	<u>5- Three – phase circuit analysis.</u> <u>Real and Reactive power computation , measurement of power and power factor in 3-phase system.</u>
Week 15	<u>Final Exam.</u>

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
------	------------------

Week 1	<u>Lab 1: Measurement amplitude, frequency and time with oscilloscope using hardware and digital simulation.</u>
Week 2	<u>Lab 2: Examine phase relation in RL & RC circuit using hardware and digital simulation.</u>
Week 3	<u>Lab 3: Calculate & verify average and RMS value,</u>
Week 4	<u>Lab 4: Impedance of series RL and RC circuit using digital simulation..</u>
Week 5	<u>Lab 5: Impedance of series RLC circuit using digital simulation..</u>
Week 6	<u>Lab 6: Determination of average value, RMS value, form factor, peak factor of sinusoidal wave using digital simulation.</u>
Week 7	<u>Lab 7: Measure currents and voltages in three-phase balanced AC circuits</u>
Week 8	<u>Lab 8: Prove Y-Δ transformation,</u>
Week 9	<u>Lab 9: Exercise on phasor diagrams for three-phase circuits</u>
Week 10	<u>Lab 10: Measurement of voltage, current & power in a three-phase circuit</u>
Week 11	<u>Lab 11: Ohm's LAW, KVL AND KCL in AC circuits using digital simulation..</u>
Week 12	<u>Lab 12: Determination of mesh currents in AC circuits using digital simulation.</u>
Week 13	<u>Lab 13: Measurement of nodal voltages in AC circuits using digital simulation.</u>
Week 14	<u>Lab 14: Review</u>

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	Charles K. Alexander, Matthew N.O. Sdiku Fundamentals of Electrical Engineering, 4th Edition, 2009	Yes
Recommended Texts	Tony R. Kuphaldt, Lessons In Electric Circuits, Volume II - AC 5th edition, 2002	No
Websites	AC circuits https://byjus.com/physics/ac-circuit/	

APPENDIX:

GRADING SCHEME

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX - Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F - Fail	راسب	(0-44)	Considerable amount of work required
Note:				

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



MODULE DESCRIPTOR FORM

نموذج وصف المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	Electronic Workshop		Module Delivery
Module Type	Core		Theory Lecture ✓ Lab ✓ Tutorial ✓ Practical Seminar
Module Code	CTE107		
ECTS Credits	3		
SWL (hr/sem)	75		
Module Level	UGx11 1	Semester of Delivery	2
Administering Department	DEPARTMENT OF COMPUTER TECHNIQUES ENGINEERING	College	Northern Technical University Engineering Technical College/Mosul
Module Leader	Thabat F. Thabet	e-mail	Thabet.tfy@ntu.edu.iq
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	PhD.
Module Tutor	None	e-mail	None
Peer Reviewer Name	None	e-mail	None
Review Committee Approval	10/06/2023	Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Aims أهداف المادة الدراسية	6. To learn the basics of electrical elements (Symbols and Abbreviations, Units). 7. To learn how to use measurement devices for DC and AC 8. How to measure electrical elements by using measurement devices 9. To learn the basics of electronic devices 10. How to test electronic devices by using measurement devices 11. How to use Oscilloscope (CRO) 12. How to use Function Generator
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	34. Learning about the electrical elements. 35. Learning about the electronic devices. 36. Learning about the measurement devices. 37. Learning about Oscilloscope and Function Generator 38. Learning about the integrated circuits.

	39. Learning about the printed circuit board
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: Part A – Basic information and electrical elements [12 hrs] - Basic information - Color of resistance - Capacitors values - Measurement devices - How to measure resistors and capacitors values - How to measure DC and AC values Part B – Electronic devices and AC circuits [10 hrs] - Diodes - Transistors. - Operating of Oscilloscope - Function Generator - DC and AC circuit Part C – Circuit Implementation [6 hrs] - Electric circuit schematic diagram - Integrated circuits - Printed circuit board - Review [2 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.
------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	30	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	2
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	45	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	3
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	75		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2/6	30% (30)	2, 4, 5, 7, 8 and 10	LO # 1, 2, 3 and 4
	Assignments	1/4	10% (20)	3, 11, 12 and 13	LO # 1, 2, 4, 5 and 6
	Projects	5/1	10% (10)	14	all
	Report	3/4	20% (20)	4, 5, 6 and 9	LO # 1, 2, 3 and 4
	LAB.	10/4	20% (20)	3-12	all
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Lab. Syllabus)

المناهج الاسبوعي للمختبر	
Week	Material Covered
Week 1	Lab 1: Basic information
Week 2	Lab 2: Color of resistance
Week 3	Lab 3: Capacitors values
Week 4	Lab 4: Measurement devices
Week 5	Lab 5: How to measure resistors and capacitors values
Week 6	Lab 6: How to measure DC and AC values
Week 7	Lab 7: Diodes
Week 8	Lab 8: Transistors.
Week 9	Lab 9: Operating of Oscilloscope
Week 10	Lab 10: Function Generator
Week 11	Lab 11: DC and AC circuit
Week 12	Lab 12: Electric circuit schematic diagram
Week 13	Lab 13: Integrated circuits
Week 14	Lab 14: Printed circuit board
Week 15	Lab 15: Review

Learning and Teaching Resources		
مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	ELECTRONIC WORKSHOP & PCB LAB MANUAL	Yes
Recommended Texts	Integrated Circuits	Yes
Websites	https://www.youtube.com/watch?v=YJr-kHy6STg https://www.youtube.com/watch?v=VxMV6wGS3NY	

APPENDIX:

GRADING SCHEME				
مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Computer Programming		Module Delivery	
Module Type	Core		☒ Theory ☒ Lecture ☒ Lab ☒ Tutorial ☒ Practical ☒ Seminar	
Module Code	CTE108			
ECTS Credits	4			
SWL (hr/sem)	100			
Module Level	UGx11 1	Semester of Delivery		1
Administering Department	Department of Computer Techniques Engineering		College	Northern Technical University Engineering Technical College/Mosul
Module Leader	Najwan Z. Waisi		e-mail	Najwan.tuhafi@ntu.edu.iq
Module Leader's Acad. Title	Lecturer		Module Leader's Qualification	M.Sc.
Module Tutor	None		e-mail	None
Peer Reviewer Name	None		e-mail	None
Review Committee Approval	21/06/2023		Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	Object Oriented Programming	Semester	S3

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	Introduce the students with computer programming techniques using C++ language, and how it can be used to solve problems related to their specialization.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	The learning outcomes for a module on computer programming in C++ can vary depending on the specific objectives of the course or program: 1-Understanding the basics of C++: Students should be able to comprehend the fundamental concepts of C++ programming, including syntax, data types, variables, operators, control structures, and functions. 2-Proficiency in C++ programming: Students should develop the skills required to write, compile, and execute C++ programs. They should be able to implement various programming constructs and algorithms using C++.

	3-Problem-solving and algorithm design: Students should gain the ability to analyze problems and design efficient algorithms to solve them using C++. They should be able to break down complex problems into smaller, manageable tasks and implement them in code. 4-Debugging and error handling: Students should develop skills in debugging C++ programs and identifying and fixing errors. They should learn techniques for error handling, exception handling, and writing robust code. 5-Code optimization and efficiency: Students should be able to optimize their C++ code for efficiency, considering factors such as algorithm complexity, data structures, and code organization. They should learn about performance analysis and profiling tools to identify bottlenecks in code. 6-Software development practices: Students should understand and apply good software development practices, including code documentation, version control, and testing. They should learn how to write readable and maintainable code.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: • <u>Part A – Introduction to C++.</u> [14 hrs] • <u>Part B- Operators & Making Decisions</u> [12 hrs] • <u>Part C- Looping & Arrays</u> [16 hrs] • <u>Part D- Looping & Arrays</u> [10 hrs] • Revision problem classes [6 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	When teaching and learning C++ programming, various strategies can be employed to enhance comprehension and mastery of the subject. Here are some effective learning and teaching strategies for C++ programming: Hands-on coding , Step-by-step approach , Visual aids and diagrams , Active learning , Real-world examples and projects , Online resources and coding platforms , Code documentation and commenting , Debugging and problem-solving techniques , Assessment and feedback , Continuous learning and staying updated
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	59	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4.2
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	42	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	3
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	100		

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	6	10% (10)	2, 12	LO # 3, 4, 6 and 7
	Projects / Lab.	1	10% (10)	Continuous	All
	Report	5	10% (10)	13	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	Introduction to C++ (Structure of a program)
Week 2	Variables, Data Types, Declaration of variables, Scope of variables, Initialization of variables, Expressions and Basic Input/Output.
Week 3	Operators (Assignment, Arithmetic operators, Compound assignment, Increase and decrease, Relational and equality operators, Conditional operator)
Week 4	Making Decisions (if...else and switch).
Week 5	Looping (while loop and for loop).
Week 6	Bitwise Operators and Explicit type casting operator
Week 7	Arrays (Single Dimensional arrays, Arrays as parameters)
Week 8	Arrays (two Dimensional arrays, Arrays as parameters)
Week 9	Character Sequences and String handling.
Week 10	Structure
Week 11	Pointers (Reference operator, dereference operator, Declaring variables of pointer types.)
Week 12	Pointers and arrays, Pointers to pointers, void pointers and Pointers to functions
Week 13	Functions (Local and global variables, Arguments passed by value and by reference, Default values in parameters)
Week 14	Overloaded functions and Recursive functions.
Week 15	Final Exam

Delivery Plan (Weekly Lab. Syllabus) المنهاج الاسبوعي للمختبر	
Week	Material Covered
Week 1	Lab 1: Introduction to C++ program using visual studio .
Week 2	Lab 2: my first program and how solve a problem.
Week 3	Lab 3: : if...else and switch programs
Week 4	Lab 4: while loop and for loop programs
Week 5	Lab 5: Bitwise Operators programs

Week 6	Lab 6: Single Dimensional arrays
Week 7	Lab 7: two Dimensional arrays ..part1
Week 8	Lab 8: two Dimensional arrays..part2
Week 9	Lab 9: : Character and String programs
Week 10	Lab 10: how implement a Structure
Week 11	Lab 11: Pointers and arrays
Week 12	Lab 12: Functions..part1
Week 13	Lab 13: Functions..part2
Week 14	Lab 14: Review

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	The Complete Reference, 4th Edition – Herbert schildt	No
Recommended Texts	complete c++ programming fundamentals with examples projects- emenwa global	No
Websites	non	

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 – 100	Outstanding Performance
	B - Very Good	جيد جدا	80 – 89	Above average with some errors
	C - Good	جيد	70 – 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 – 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 – 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				

MODULE DESCRIPTION FORM

نموذج وصف المادة الدراسية

Module Information

معلومات المادة الدراسية

Module Title	COMPUTER PRINCIPLES		Module Delivery	
Module Type	<u>SUPPORTED</u>		☒ Theory ☐ Lecture ☒ Lab ☐ Tutorial ☒ Practical ☒ Seminar	
Module Code	<u>TECCAI01</u>			
ECTS Credits	<u>3</u>			
SWL (hr/sem)	<u>75</u>			
Module Level	First	Semester of Delivery	First	
Administering Department	Med. Ins. Tech. Eng.	College	Technical Engineering College	
Module Leader	Zaid Abdulsattar Abdulrazzaq	e-mail	zaid.a.abdulrazzaq@ntu.edu.iq	
Module Leader's Acad. Title	Assistant Lecturer	Module Leader's Qualification	Master	
Module Tutor	Name (if available)	e-mail	E-mail	
Peer Reviewer Name		e-mail		
Scientific Committee Approval Date	01/06/2023	Version Number	1.0	

Relation with other Modules

العلاقة مع المواد الدراسية الأخرى

Prerequisite module	N/A	Semester	2
Co-requisites module	N/A	Semester	2

Module Aims, Learning Outcomes and Indicative Contents

أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

Module Objectives أهداف المادة الدراسية	1. Studying computer principles. 2. Defining keyboards and mice. 3. Presenting principles of memories. 4. Explaining disc drives. 5. Explaining principles of windows. 6. Illustrating accessories of windows.
---	---

Module Learning Outcomes مخرجات التعلم للمادة الدراسية	1. Abilities to recognize different computer hardware parts. 2. Defining various types of keyboards and mice. 3. Getting knowledge about computer memories and drives. 4. Getting knowledge about windows. 5. Presenting different windows accessories.
Indicative Contents المحتويات الإرشادية	• Indicative content includes the following. • Computer types of: digital, analogues and hybrid. • Different memory types of: RAM, ROM, PROM, EPROM and EEPROM. • Different drives types of: magnetic and optical. • Windows facilities of: Notepad, Wordpad, Paint, Accessories and others.

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	Strategies that will be adopted for delivering this module are theoretical lectures, practical experiments, home works and exams. This will be achieved through classes, interactive tutorials and by considering practical experiments.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطلاب محسوب لـ ١٥ اسبوعا

Structured SWL (h/sem) الحمل الدراسي المنتظم للطلاب خلال الفصل	43	Structured SWL (h/w) الحمل الدراسي المنتظم للطلاب أسبوعيا	3
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطلاب خلال الفصل	29	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطلاب أسبوعيا	2
Total SWL (h/sem) الحمل الدراسي الكلي للطلاب خلال الفصل	75		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (20)	4,7	LO #2, #4
	Assignments	2	10% (20)	10	LO #1, #3, #5
	Projects / Lab.	1	10% (40)	Continuous	All
	Report	8	10% (5)	9	LO #3
Summative assessment	Midterm Exam	2hr	10% (10)	10	All
	Final Exam	3hr	50% (50)	15	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week	Material Covered
1 st	Introducing to the Computer System Including: What is Computer? Computer System, Functions of Computer Input Storage Process & Output, Classification of Computers and Computer Units
2 nd , 3 rd , 4 th	Explaining Types of Computer Keyboards and Types of Keyboard Keys
5 th	Explaining Types of Computer Mice and Mouse Functions
6 th	Explaining Different Plugs and Ports for Some Computer Parts
7 th	Illustrating Computer Discs and Drives
8 th	Illustrating RAM, Non-Volatile and Cache Memories
9 th , 10 th , 11 th	Demonstrating Computer Hardware Parts and Definitions
12 th , 13 th	Presenting Windows, Windows Desktop and Windows Taskbar
14 th , 15 th	Illustrating Start Menu and Windows Accessories

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
1 st	Introducing to the Computer System Including: What is Computer?, Computer System, Functions of Computer Input Storage Process & Output, Classification of Computers and Computer Units
2 nd , 3 rd , 4 th	Explaining Types of Computer Keyboards and Types of Keyboard Keys
5 th	Explaining Types of Computer Mice and Mouse Functions
6 th , 7 th	Explaining Different Plugs and Ports for Some Computer Parts, and Illustrating Computer Discs and Drives
8 th	Illustrating RAM, Non-Volatile and Cache Memories
9 th , 10 th , 11 th , 12 th	Demonstrating Computer Hardware Parts and Definitions, and Presenting Windows, Windows Desktop and Windows Taskbar
13 th , 14 th , 15 th	Illustrating Start Menu and Windows Accessories

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	[1] Umar Farooq, "What is Computer - Definition & Basic Concept of Computer", Study Lecture Notes, 2016. [2] University Information Technology Services, "Microsoft Windows 10, Getting Started Guide", Kennesaw State University – UITS, 2016.	In the internet
Recommended Texts	Cre8te Opportunities, "Introduction to Computers (Windows 10)", Digital Skills Academy, 2016.	In the internet
Websites	[1] http://www.studylecturenotes.com/computer-science/what-is-computer-definition-basic-concept-of-computer [2] http://ergonomictrends.com/different-types-of-computer-keyboards/ [3] UKEssays, "Wireless Mouse: History and Types", 2018. [Online]. Available: https://www.ukessays.com/essays/computer-science/wireless-mouse-history-types-5302.php?vref=1 . [4] https://searchstorage.techtarget.com/definition/RAM-random-access-memory [5] https://tldp.org/HOWTO/Network-boot-HOWTO/a610.html#:~:text=PROM%3A%20Pronounced%20prom%2C%20an%20acronym,the%20computer%20is%20turned%20off	

Grading Scheme

مخطط الدرجات

Group	Grade	التقدير	Marks %	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D – Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note: Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information معلومات المادة الدراسية			
Module Title	<u>Arabic Language</u>	Module Delivery	
Module Type	<u>Supplement</u>	✓ Theory ✓ Lecture Lab Tutorial Practical ✓ Seminar	
Module Code	<u>NTU103</u>		
ECTS Credits	<u>2</u>		
SWL (hr/sem)	<u>50</u>		
Module Level	<u>٢</u>	Semester of Delivery	2
Administering Department	<u>Department of Computer Techniques Engineering</u>	College	<u>Northern Technical University Engineering Technical College/Mosul</u>
Module Leader	Dr. Eesha I. Mohammed	e-mail	aysha.ibrahim@ntu.edu.iq
Module Leader's Acad. Title	Assist. Prof.	Module Leader's Qualification	PHD
Module Tutor	None	e-mail	None
Peer Reviewer Name	None	e-mail	None
Review Committee Approval	21/06/2023	Version Number	1.0

Relation With Other Modules العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	ينشأ الطالب على حب اللغة العربية لغة القرآن الكريم. التعرف على مواطن الجمال في اللغة العربية وأدائها، وأن يكتسب الطالب القدرة على دراسة فروع اللغة العربية. تعريف الطالب بالفاظ اللغة العربية الصحيحة وتراكيبها وأساليبها السليمة بطريقة مشوقة وجذابة. أن يستغل الطالب وقت فراغه بالقراءة والاطلاع والرجوع إلى المكتبة. تمكين الطالب من القراءة الصحيحة، وأن يكتسب القدرة على استعمال اللغة استعمالاً صحيحاً في الاتصال مع الآخرين؛ كالسرعة وجودة الإلقاء وحسن التعبير، وتعويد حسن الاستماع لما يسمع مما يبسر له أموره ويعينه على قضاء حوائجه. تنمية الذوق الأدبي لدى الطالب حتى يدرك النواحي الجمالية في أساليب الكلام ومعانيه وصوره. تعويد الطالب التعبيرات السليمة الواضحة عن أفكاره وما يقع تحت حواسه نطقاً وكتابة وحسن استخدام علامات الترقيم. تنمية قدرة ومهارة الطالب الإملائية والخطية بحيث يستطيع الكتابة الصحيحة من جميع النواحي. إيقاظ وعي الطالب لإدراك شرف الكلمة وتوجيهه؛ للمحافظة على طهارتها ونقاها حتى لا تستعمل إلا في الخير. مساعدة الطالب على فهم التراكيب المعقدة والأساليب الغامضة.
Module Learning Outcomes	١. معرفة القواعد النحوية والصرفية. ٢. التعريف بأبرز المصنفات اللغوية والأدبية. ٣. تحديد المشكلات اللغوية والأدبية لدى الدارسين. ٤. القراءة المعاصرة للنصوص اللغوية والأدبية.

مخرجات التعلم للمادة الدراسية	٥- قراءة النصوص الأدبية وكتابتها وفق المعايير النحوية والصرفية ٦- تعزيز الثقة بالنفس والجرأة والفصاحة ٧- المنافسة والتميز في سوق العمل.
Indicative Contents المحتويات الإرشادية	مقدمة عن الأخطاء اللغوية التاء المربوطة والتاء المفتوحة (٤ ساعات) تطبيقات الأخطاء اللغوية الشائعة وأقسام الكلام (٦ ساعات) همزة الوصل والقطع والهمزة المتوسطة والمتطرفة قواعد كتابة الالف الممدودة والمقصورة الحروف الشمسية والقمرية والضاد والظاء (١٢ ساعة) المشاكل والمعوقات ونقاشات (٦ ساعات)

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	١- تبسيط المعلومات وتنظيمها. ٢- تسهيل عملية استرجاع المعلومات. ٣- ربط المفاهيم الجديدة بالمكتسبات السابقة. ٤- إيجاد العلاقة بين المفاهيم. ٥ - تسهيل تذكر المعارف والمعلومات
------------	---

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	31	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	2
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	19	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	1
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	50		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	1	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	10	10% (10)	Continuous	All
	Seminar	1	10% (10)	8	#10
	Report	1	10% (10)	Continuous	All
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المناهج الأسبوعي النظري

Week	Material Covered
Week 1	<u>مقدمة عن الأخطاء اللغوية</u>
Week 2	<u>التاء المربوطة والتاء المفتوحة</u>
Week 3	<u>همزة الوصل والقطع</u>
Week 4	<u>الهمزة المتوسطة والمتطرفة</u>
Week 5	<u>قواعد كتابة الالف الممدودة والمقصورة</u>
Week 6	<u>الحروف الشمسية والقمرية</u>
Week 7	<u>الضاد والظاء</u>
Week 8	<u>العدد</u>
Week 9	<u>المفاعيل</u>
Week 10	<u>أقسام الكلام</u>
Week 11	<u>معاني حروف الجر</u>
Week 12	<u>تطبيقات الأخطاء اللغوية الشائعة</u>
Week 13	<u>النون والتنوين</u>
Week 14	<u>مقدمة عن الأخطاء اللغوية</u>
Week 15	<u>الامتحان النهائي</u>

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	الكامل في اللغة والادب لابي عباس المبرد	Yes
Recommended Texts	أخطاء لغوية شائعة لخالد بن هلال بن ناصر العبري	No
Websites	https://www.eshamel.net https://www.ektebsa7.com	

APPENDIX:

GRADING SCHEME

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information					
معلومات المادة الدراسية					
Module Title	<u>Microprocessors</u>		Module Delivery		
Module Type	<u>Core</u>		✓ Theory ✓ Lecture ✓ Lab ✓ Tutorial ✓ Practical ✓ Seminar		
Module Code	<u>CTE200</u>				
ECTS Credits	<u>6</u>				
SWL (hr/sem)	<u>150</u>				
Module Level	2	Semester of Delivery		1	
Administering Department	<u>Department of Computer Techniques Engineering</u>	College	<u>Northern Technical University Engineering Technical College/Mosul</u>		
Module Leader	Ahmad F. Al-Allaf		e-mail	<u>Ahmed.faleh@atu.edu.iq</u>	
Module Leader's Acad. Title		Assistant Professor	Module Leader's Qualification		Ph.D.
Module Tutor	None		e-mail	None	
Peer Reviewer Name		None	e-mail	None	
Review Committee Approval		/06/202313	Version Number		1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	13. Understanding Microprocessor Architecture: The course provides a comprehensive understanding of microprocessor architecture, including its components, data path, control unit, memory hierarchy, and input/output systems. Students will learn about different microprocessor families and their characteristics. 14. Instruction Set Architecture (ISA): Students gain proficiency in understanding and working with the instruction set architecture of a microprocessor. This involves learning about different instruction formats, addressing modes, data types, and the relationship between instructions and the underlying hardware. 15. Assembly Language Programming: The course cover assembly language programming for a specific microprocessor. Students learn the syntax, conventions, and techniques for writing efficient assembly language programs. They also understand the translation process from assembly language to machine code.
Module Learning Outcomes	40. Understand the fundamental concepts and principles of microprocessor architecture, including the components, data path, control unit, memory hierarchy, and input/output systems.

مخرجات التعلم للمادة الدراسية	41. Analyze and interpret the instruction set architecture (ISA) of a microprocessor, including instruction formats, addressing modes, data types, and the relationship between instructions and hardware. 42. Demonstrate proficiency in writing and debugging assembly language programs for a specific microprocessor, considering syntax, conventions, and efficient programming techniques.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: <u>Part-A: Introduction to Microprocessor:</u> Introduction and History of Microprocessors, Basic Block Diagram of a Microprocessor, Organization of Microprocessor Based System, Bus Organization, Processing Cycle of a Stored Program Computer. 8085 Microprocessor: Internal Architecture and Features of 8085 microprocessor, pin description. [6hrs.] <u>Part-B: 8086/8088 Microprocessor:</u> Internal Architecture and Features of 8086/8088 Microprocessor, components of BIU and EU. Pin descriptions and bus cycles. Pin descriptions and bus cycles, 8284 clock generator and 8288 bus controller circuits, Minimum and Maximum configurations, Memory and I/O organization, [24hrs] <u>Part-C: 8086 programming and instruction sets:</u> 8086 Addressing Modes, instruction groups, Data Movement instructions, Arithmetic and logical instructions, Jump instructions, String instructions, example. [24hrs] <u>Part -D : Different Microprocessor Architectures:</u> Register Based and Accumulator Based Architecture, RISC and CISC Architectures, Digital Signal Processors. [4hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.
------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	61	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	89	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	6
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	150		

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	3	5% (10)	4,6,10	LO # 2, 10 and 11
	Assignments	3	5% (10)	2, 5,8	LO # 3, 4 and 7
	Lab.(mid pract.)	2	10%(30)	Continuous	All
	Projects	3	5% (10)	3,9,11	LO # 5, 8 and 10
	Report	6	10% (10)	Continuous	All
	Seminars	3	5% (10)	4,10,12	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	15	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	<u>1-Introduction to Microprocessor:</u> Introduction and History of Microprocessors, Basic Block Diagram of a Microprocessor, Organization of Microprocessor Based System, Bus Organization, Processing Cycle of a Stored Program Computer.
Week 2	<u>2-8085 Microprocessor:</u> Internal Architecture and Features of 8085 microprocessor, pin description.
Week 3	<u>3-8086/8088 Microprocessor:</u> Internal Architecture and Features of 8086 Microprocessor, components of BIU and EU.
Week 4	<u>4-8086 Microprocessor:</u> Pin descriptions and bus cycles.
Week 5	<u>5-8086 Microprocessor:</u> Pin descriptions and bus cycles.
Week 6	<u>6-8086 Microprocessor:</u> 8284 clock generator and 8288 bus controller circuits
Week 7	<u>7-8086 Microprocessor:</u> Minimum and Maximum configurations, Memory and I/O organization.
Week 8	<u>8-8086 programming and instruction sets</u> 8086 Addressing Modes, instruction groups
Week 9	<u>9-8086 instruction sets:</u> Data Movement instructions
Week 10	<u>10-8086 instruction sets:</u> Arithmetic and logical instructions
Week 11	<u>11-8086 instruction sets:</u> Jump instructions
Week 12	<u>12-8086 instruction sets:</u> String instructions
Week 13	<u>13-8086 instruction sets:</u> Programming examples
Week 14	<u>14-Different Microprocessor Architectures:</u> Register Based and Accumulator Based Architecture, RISC and CISC Architectures, Digital Signal Processors.
Week 15	Final Exam

Delivery Plan (Weekly Lab. Syllabus) المنهاج الاسبوعي للمختبر	
Week	Material Covered
Week 1	Lab 1: Introduction to debugging program
Week 2	Lab 2: 8086 instruction formats
Week 3	Lab 3: 8086 addressing modes
Week 4	Lab 4: Program examples of Data movement instructions
Week 5	Lab 5: Program examples of Arithmetic instructions
Week 6	Lab 6: Program examples of Arithmetic instructions (addition and subtraction)
Week 7	Lab 7: Program examples of Arithmetic instructions (Multiplication and division)
Week 8	Lab 8: Program examples of logical instructions
Week 9	Lab 9: Program examples of shift and rotate instructions
Week 10	Lab 10: Program examples of timing delay using counters
Week 11	Lab 11: Program examples of JMPs instructions
Week 12	Lab 12: Program examples of stack instructions
Week 13	Lab 13: Program examples of strings instructions
Week 14	Lab 14: Program examples of call and return instructions

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	The Intel Microprocessors (8th Edition), BARRY B. BREY, 2009	No
Recommended Texts	MICROPROCESSOR 8086 Architecture, Programming and Interfacing, Sunil Mathur, 2011	No
Websites	Digital Systems: From Logic Gates to Processors: https://www.coursera.org https://www.edx.org https://ocw.mit.edu	

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



MODULE DESCRIPTOR FORM

نموذج وصف المادة الدراسية

Module Information					
معلومات المادة الدراسية					
Module Title	<u>Analog Electronics Fundamentals</u>			Module Delivery	
Module Type	Core			✓ Theory ✓ Lecture ✓ Lab ✓ Tutorial ✓ Practical ✓ Seminar	
Module Code	CTE201				
ECTS Credits	5				
SWL (hr/sem)	125				
Module Level	UGx11 2		Semester of Delivery		1
Administering Department		DEPARTMENT OF COMPUTER TECHNIQUES ENGINEERING	College	<u>Northern Technical University</u> Engineering Technical College/Mosul	
Module Leader	Thabat F. Thabet		e-mail	Thabet.tfy@ntu.edu.iq	
Module Leader's Acad. Title		Lecturer	Module Leader's Qualification		PhD.
Module Tutor	None		e-mail	None	
Peer Reviewer Name		None	e-mail	None	
Review Committee Approval		10/06/2023	Version Number		1.0

Relation With Other Modules العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية		
Module Aims أهداف المادة الدراسية	16. To learn the physics of material and the basics of electronic devices. 17. Study the structure and the characteristics of electronic devices (diodes and transistors). 18. To learn the applications of different types of diodes. 19. Study the principles of binary junction transistors (BJT), biasing, cutoff, saturation, operating point and DC load line.	
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	43. Learning about the physics of material. 44. Learning about the different electronic devices (structure and characteristics). 45. Ability to recognize and test different electronic devices (diodes and transistors). 46. Learning about the applications of diodes (circuits and their functions). 47. Ability to design, and implement different diode circuits (with a specific required output). 48. Ability to design, or analyze BJT biasing circuits to know the operating point.	

Indicative Contents المحتويات الإرشادية	Indicative content includes the following: Part A – Introduction to electronics Physics of material, atoms, electrons and energy bands, types of material (insulators, conductors, and semiconductors), N-type and P-type semiconductor. Diodes, forward bias, reverse bias, V-I characteristics [8 hrs] Part B- Application of diodes. Half-wave rectifier, average value, r.m.s. value, capacitor filter, ripple voltage. Full-wave rectifier, average value, r.m.s. value, capacitor filter, ripple voltage. Half-wave rectifier, average value, r.m.s. value, capacitor filter, ripple voltage. Diode limiters, output voltage signal. Clampers and Voltage Doubler. [16 hrs] Part C Other types of diodes Zener diodes, V-I characteristics. Voltage regulators using Zener diode (variable input voltage, and variable load). Zener limiters. Special purpose diodes, Varactor, Light Emitting diode LED, Photo diode, Schottky diode, Tunnel diodes. [16 hrs] Part D Transistors Bipolar junction transistor BJT, current, voltages, and parameters, maximum ratings. BJT biasing, cutoff, saturation, operating point. Transistor bias circuits, base-bias, voltage divider Transistor bias circuits, emitter-bias, collector-feedback. [16 hrs] - Revision problem classes [4 hrs]
---	---

Learning and Teaching Strategies استراتيجيات التعلم والتعليم	
Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.

Student Workload (SWL) الحمل الدراسي للطالب			
Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	60	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	65	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4.3
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	3, 7, 11, 14	LO #2, 4, 5 and 6
	Assignments	6	10% (10)	3, 5, 7, 9, 11, 14	LO # 2, 4, 5 and 6
	Projects / Lab. Report	1	10% (10)	Continuous	All
		5	10% (10)	5, 6, 8, 10, 13	LO # 2, 3, 4, 5 and 6
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week	Material Covered
Week 1	Introduction to electronics.
Week 2	Physic of diode. Diode's equivalent circuits.
Week 3	Application of diodes.
Week 4	Half-wave rectifier.
Week 5	Full-wave rectifier .
Week 6	Diode Limiters
Week 7	Clampers.
Week 8	Zener diode Characteristics.
Week 9	Voltage regulator using Zener diode.
Week 10	Another typed of diode.
Week 11	Physic of transistor: Bipolar junction transistor BJT.
Week 12	DC operation point.
Week 13	Transistor bias circuits.
Week 14	Transistor bias circuits.
Week 15	Preparatory Week
Week 16	Final Exam

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	Lab 1: Introduction to the Electronic Laboratory
Week 2	Lab 2: Diode characteristics
Week 3	Lab 3: Half-wave rectifiers
Week 4	Lab 4: Full-wave rectifiers
Week 5	Lab 5: Filter for Half-wave rectifiers
Week 6	Lab 6: Filter for Full-wave rectifiers
Week 7	Lab 7: Clipping Circuits
Week 8	Lab 8: Clamper and Voltage Doubler
Week 9	Lab 9: Zener diode characteristics

Week 10	Lab 10: Voltage regulators using Zener diode
Week 11	Lab 11: Transistor Characteristics
Week 12	Lab 12: Transistor Biasing (part 1)
Week 13	Lab 13: Transistor Biasing (part 2)
Week 14	Lab 14: Review

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	Thomas L. Floyd, Electronic Devices, 9th Edition, Pearson Education 2012	Yes
Recommended Texts	R. BOYLESTAD and L. NASHELSKY, "ELECTRONIC DEVICES AND CIRCUIT THEORY", 11th edition, Pearson Education , 2013	Yes
Websites	Digital Systems: From Logic Gates to Processors: https://www.coursera.org/learn/electronics	

APPENDIX:

APPENDIX:

GRADING SCHEME				
مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	Object oriented programing		Module Delivery
Module Type	Core		☒ Theory ☒ Lecture ☒ Lab ☒ Tutorial ☒ Practical ☒ Seminar
Module Code	CTE202		
ECTS Credits	5		
SWL (hr/sem)	125		
Module Level	UGx11 2	Semester of Delivery	1
Administering Department	Department of Computer Techniques Engineering		College
			Northern Technical University Engineering Technical College/Mosul
Module Leader	Anmar Burhan Mohammed		e-mail
			Anmar.salih@ntu.edu.iq
Module Leader's Acad. Title	Lecturer		Module Leader's Qualification
			PhD.
Module Tutor	None		e-mail
			None
Peer Reviewer Name	None		e-mail
			None
Review Committee Approval	21/06/2023		Version Number
			1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	20. • Understanding OOP Concepts: Gain a solid understanding of fundamental OOP concepts such as encapsulation, inheritance, and polymorphism. Learn how these concepts promote code reusability, modularity, and maintainability. 21. • Programming Language Proficiency: Develop proficiency in a specific programming language that supports OOP paradigms, such as Java, C++, Python, or C#. Learn the syntax, data types, control structures, and other language features necessary for implementing OOP concepts. 22. • Class and Object Creation: Learn how to create classes and objects, define attributes and behaviors, and establish relationships between objects using techniques like composition and aggregation. 23. • Encapsulation and Data Hiding: Understand the principles of encapsulation and data hiding to enforce proper access control and protect data integrity. Learn how to define public, private, and protected access levels for class members.

	24. • Inheritance and Polymorphism: Explore inheritance, where classes can inherit properties and behaviors from other classes, and polymorphism, where objects can take on different forms depending on their context. Understand the benefits and use cases for these concepts. 25. • Abstraction and Interface Design: Learn how to create abstract classes and interfaces to define common behaviors and establish contracts for implementing classes. Understand the advantages of abstraction in managing complex systems. 26. • OOP Design Patterns: Study common design patterns used in OOP, such as the Singleton, Factory, Observer, and Strategy patterns. Learn how to apply these patterns to solve common software design problems. 27. • Error Handling and Exception Handling: Understand how to handle errors and exceptions in an OOP context. Learn techniques for graceful error recovery, exception propagation, and creating custom exceptions. 28. • OOP Software Development Practices: Gain familiarity with software development practices aligned with OOP, such as modular programming, code organization, and documentation. Learn about version control systems and collaboration tools commonly used in OOP projects. 29. • OOP Analysis and Design: Learn how to analyze and design software systems using OOP principles. Understand the importance of modeling techniques like class diagrams and sequence diagrams in the software development lifecycle.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	49. Understand the principles of Object-Oriented Programming 50. Design and implement classes and objects 51. Apply access modifiers to control class member visibility. 52. Utilize inheritance and polymorphism 53. Implement function overriding and virtual functions for runtime polymorphism. 54. Develop object-oriented programs and projects. 55. Develop larger projects that demonstrate effective use of OOP concepts.
Indicative Contents المحتويات الإرشادية	Introduction to Object-Oriented Programming (4 hours) C++ Basics and Syntax Review (6 hours) Encapsulation and Access Control (8 hours) Inheritance and Polymorphism (10 hours) Dynamic Memory Management (8 hours) Object Relationships and Composition (6 hours) Operator Overloading (6 hours) Exception Handling (6 hours) Templates and Generic Programming (8 hours) Advanced OOP Concepts (8 hours) Design Patterns (8 hours) Project Development (16 hours)
Learning and Teaching Strategies استراتيجيات التعلم والتعليم	
Strategies	Understand the Core Concepts: Begin by grasping the fundamental principles of OOP, including encapsulation, inheritance, and polymorphism. Gain a clear understanding of how these concepts work together to create object-oriented systems.

	Hands-on Coding: Actively practice writing code in C++ to reinforce your understanding of OOP. Implement classes, objects, and inheritance hierarchies. Solve programming exercises and work on small projects to apply OOP concepts in practical scenarios. Read and Analyze Code Examples: Study well-written C++ code that utilizes OOP techniques. Analyze how classes and objects are structured, how inheritance is implemented, and how polymorphism is achieved. This will help you understand real-world applications of OOP. Work with Real-World Examples: Seek out real-world examples or case studies where OOP has been employed in C++ projects. Examine open-source projects or sample code to understand how OOP is used to solve complex problems and create modular, reusable code.
--	---

Student Workload (SWL) الحمل الدراسي للطالب			
Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	61	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	64	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	8	10% (10)	5, 10	LO #1, 2, 6,7,8,9,10 and 11
	Assignments	8	10% (10)	2, 12	LO # 3, 4, 6, 7,9,10,12,14
	Projects / Lab.	16	10% (10)	Continuous	All
	Report	5	10% (10)	2,4,8,10and13	LO#2,4,8,10 and 13
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الأسبوعي النظري	
Week	Material Covered
Week 1	Introduction to C++ and OOP Basics Introduction to C++ programming language • Basic syntax, variables, and data types • Functions and control structures • Introduction to object-oriented programming (OOP) concepts: classes, objects, and methods •
Week 2	Classes and Objects Defining and declaring classes • Creating objects and using constructors • Encapsulation and access modifiers (public, private, protected) •

	Member functions and data members •
Week 3	<u>Inheritance and Polymorphism</u> <u>Inheritance hierarchy and base/derived classes</u> • <u>Single inheritance and multiple inheritance</u> • <u>Polymorphism and function overriding</u> • Abstract classes and pure virtual functions asses •
Week 4	<u>Dynamic Memory Allocation and Pointers</u> <u>Dynamic memory allocation with new and delete</u> • <u>Introduction to pointers and references</u> • <u>Memory management and deallocation</u> • Object lifetime and scope •
Week 5	<u>Operator Overloading</u> <u>Overloading unary and binary operators</u> • <u>Overloading comparison and assignment operators</u> • <u>Friend functions and operator overloading</u> • Best practices and guidelines for operator overloading •
Week 6	<u>Templates and Generic Programming</u> <u>Introduction to templates and generic programming</u> • <u>Function templates and class templates</u> • <u>Template specialization</u> • Standard Template Library (STL) containers and algorithms •
Week 7	<u>Exception Handling</u> <u>Introduction to exception handling</u> • <u>try-catch blocks and handling exceptions</u> • <u>Throwing and catching exceptions</u> • Exception specifications and best practices •
Week 8	Midterm exam •
Week 9	<u>File Handling and Streams</u> <u>Input/output streams and file handling</u> • <u>Reading from and writing to files</u> • <u>Error handling and file status flags</u> • Working with text and binary files •
Week 10	<u>Advanced OOP Concepts</u> <u>Polymorphism and virtual functions</u> • <u>Virtual base classes and diamond problem</u> • <u>Type casting and runtime type identification (RTTI)</u> • Object slicing and dynamic casting •
Week 11	<u>Standard Library Algorithms</u> <u>Overview of the standard library algorithms</u> • <u>Sorting and searching algorithms</u> • <u>Numeric algorithms and iterators</u> • Practical applications and usage examples •
Week 12	<u>Memory Management</u> <u>Smart pointers: unique_ptr, shared_ptr, weak_ptr</u> • <u>Memory management strategies and pitfalls</u> • <u>Resource Acquisition Is Initialization (RAII)</u> • Memory leaks and debugging techniques •
Week 13	<u>Namespaces and Organizing Code</u> <u>Using namespaces for code organization</u> • <u>Creating and managing namespaces</u> •

	<u>Namespace conflicts and resolutions</u> • Best practices for code modularization •
Week 14	<u>Namespaces and Organizing Code</u> <u>Using namespaces for code organization</u> • <u>Creating and managing namespaces</u> • <u>Namespace conflicts and resolutions</u> • Best practices for code modularization •
Week 15	Review
Week 16	Final Exam

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	<u>Introduction to C++</u> • <u>Basic syntax, variables, and data types</u> • Functions and control structures •
Week 2	<u>Classes and Objects</u> • classes, objects, and methods document analysis •
Week 3	<u>Encapsulation and access modifiers (public, private, protected)</u> • Member functions and data member •
Week 4	<u>Introduction to templates and generic programming</u> • Function templates and class templates •
Week 5	<u>Template specialization</u> • Standard Template Library (STL) containers and algorithms •
Week 6	<u>Introduction to exception handling</u> • try-catch blocks and handling exceptions •
Week 7	<u>Introduction to exception handling</u> • try-catch blocks and handling exceptions •
Week 8	Midterm
Week 9	•OLID principles: Single Responsibility, Open-Closed, Liskov Substitution, Interface Segregation, Dependency Inversion
Week 10	Design patterns: overview and examples
Week 11	•Multithreading and concurrency in C++
Week 12	Assignment
Week 13	<u>Applying design principles to real-world scenarios</u> • Code refactoring and improvement
Week 14	Review

Week 15	Review
Week 16	Final Exam

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	"Effective Modern C++: 42 Specific Ways to Improve Your Use of C++11 and C++14" by Scott Meyers. "C++ Primer" by Stanley B. Lippman, Josée Lajoie, and Barbara E. Moo. "Design Patterns: Elements of Reusable Object-Oriented Software" by Erich Gamma, Richard Helm, Ralph Johnson, and John Vlissides.	No
Recommended Texts	Online tutorials and documentation specific to advanced C++ programming and libraries.	No
Websites	https://www.w3schools.com/cpp/cpp_oop.asp	

APPENDIX:

APPENDIX:

GRADING SCHEME				
مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information					
معلومات المادة الدراسية					
Module Title	<u>Applied Mathematics</u>			Module Delivery	
Module Type	<u>Basic</u>			✓	Theory
Module Code	<u>CTE203</u>			✓	Lecture
ECTS Credits	<u>4</u>			✓	Tutorial
SWL (hr/sem)	<u>100</u>			✓	Seminar
Module Level	UGx11 2		Semester of Delivery		1
Administering Department	<u>Department of Computer Techniques Engineering</u>		College	<u>Northern Technical University Engineering Technical College/Mosul</u>	
Module Leader	Ayhan A. khaleel		e-mail	Ay_ahmed@ntu.edu.iq	
Module Leader's Acad. Title		Lecturer	Module Leader's Qualification		M.Sc.
Module Tutor		None	e-mail	None	
Peer Reviewer Name		None	e-mail	None	
Review Committee Approval		21/06/2023	Version Number		1.0

Relation With Other Modules العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	Mathematics is an important tool for understanding modern and scientific technologies, and the modern world today relies heavily on mathematics. Mathematics is also characterized by multiple benefits, including that it is an intellectual tool, a strong communication method, and it is in itself a way of thinking, through which the capabilities of individuals develop, and it helps us in advanced logical thinking. It also Introduce students to mathematics through the laws and issues necessary for the purpose of assisting them in their studies in their field of specialization .
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	56. Learning about the basic Matrix and Determinants Learning the Gaussian and Gauss-Jordan elimination, rank of a matrix. 57. Learning the Eigen values and Eigenvectors 58. Learning the First order differential equations, variable separable,

	homogeneous, linear first order and exact differential equations 59. Convergence and the Divergence tests, Alternating series ,Absolute and conditional convergence 60. Power series and Taylor and Maclaurin series
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: Part A – Review of matrices and their properties, Complex matrices, Hermitian, skew-Hermitian and unitary matrices, Inverse matrices and elementary row operation, Gaussian and Gauss-Jordan elimination, rank of a matrix. Eigen values and Eigenvectors. [20 hrs] Part B – First order differential equations, variable separable, homogeneous, linear first order and exact differential equations. [10 hrs] Part C – Convergence and the Divergence tests, Alternating series ,Absolute and conditional convergence, Power series and Taylor and Maclaurin series. [10 hrs] Revision problem classes [7 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in the delivery of this unit is to encourage students to participate in exercises, while improving and expanding their mathematical reasoning skills.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	58	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعيا	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	42	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعيا	3
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	100		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	8	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	13	10% (10)	Continuous	All
	Projects / Lab.	0	0		
	Report	0	0		
Summative assessment	Midterm Exam	2 hr	20% (20)	7	LO # 1-7
	Final Exam	3 hr	60% (60)	16	All

Total assessment	100% (100 Marks)		
-------------------------	------------------	--	--

Delivery Plan (Weekly Syllabus) المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	Review of matrices and their properties
Week 2	Complex matrices, Hermitian, skew-Hermitian and unitary matrices
Week 3	Inverse matrices and elementary row operation
Week 4	Gaussian and Gauss-Jordan elimination.
Week 5	rank of a matrix
Week 6	Eigen values and Eigenvectors.
Week 7	First order differential equations, variable separable, homogeneous
Week 8	linear first order and exact differential equations
Week 9	Non-homogeneous second order with constant coefficients
Week 10	Convergence and the Divergence tests-part1
Week 11	Convergence and the Divergence tests-part2
Week 12	Alternating series ,Absolute and conditional convergence
Week 13	Power series
Week 14	Taylor and Maclaurin series
Week 15	Final Exam

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	Advance Engineering Mathematics, Alan Jeffrey, 2002	Yes
Recommended Texts	Calculus II & Calculus III, Paul Dawkins, 2007	No
Websites	https://tutorial.math.lamar.edu/Classes/CalcIII/CalcIII.aspx https://tutorial.math.lamar.edu/Classes/CalcII/CalcII.aspx	

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D – Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	Data Structure	Module Delivery	
Module Type	Core	☒ Theory ☒ Lecture ☒ Lab ☒ Tutorial ☒ Practical ☒ Seminar	
Module Code	CTE204		
ECTS Credits	4		
SWL (hr/sem)	100		
Module Level	UGx11 2	Semester of Delivery	1
Administering Department	Department of Computer Techniques Engineering	College	Northern Technical University Engineering Technical College/Mosul
Module Leader	Mohand lokman Ahmed	e-mail	mohandaldabag@ntu.edu.iq
Module Leader's Acad. Title	Asst.Prof.	Module Leader's Qualification	PhD.
Module Tutor	None	e-mail	None
Peer Reviewer Name	None	e-mail	None
Review Committee Approval	21/06/2023	Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	30. Understanding the basic concepts of Data structures such as arrays, stacks, queues, trees, graphs, and so on. for building blocks of algorithms and programs.. 31. Analyzing the algorithms that are used to manipulate data. By analyzing algorithms, we can determine their efficiency and optimize them to make them faster and more efficient. 32. Choosing the right data structure is essential for developing efficient programs. The study of data structures helps in choosing the right data structure for a particular problem. 33. The study of data structures helps in implementing data structures such as linked lists, trees, and graphs. By implementing data structures, we can create efficient programs that can handle large amounts of data.
Module Learning Outcomes	61. Understanding the fundamental concepts of data structures. 62. Analyzing the performance of algorithms 63. Choosing appropriate data structures. 64. Implementing data structures.

مخرجات التعلم للمادة الدراسية	65. Designing algorithms. 66. Applying data structures to real-world problems
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: - Part A – Introduction to data structures: Overview of data structures, their types, and applications. [8 hrs] - Part B- Arrays and Linked lists:: One-dimensional and multi-dimensional arrays, array operations, and applications. Linked lists: Singly linked lists, doubly linked lists, circular linked lists, and their operations.[12hrs] - Part C -: Stacks and Queues Array-based and linked-list based implementation of stacks and queues, their operations, and applications.. [12 hrs] - Part D - Trees: Binary trees, binary search trees, AVL trees, red-black trees, and their operations. [14 hrs] - Part E – Graphs: Graph representation, graph traversal algorithms, shortest path algorithms, and minimum spanning tree algorithms..[10 hrs] - Revision problem classes [4 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be used in Data structure courses to introduce the concepts of data structures and explain the theoretical aspects of algorithms that operate on data structures, provide hands-on exercises to help students implement data structures and algorithms using programming languages such as C++, Java, or Python, provide practice problems to help students improve their problem-solving skills and prepare for exams and assess students' understanding of data structures and algorithms through quizzes, exams, programming assignments, and group projects..
-------------------	---

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	61	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	64	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation

تقييم المادة الدراسية

As		Time/ Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (15)	3, 6, 9, 12	LO #1-2, 3-5, 6-8 and 10-11
	Assignments	2	5% (10)	2, 5, 11, 14	LO # 1, 3, 7-9 and 12-13
	Lab.	1	10% (30)	Continuous	All
	Report	6	10% (10)	Continuous	All
	Seminar	1	5% (10)	10	LO 1-8
	Midterm Exam	2 hr.	10% (10)	7	LO # 1-7

Summative assessment	Final Exam	3 hr.	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week	Material Covered
Week 1	General introduction to data structures: Introduce to the Basic types of Data Structures and the common algorithm
Week 2	Linear data structures: What is linear data structure, characteristics of linear data structure and types of linear data structure
Week 3	Algorithm Analysis: Algorithm Analysis types and methods,experimental of analysis algorithm
Week 4	Recursion: Introduction to recursion, some problems that solved by recursion and the difference between recursion and iteration
Week 5	Back tracking technique: Introduction to back tracking technique ,general method of back tracking technique ,when to use a Backtracking algorithm and How does Backtracking work.
Week 6	Linked Lists: Introduction linked lists data structures ,comparison between linked lists and array .
Week 7	Linked Lists: basic operations on linked lists(Insertion, Deletion and traversing).
Week 8	Types of linked lists: Doubly linked lists,circular linked lists,memory -efficient doubly linked list,unrolled linked lists
Week 9	Stacks: What is a Stack,how stacks are used and stack applications and implementations.
Week 10	Queue: What is queue ,how are queues used and queue exceptions and implementations
Week 11	Tree : What is tree,binary trees and types of binary trees and properties of binary trees.
Week 12	Tree: Binary tree traversals,generic trees(N-ary trees) and threaded binary tree traversals
Week 13	Sorting algorithm: What is sorting ,why is sorting necessary and classification of sorting algorithms.
Week 14	Sorting algorithm: Classification of sorting algorithm types:bubble sort, selection sort, insertion sort,shell sort,merge sort ,quick sort and tree sort.
Week 15	Review
Week 16	Final Exam

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	Lab 1: function declaration and function expression
Week 2	Lab 2: pointer declaration and initialization.
Week 3	Lab 3:user defined data structures
Week 4	Lab 4: Implementation problems using iteration/recursion problems
Week 5	Lab 5: implementation of back tracking method
Week 6	Lab 6:how to define a linked list node and programming traversal operation.

Week 7	Lab 7:programming a linked list insertion operation.
Week 8	Lab 8: programming a linked list deletion operation.
Week 9	Lab 9: Implementation of push and pop operation on stack
Week 10	Lab 10: Programming some application using stack.
Week 11	Lab11: Programming the queue to store some of data
Week 12	Lab 12: Programming a storing data as tree structure and implementation of various traversal techniques
Week 13	Lab 13: Programming a storing data as graph structure and implementation of various traversal technique
Week 14	Lab 14: Programming a bubble sort, selection sort and insertion sort algorithms
Week 15	Lab 15: Programming a shell sort,merge sort ,quick sort and tree sort algorithms
Week 16	Final Exam

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	1- Data Structures And Algorithms Made Easy by Narasimha Karumanch (Author)	YES
Recommended Texts	data structure, algorithm and application in c++ by Sartaj sahni	No
Websites	https://opendatastructures.org/	

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Ministry of Higher Education and
Scientific Research - Iraq
Northern Technical University
Engineering Technical College/Mosul
Department of Computer Techniques Engineering



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information					
معلومات المادة الدراسية					
Module Title	<u>Measurements & Sensors</u>		Module Delivery		
Module Type	<u>Core</u>		✓ Theory ✓ Lecture ✓ Lab Tutorial Practical ✓ Seminar		
Module Code	<u>CTE205</u>				
ECTS Credits	<u>2</u>				
SWL (hr/sem)	<u>50</u>				
Module Level	UGx11 2		Semester of Delivery	1	
Administering Department	<u>Department of Computer Techniques Engineering</u>		College	<u>Northern Technical University Engineering Technical College/Mosul</u>	
Module Leader	Ahmed Waled Kasim		e-mail	ahmadwaled1973@ntu.edu.iq	
Module Leader's Acad. Title	Lecturer		Module Leader's Qualification	Ph.D.	
Module Tutor	None		e-mail	None	
Peer Reviewer Name	None		e-mail	None	
Review Committee Approval	12/06/2023		Version Number	1.0	

Relation With Other Modules

العلاقة مع المواد الدراسية الأخرى

Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents

أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

Module Objectives أهداف المادة الدراسية	• Explain the basic working principle of various electronic measurement instruments used to measure electrical parameters like current, voltage, power etc. • Understand and describe the specifications, features, characteristics, error and the performance of an instrument. • Learn about various types AC bridges and their applications in measurements of capacitance, frequency, inductance etc. • Gain knowledge about the functional blocks of a CRO and do analysis, measurements of waveform display. • Explain working of various types of sensors, transducers and their applications.
Module Learning Outcomes	After the completion of course, the students will have ability to: 67. Learning about the principle of various electronic measurement instruments. 68. Ability to design the AVO-meter instrument from PMMC.

مخرجات التعلم للمادة الدراسية	69. Learning about the both types of electrical bridges (DC and AC) bridges. 70. Learning the main principles of Oscilloscopes instruments. 71. 5. Learning the main principles of the electrical sensors, transducers and taking some examples about them.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: - Part A – Measurement & Errors: Definitions, significant figures, some examples, Types of Errors, Statistical Analysis with applications examples [2 hrs]. - Part B- Electromechanical Indicating Instruments: The DC Ammeters and DC Voltmeters, Properties of DC Voltmeters and Series type Ohmmeter, Alternating - Current Indicating Instruments, Thermo-instruments(Thermocouple Instrument), Electrodynamometer and their application [6 hrs] - Part C Bridges and their Applications: DC and AC Bridges with some examples [4 hrs] - Part D Oscilloscopes; [2 hrs] - Part E Hall Effect Sensors: Types of Hall Effect Sensors, Some Examples about Hall Effect Sensors [4 hrs] - Part F Signal Generation: [2 hrs] - Part G Analogue and Digital Data Acquisition System: [2 hrs] - Part H Computer – Controlled Test Syste: [2 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	29	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعيا	2
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	21	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعيا	1
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	50		

Module Evaluation

تقييم المادة الدراسية

As	Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Quizzes	4	10% (10)	5, 8,11,14	LO #1, 2, 10 and 11

Formative assessment	Assignments	6	10% (10)	2, 3,4,5,7,10	LO # 3, 4, 6 and 7
	Projects / Lab. Report	15	10% (10)	Continuous	All
Summative assessment	Midterm Exam	2 hr	20% (20)	9	LO # 1-9
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week	Material Covered
Week 1	Measurement and Errors.
Week 2	Electromechanical Indicating Instruments.
Week 3	Electromechanical Indicating Instruments.
Week 4	Electromechanical Indicating Instruments.
Week 5	Bridges and their Applications.
Week 6	Bridges and their Applications.
Week 7	Oscilloscopes.
Week 8	1-Theory of Hall Effect, Hall Effect Sensors, Basic Hall Effect Sensors. 2-Analogue output Sensors.
Week 9	Mid-Term Exam.
Week 10	1- Digital output Sensors. 2- Some Examples about Hall Effect Sensors.
Week 11	Signal Generation.
Week 12	Analogue and Digital Data Acquisition System.
Week 13	Computer – Controlled Test System.
Week 14	Preparatory Week.
Week 15	Final Exam

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	Lab1: Measurements of DC current.
Week 2	Lab2: Measurements of DC voltage.
Week 3	Lab 3: Loading effect on Voltmeter.
Week 4	Lab 4: Series type Ohmmeter.
Week 5	Lab 5: AC Voltmeter using half wave rectifier.
Week 6	Lab 6: AC Voltmeter using full wave rectifier.
Week 7	Lab 7: DC Bridges (Wheatstone bridge).
Week 8	Lab 8: Comparison bridges.
Week 9	Lab 9: Maxwell and Hay bridges.
Week 10	Lab 10: Measurements of frequency.
Week 11	Lab 11: Measurements of phase angle using Lissajous method.
Week 12	Lab 12: Calibration of Thermocouple.
Week 13	Lab 13: Photosensitive.

Week 14	Lab 14: Review.
----------------	------------------------

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	Electronic Instrumentation and Measurement Techniques by: W. D. Cooper and A. D. Helfrick. 3 rd edition.	Yes
Recommended Texts	Principles of Measurement Systems" by John P. Bentley	No
Websites	Measurement systems: Application and design 4th Revised edition by Ernest O. Doebelin https://www.amazon.com/Measurement-Systems-Application-Design-Doebelin/dp/0070173389	

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	<u>Computer architecture</u>		Module Delivery
Module Type	<u>Core</u>		✓ Theory
Module Code	<u>CTE206</u>		✓ Lecture
ECTS Credits	<u>6</u>		✓ Lab
SWL (hr/sem)	<u>150</u>		✓ Tutorial
			✓ Practical
			✓ Seminar
Module Level	2	Semester of Delivery	2
Administering Department	<u>Department of Computer Techniques Engineering</u>	College	<u>Northern Technical University Engineering Technical College/Mosul</u>
Module Leader	Ahmad F. Al-Allaf	e-mail	Ahmed.faleh@atu.edu.iq
Module Leader's Acad. Title	Assistant Professor	Module Leader's Qualification	Ph.D.
Module Tutor	None	e-mail	None
Peer Reviewer Name	None	e-mail	None
Review Committee Approval	/06/202313	Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	34. Microprocessor Interfacing: Students learn about interfacing microprocessors with external devices such as memory, input/output ports, and peripherals.. 35. Memory Systems: The course cover different memory types used in microprocessor systems, such as main memory, and secondary storage, types of semiconductor memories (ROMs and RAMs). 36. Interrupts: Students learn about interrupt handling mechanisms in 8086 microprocessors. This includes understanding interrupt prioritization, interrupt service routines, and designing hardware interrupt circuits.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	72. Explain the principles and characteristics of different memory types used in microprocessor systems, including, main memory, secondary storage. 73. Design and implement interfaces between a microprocessor (16 and 32 bit) and external devices, such as memory, input/output ports, and peripherals, applying relevant protocols and techniques. 74. Interfacing different I/O deceives to the 8088 and 8086 microprocessors, such as Keyboard,

	&-segment displays, and ADC/DAC circuits. 75. Understand the concepts and mechanisms of interrupts in microprocessors, including interrupt prioritization, and interrupt service routines. 76. Designing hardware interrupt circuits.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: Part-A: Basic computer architecture and memory: Computer organization, Primary and secondary memories, Memory hierarchy, types of ROMs and RAMs, primary memory architecture, Internal structure and operation of ROMs, and RAMs [8hrs.] Part-B: Memory Interfacing: Memory address decoder, Simple address decode, 2-4 and 3-8 address decoders. Use programmable logic devices (PLDs) to decode memory addresses, Interfacing ROM and SRAM to the 8088 microprocessor, Expanding memory in size and words. Interfacing ROM and SRAM to the 8086 microprocessor, Interfacing ROM and SRAM to the 32-bit microprocessor, memory interfacing Design examples [20hrs] Part-C: I/O interfacing: The I/O Instructions, Isolated and Memory-Mapped I/O, Basic Input and Output Interfaces, Interfacing simple devices (LEDs and switches) to the 8088/8086 microprocessor, Interfacing ADC and DAC to the 8088/8086 microprocessor, Interfacing Keyboard and 7-segment displays to the 8088/8086 microprocessor [20hrs] Part -D : Interrupts: Basic Interrupt Processing, Interrupt Instructions, Interrupt Vector, Hardware Interrupts, Expanding the Interrupt Structure, Using the 74ALS244 to Expand Interrupts, Daisy-Chained Interrupt, Interrupt Examples, Real-Time Clock, Interrupt-Processed Keyboard. [12hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	61	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	89	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	6
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	150		

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	6, 10	LO #1, 2, 10 and 11
	Assignments	8	10% (10)	4, 7,10,13	LO # 3, 4, 6 and 7
	Projects / Lab.	15	10% (10)	Continuous	All
	Report	5	10% (10)	3, 6,9,12	LO # 3,6, 9 and 12
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	Basic computer architecture: • Computer organization, Primary and secondary memories, Memory hierarchy, types of ROMs and RAMs
Week 2	Primary memory architecture: • Internal structure and operation of ROMs, and RAMs
Week 3	Memory address decoder: • Simple address decode, 2-4 and 3-8 address decoders. Use programmable logic devices (PLDs) to decode memory addresses.
Week 4	Memory interfacing: • Interfacing ROM and SRAM to the 8088 microprocessor, Expanding memory in size and words.
Week 5	Memory interfacing: • Interfacing ROM and SRAM to the 8086 microprocessor.
Week 6	Memory interfacing: • Interfacing ROM and SRAM to the 32-bit microprocessor.
Week 7	Memory Interfacing: • Memory interfacing Design examples
Week 8	I/O system: • The I/O Instructions, Isolated and Memory-Mapped I/O, Basic Input and Output Interfaces
Week 9	I/O system interfacing: • Interfacing simple devices (LEDs and switches) to the 8088/8086 microprocessor
Week 10	I/O system interfacing: • Interfacing ADC and DAC to the 8088/8086 microprocessor,
Week 11	I/O system interfacing • Interfacing Keyboard and 7-segment displays to the 8088/8086 microprocessor
Week 12	Interrupts: • Basic Interrupt Processing, Interrupt Instructions, Interrupt Vector, Hardware Interrupts.
Week 13	Interrupts: • Expanding the Interrupt Structure, Using the 74ALS244 to Expand Interrupts, Daisy-Chained Interrupt.
Week 14	Interrupts: • Interrupt Examples, Real-Time Clock, Interrupt-Processed Keyboard
Week 15	• Final Exam.

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الأسبوعي للمختبر

Week	Material Covered
Week 1	Lab 1: Introduction to Proteus simulator for digital systems
Week 2	Lab 2: Introduction to Memory type and organization
Week 3	Lab 3: Address decoders
Week 4	Lab 4: SRAM interfacing
Week 5	Lab 5: ROM interfacing
Week 6	Lab 6: Expanding ROM and RAM
Week 7	Lab 7: Interfacing LEDs and switches to the microprocessor
Week 8	Lab 8: Interfacing Keyboard to the microprocessor
Week 9	Lab 9: Interfacing 7-segment display to the microprocessor
Week 10	Lab 10: Interfacing ADC to the microprocessor
Week 11	Lab 11: Interfacing DAC to the microprocessor
Week 12	Lab 12: Expanding the Interrupt Structure using the 74ALS244
Week 13	Lab 13: Interrupt design example
Week 14	Lab 14: Review

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	The Intel Microprocessors (8th Edition), BARRY B. BREY, 2009	No
Recommended Texts	MICROPROCESSOR 8086 Architecture, Programming and Interfacing, Sunil Mathur, 2011	No
Websites	https://www.coursera.org https://www.edx.org https://ocw.mit.edu	

APPENDIX:

GRADING SCHEME

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



MODULE DESCRIPTOR FORM

نموذج وصف المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	Electronic Circuits		Module Delivery
Module Type	Core		✓ Theory ✓ Lecture ✓ Lab ✓ Tutorial ✓ Practical ✓ Seminar
Module Code	CTE207		
ECTS Credits	6		
SWL (hr/sem)	125		
Module Level	UGx11	Semester of Delivery	
Administering Department	DEPARTMENT OF COMPUTER TECHNIQUES ENGINEERING	College	Northern Technical University Engineering Technical College/Mosul
Module Leader	Thabat F. Thabet	e-mail	Thabet.tfy@ntu.edu.iq
Module Leader's Acad. Title	Lecturer	Module Leader's Qualification	PhD.
Module Tutor	None	e-mail	None
Peer Reviewer Name	None	e-mail	None
Review Committee Approval	10/06/2023	Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Aims أهداف المادة الدراسية	37. To learn the applications of BJT . 38. Study the types of BJT amplifiers (Common Emitter, Common Collector, and Common Base). 39. Study the Frequency response of amplifiers. 40. Differential and Operational Amplifiers 41. Negative Feed-back (Inverting and Non-inverting Amplifiers) and other Applications of Operational Amplifiers. 42. Study the family of Field Effect Transistors (FET).
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	77. Learning about the BJT applications. 78. Learning about the types of BJT amplifiers. 79. Frequency Response 80. Learning about Differential and Operational Amplifiers 81. Study the family of Field Effect Transistors (FET)..

Indicative Contents المحتويات الإرشادية	Indicative content includes the following:
	Part A – BJT Applications BJT as a Switch (cutoff and saturation). Linear operation and DC load line [8 hrs] Part B- BJT Amplifiers. Common Emitter CE. Common Collector CC. Common Base CB. [12 hrs] Part C Frequency Response The Decibel. Low Frequency Amplifier Response (Effect of the external capacitors). High Frequency Amplifier Response (Effect of the internal capacitors). Total Frequency Response (Bode Plot). [16 hrs] Part D Differential and Operational Amplifiers Differential and Operational Amplifiers. Negative Feed-back (Inverting and Non-inverting Amplifiers). Applications of Operational Amplifiers. [12 hrs] Part E Field Effect Transistors (FET). Junction Field Effect Transistors (JFET). Metal Oxide Semiconductor Field Effect Transistors (MOSFET). [8 hrs] - Revision problem classes [4 hrs]

Learning and Teaching Strategies استراتيجيات التعلم والتعليم	
Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.

Student Workload (SWL) الحمل الدراسي للطالب			
Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	61	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	89	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	6
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	150		

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	4, 7, 9, 13	LO # 1, 2, 4 and 5
	Assignments	8	10% (10)	3, 4, 7, 9, 14	LO # 1, 2, 4, 5 and 6
	Projects / Lab.	1	10% (10)	Continuous	All
	Report	5	10% (10)	3, 7, 8, 11, 13	LO # 2, 3, 4, 5 and 6
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week	Material Covered
Week 1	BJT as a Switch (cutoff and saturation).
Week 2	Linear operation and DC load line.
Week 3	Common Emitter CE.
Week 4	Common Collector CC.
Week 5	Common Base CB.
Week 6	The Decibel.
Week 7	Low Frequency Amplifier Response (Effect of the external capacitors)
Week 8	High Frequency Amplifier Response (Effect of the internal capacitors)
Week 9	Total Frequency Response (Bode Plot)
Week 10	Differential and Operational Amplifiers.
Week 11	Negative Feed-back (Inverting and Non-inverting Amplifiers).
Week 12	Applications of Operational Amplifiers.
Week 13	Junction Field Effect Transistors (JFET).
Week 14	Metal Oxide Semiconductor Field Effect Transistors (MOSFET).
Week 15	Preparatory Week
Week 16	Final Exam

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	Lab 1: Review of Transistor Biasing (operating point)
Week 2	Lab 2: BJT as a Switch (cutoff and saturation).
Week 3	Lab 3: Linear operation and DC load line.
Week 4	Lab 4: Common Emitter Amplifiers
Week 5	Lab 5: Common Collector Amplifiers
Week 6	Lab 6: Common Base Amplifiers
Week 7	Lab 7: Frequency response of OPAMP
Week 8	Lab 8: Inverting and Non-inverting OPAMPs
Week 9	Lab 9: Analogue Comparator
Week 10	Lab 10: The Integrator Circuit
Week 11	Lab 11: The Differentiator Circuit
Week 12	Lab 12: FET
Week 13	Lab 13: FET Amplifier
Week 14	Lab 14: Review

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
--	------	---------------------------

Required Texts	Thomas L. Floyd, Electronic Devices, 9th Edition, Pearson Education 2012	Yes
Recommended Texts	R. BOYLESTAD and L. NASHELSKY, "ELECTRONIC DEVICES AND CIRCUIT THEORY", 11th edition, Pearson Education , 2013	Yes
Websites	Digital Systems: From Logic Gates to Processors: https://www.coursera.org/learn/electronics	

APPENDIX:

GRADING SCHEME				
مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Communication Fundamentals		Module Delivery	
Module Type	Core		☒ Theory ☒ Lecture ☒ Lab ☒ Tutorial ☒ Practical ☒ Seminar	
Module Code	CTE208			
ECTS Credits	7			
SWL (hr/sem)	175			
Module Level	UGx11 2	Semester of Delivery		2
Administering Department	Department of Computer Techniques Engineering		College	Northern Technical University Engineering Technical College/Mosul
Module Leader	Dr. Emad A. Mohammed		e-mail	e.a.mohammed@ntu.edu.iq
Module Leader's Acad. Title	Asst. prof.		Module Leader's Qualification	PhD
Module Tutor	None		e-mail	None
Peer Reviewer Name	None		e-mail	None
Review Committee Approval	21/06/2023		Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	43. To learn the fundamentals of communications system architecture. 44. To learn the basic components used in communication system and each component basic functions. 45. To learn the types of channels that are used in communication system. 46. To learn the basic techniques used in signal representation, modulation and demodulation. 47. To learn the basics of transmission lines, their use and their equivalent circuits
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	82. To learn how to deal with different types of signals. 83. To learn how to utilize linear and nonlinear systems. 84. To learn how to design different types of filters. 85. To learn the basics of finding the spectrum of different types of signals. 86. To be familiar with various types of modulation. 87. To be familiar with how to use smith chart for transmission lines.

Indicative Contents المحتويات الإرشادية	Indicative content includes the following:
	<u>Part A – Signals and Systems</u> Signals and system definition, periodic signals, non-periodic signal, deterministic and non-deterministic signals Linear systems and nonlinear systems, filters, [8 hrs]
	<u>Part B- Fourier Series and Transform</u> Fourier series, signal harmonics, Fourier transform, Frequency domain, exponential and trigonometric Fourier transform, Properties of Fourier Transform, application of Fourier transform [12 hrs]
	<u>Part C -Signals Transmission</u> Baseband signal transmission, line coding, polar code, bipolar code, Manchester code, Analogue modulation Techniques, AM, FM, PM, Pulse modulation techniques, PAM, PPM, PWM [12 hrs]
	<u>PartD- Digital Modulation and Digital Channels.</u> Digital modulation Techniques ASK, PSK, FSK, Multilevel modulation, QAM, Wireless channels, Shannon equation, channel capacity [12 hrs]
	<u>PartE-Transmission lines.</u> Transmission lines and their equivalent circuits, TL characteristics, Incident wave, reflected wave, Smith Chart, matching techniques [10 hrs] - Revision problem classes [6 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	74	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	5
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	101	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	7
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	175		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	8	10% (10)	4,6,10,12	LO #1, 2, 10 and 11
	Assignments	8	10% (10)	2, 5,8,11	LO # 3, 4, 6 and 7
	Projects / Lab.	15	10% (10)	Continuous	All
	Report	8	10% (10)	1,3,9,13	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	15	All

Total assessment	100% (100 Marks)		
-------------------------	------------------	--	--

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week	Material Covered
Week 1	Signals and system definition, periodic signals, non-periodic signal, deterministic and non-deterministic signals
Week 2	Linear systems and nonlinear systems, filters
Week 3	Fourier series, signal harmonics
Week 4	Fourier transform, Frequency domain, exponential and trigonometric Fourier transform
Week 5	Properties of Fourier Transform, application of Fourier transform
Week 6	Baseband signal transmission, line coding, polar code, bipolar code, Manchester code
Week 7	Analogue modulation Techniques, AM, FM, PM
Week 8	Pulse modulation techniques, PAM, PPM, PWM
Week 9	Digital modulation Techniques ASK, PSK, FSK
Week 10	Multilevel modulation, QAM
Week 11	Wireless channels, Shannon equation, channel capacity
Week 12	Transmission lines and their equivalent circuits, TL characteristics
Week 13	Incident wave, reflected wave
Week 14	Smith Chart, matching techniques
Week 15	Final Exam.

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	Lab 1: Signals properties
Week 2	Lab 2: Linear systems and nonlinear systems, filters
Week 3	Lab 3: Harmonics determination
Week 4	Lab 4: Fourier transform, Spectrum analysis
Week 5	Lab 5: Fourier transform properties
Week 6	Lab 6: Types of Baseband signals
Week 7	Lab 7: Amplitude and phase modulation
Week 8	Lab 8: Frequency modulation
Week 9	Lab 9: PPM, PAM, PWM
Week 10	Lab 10: ASK
Week 11	Lab 11: FSK
Week 12	Lab 12: PSK
Week 13	Lab 13: QAM
Week 14	Lab 14: Review

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
--	------	---------------------------

Required Texts	Ferrel Stremier "Introduction to Communication Systems" Addison Wesley Longman, 3rd Edition 1992	Yes
Recommended Texts	B.P. Lathi "Modern Digital and Analog Communication Systems" Oxford University Press, 4 th Edition, 2010	No
Websites	Communication Skills Courses & Tutorials Online https://www.udemy.com	

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Computer applications		Module Delivery	
Module Type	Core		☒ Theory ☒ Lecture ☒ Lab ☒ Tutorial ☒ Practical ☒ Seminar	
Module Code	CTE209			
ECTS Credits	4			
SWL (hr/sem)	100			
Module Level	UGx11 2	Semester of Delivery		1
Administering Department	Department of Computer Techniques Engineering		College	Northern Technical University Engineering Technical College/Mosul
Module Leader	Shaima Miqdad Mohamed Najeeb		e-mail	shaimamiqdad76@ntu.edu.iq
Module Leader's Acad. Title	Lecturer		Module Leader's Qualification	M.Sc.
Module Tutor	None		e-mail	None
Peer Reviewer Name	None		e-mail	None
Review Committee Approval	21/06/2023		Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	48. provide a foundation in programming for engineering problem solving using the MATLAB software package. 49. develop the skills analyze and break down a program and solve it . 50. study the creation and use of functions and scripts in MATLAB. 51. study the use of MATLAB for data analysis and visualization, including plotting functions. 52. Learn the capabilities and applications supported by the MATLAB program, implement them, and use them to solve various problems.
Module Learning Outcomes	88. Ability to use MATLAB: Students should be able to use the MATLAB software effectively, including navigating the MATLAB desktop, using the command window, and accessing the help system.

مخرجات التعلم للمادة الدراسية	89. Ability to create scripts and functions: Students should be able to create and use MATLAB scripts and functions to solve problems and automate tasks. 90. Ability to perform data analysis and visualization: Students should be able to use MATLAB's built-in functions and tools for data analysis and visualization 91. Ability to perform simulations and modeling: Students should be able to use MATLAB for simulations and modeling of systems. 92. Ability to perform Graphical User Interfaces(GUI) and apply to construct the front end for different applicatio
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: - Part A – Introduction to MATLAB and Data Structures Overview of MATLAB environment, history, and applications. Basic commands, arrays, matrices, vectors, and cell arrays. scripts, functions and File I/O and Data Manipulation: Importing and exporting data from files, data cleaning, and manipulation.[10 hrs] - Part B- Numeric Data Types and Basic Operations: Numeric data types, arithmetic operations, and mathematical functions.[4 hrs] - Part C - Control Structures: Conditional statements, loops, and logical operators. [10 hrs] - Part D -Plotting and Visualization: Creating plots, customizing plots, and 2D/3D visualization. [6 hrs] - Part E – Simulink and GUI Simulink concept, creating models, and simulation. Graphical User Interfaces(GUI) construct the front end for different applicatio.[10 hrs] - Revision problem classes [3 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be used in MATLAB courses to introduce concepts, explain syntax and functions, and provide examples. Hands-on exercises are an effective learning strategy in MATLAB courses to help students apply what they have learned. Exercises may involve solving problems, writing functions, and working with data. Case studies are a useful teaching strategy in MATLAB courses to help students understand how MATLAB can be used in real-world applications. Overall, the learning and teaching strategies of a MATLAB course should be designed to maximize students' understanding and proficiency in using MATLAB for various applications in engineering, science, and other fields.
------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	45	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	3
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	55	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	100		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	5	10% (10)	2, 12	LO # 3, 4, 6 and 7
	Projects / Lab.	15	10% (10)	Continuous	All
	Report	2	10% (10)	13	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week	Material Covered
Week 1	General introduction to matlab programming: Introduce to the basics of programming in general and programming MATLAB® in particular.Environment and Settings,Preferences and settings, platform differences, adding hardware and optional features
Week 2	Programming in MATLAB: Introduction to matrices and vectors , creating a Matlab Matrix, referencing the Elements of a Matrix, deleting a Row or a Column in a Matrix.
Week 3	Programming in MATLAB: Arithmetic ,logical and bitwise operations.
Week 4	Programming IN MATLAB: Writing MATLAB scripts and functions, a custom-made Matlab functions.
Week 5	Programming in MATLAB : Loops and control flow (for-loops, if-statements)
Week 6	Function in MATLAB : Declare function name, inputs, and outputs(syntax) with examples.
Week 7	Plotting in matlab: Overview of MATLAB Plotting, Plotting Process graph components,figure tools,selecting plot types
Week 8	Plotting in matlab: Basic Plotting (Multiple Data Sets in One Graph, Specifying Line Styles and Colors, Multiple Plots in One Figure, Setting Axis Limits).
Week 9	Plotting in matlab: Mesh and surface plots, visualizing functions of two variables .
Week 10	Plotting in matlab: Handle graphics: Work with graphics objects and set object properties. Animations: Create moving graphics
Week 11	Matlab simulink: Simulink Concepts, simulink environment,basic elements,simulink librarys
Week 12	Matlab simulink: Block Libraries,modifying the blocks ,interactive model editing,programmatic model editing and running simulation .
Week 13	MATLAB GUI: Creating Graphical User Interfaces, introduces GUIDE, the MATLAB graphical user interface design environment, Laying out a GUI,
Week 14	3D Computer Graphics Operations:

	Programming a GUI, introduces callbacks to define behavior of the GUI components, Menu-driven programs, Controls: uimenu and uicontrol.
Week 15	Final Exam.

Delivery Plan (Weekly Lab. Syllabus)

المناهج الأسبوعي للمختبر

Week	Material Covered
Week 1	Lab 1: Introduction to MATLAB .
Week 2	Lab 2: Basic commands
Week 3	Lab 3: Working with matrices part(I)
Week 4	Lab 4: Working with matrices part(II)
Week 5	Lab 5: Relational ,logical bitwise operations
Week 6	Lab 6: Input and output commands in a script file.
Week 7	Lab 7: Flow control(if and switch-case) statements
Week 8	Lab 8: Loop(for,while,break,continue)statements
Week 9	Lab 9: M-file functions
Week 10	Lab 10: 2D Plotting functions
Week 11	Lab 11: 3D Plotting functions
Week 12	Lab 12: Basics of Matlab simulink
Week 13	Lab 13:Graphical user interface part(I)
Week 14	Lab 14: Graphical user interface part(II)

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	1-MATLAB for Engineering Applications 4th Edition by William Palm Iii (Author)	Yes
Recommended Texts	Getting Started with MATLAB® Version 7 by Mathwoks	No
Websites	Digital Systems: From Logic Gates to Processors: https://www.coursera.org/learn/matlab	

APPENDIX:

GRADING SCHEME

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded

(0 – 49)	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Website Design		Module Delivery	
Module Type	Core		☒ Theory ☒ Lecture ☒ Lab ☒ Tutorial ☒ Practical ☒ Seminar	
Module Code	CTE210			
ECTS Credits	3			
SWL (hr/sem)	75			
Module Level	UGx11 2	Semester of Delivery		2
Administering Department	Department of Computer Techniques Engineering		College	Northern Technical University Engineering Technical College/Mosul
Module Leader	Nawar Ali Ibrahim Al_Obaidy		e-mail	Nawar.ali@ntu.edu.iq
Module Leader's Acad. Title	Assist Lecturer		Module Leader's Qualification	PhD.
Module Tutor	None		e-mail	None
Peer Reviewer Name	None		e-mail	None
Review Committee Approval	21/06/2023		Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	The objectives of a course on Website Design: Upon successful completion of the Diploma, students should be able to: 1. Developing front-end website architecture 2. Designing user interactions on web pages 3. Developing back-end website applications 4. Creating servers and databases for functionality 5. Developing adaptive content for multiple devices (cell phones, tablets, etc.) ensure cross-platform optimization for mobile phones 6. Ensure responsiveness of applications 7. Working alongside graphic designers for web design features 8. Managing a project from conception to finished product 9. Designing and developing Application Programming Interfaces (APIs) 10. Meeting both technical and consumer needs for a web development project

	11. Learning to research new methods of development in web applications and programming languages 12. Preparing mock-ups and storyboards for a web development project. 13. Consult with clients to develop and document website requirements. 14. Demonstrating communication skills, service management skills, and presentation skills. 15. Completing job preparation tasks including writing resumes and cover letters, conducting job interviews, and developing an ePortfolio
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	Upon successful completion of the Certificate , graduates should be able to: • Use their learned skills, knowledge, and abilities to develop websites for the internet • Apply basic design principles to present ideas, information, products, and services on websites • Apply fundamental programming principles to the construction of websites • Effectively manage website projects using available resources • Demonstrate communication skills, service management skills, and presentation skills • Complete job preparation tasks including writing resumes and cover letters, conducting job interviews, and developing an ePortfolio • Apply employability skills including fundamental skills, personal management skills, and teamwork skills
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: • Part A – Introduction to Website Building: Provide a historical review of web design and the stages it went through in the process of development. Learning the basics of web design and development [4 hrs.] • Part B- The Website and Its Future: Explaining how website design can provide students with great opportunities in many jobs. In addition to increasing the demand for web designers in the future because companies are in constant need of competencies in the field of web work to provide them with all means of technical support for websites because the website is the first interface for companies and institutions. [2 hrs.] • Part C -: The Language of the Web: HTML5: present the design of a web page by referring to the HTML5 semantic tags and using some CSS3 style properties. learning a simple structure of a web page with its associated style sheet. [8 hrs.] • Part D - Structuring the content of a web page: It covers the following concepts: ○ Structuring an HTML page (head/body/header/nav/main/article/aside/footer); ○ Importing elements (font/icons/style sheet/conditional import); ○ Organizing the elements of an HTML page (container/header/menu/sidebar/footer); ○ Adding style properties to these elements. [6 hrs.] • Part E – Style Sheets: CSS3: CSS – Cascading Style Sheets – is commonly used to format HTML-type web pages using display properties (colors, fonts, borders, etc.) and positioning properties (height, width, top-down, side-by-side, etc.). The display result of a web page can be completely changed without adding additional code to the web page. [6 hrs.] • Part F – Design and Creation a Website: The purpose is: ○ Avoid repeating the same formatting code in each web page; ○ Employ common styles, using clear names (e.g. employing the same shaded style for images or text); ○ Modify the appearance of an entire website by changing only one single file (the style sheet). [4 hrs.]

- understand the code of the web page. [4 hrs.]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies

Following planned steps to teach students the basic skills that they must learn (in the correct order) as follow:

Getting Started: Fundamentals of Web Design and Development

Learn Essential Skills: Design and programming concepts that all new web designers should learn.

Using the Best Resources: A complete list of the best resources for learning web design.

Gain Experience: How to gain hands-on web design experience and build your portfolio.

Choosing a Career Path: Decide if freelancing or working at home is right for them.

Finding a job: How to find their first web design job and start their career.

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem)

الحمل الدراسي المنتظم للطالب خلال الفصل

43

Structured SWL (h/w)

الحمل الدراسي المنتظم للطالب أسبوعيا

3

Unstructured SWL (h/sem)

الحمل الدراسي غير المنتظم للطالب خلال الفصل

32

Unstructured SWL (h/w)

الحمل الدراسي غير المنتظم للطالب أسبوعيا

2

Total SWL (h/sem)

الحمل الدراسي الكلي للطالب خلال الفصل

75

Module Evaluation

تقييم المادة الدراسية

As

Time/Number

Weight (Marks)

Week Due

Relevant Learning Outcome

Formative assessment

Quizzes

3

10% (10)

4, 6, 8, 10, 12, 14

LO # 2, 3, 8, 9, 10 and 11

Formative assessment

Assignments

4

10% (10)

2, 5, 8, 12

LO # 1-2, 4-5, 7, 11

Formative assessment

Projects / Lab.

16

10% (10)

Continuous

All

Formative assessment

Report

6

10% (10)

3, 6-8,10-12

LO#2,4and10

Summative assessment

Midterm Exam

2 hr

10% (10)

7

LO # 1-6

Summative assessment

Final Exam

3 hr

50% (50)

15

All

Total assessment

100% (100 Marks)

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week

Material Covered

Week 1

Background and Phases of Evolution of the Web.

Week 2

Web application architecture.

Week 3

Choosing a Domain Name and Hosting

Week 4

Installing WordPress and Account Setup

Week 5

WordPress Admin Dashboard and the Features

Week 6

Structure of an HTML5 document.

Week 7

CSS3 Overview.

Week 8

Process of creating a website.

Week 9	Different website types and ergonomics the website
Week 10	New Theme Installation
Week 11	Header and Landing Page Top Design
Week 12	How to Insert Logo, Site Title, and Setup Search Box on a Website
Week 13	Explanation of Post Screen Option and its Use
Week 14	Footer Design of a Website and Adding Social Media Link
Week 15	Final Exam

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	Lab 1: Step-by-Step Guide to Registering Domain Name.
Week 2	Lab 2: Structure of an HTML5 web page.
Week 3	Lab 3: How to Design the Menu Items.
Week 4	Lab 4: Simple Forms and Table Formatting.
Week 5	Lab 5: How to Add Search on the Website.
Week 6	Lab 6: How to Change Website Title and Description
Week 7	Lab 7: Steps in Adding Gallery to a Website using Gallery Widget Option.
Week 8	Lab 8: How to Place Slider on the Website.
Week 9	Lab 9: How to Publish with Post Tool.
Week 10	Lab 10: How to insert Page Break (Block) in a Post.
Week 11	Lab 11: How to Hyperlink in a Post.
Week 12	Lab 12: Inserting Image/Photo in the Post or Pages.
Week 13	Lab 13: Creating a template model.
Week 14	Lab 14: Creating a website from A to Z.
Week 15	Final Exam

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	Website Design and Development with HTML5 and CSS3. Authors: Hassen Ben Rebah, Hafedh Boukthir, and Antoine Chédebois	No
Recommended Texts	How to Build and Design a Website using wordpress, William S. Page, 2020	No
Websites		

APPENDIX:

GRADING SCHEME

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors

	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	<u>Summer Training 1</u>		Module Delivery	
Module Type	<u>Suplement</u>		Theory Lecture ✓ Lab Tutorial ✓ Practical ✓ Seminar	
Module Code	<u>CTE211</u>			
ECTS Credits	<u>2</u>			
SWL (hr/sem)	<u>50</u>			
Module Level	UGx11 2	Semester of Delivery		2
Administering Department	<u>Department of Computer Techniques Engineering</u>	College	<u>Northern Technical University Engineering Technical College/Mosul</u>	
Module Leader	All Acadimic staf	e-mail		
Module Leader's Acad. Title			Module Leader's Qualification	
Module Tutor	None		e-mail	None
Peer Reviewer Name		None	e-mail	None
Review Committee Approval		21/06/2023	Version Number 1.0	

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	١- القدرة على ربط المعرفة المكتسبة من الطالب خلال دراسته الأكاديمية بالمشاكل الحقيقية على أرض الواقع ٢- اكتشاف بيئة العمل واحتياجاتها وقيودها ٣- القدرة على تحديد المتطلبات المطلوبة لإيجاد حلول مناسبة وفعالة للمشاكل الحقيقية على أرض الواقع مع وجود قيود فنية مختلفة ٤- القدرة على تكوين رؤية واضحة حول الأهداف والمعوقات والعمل بشكل فعال ٥- إيجاد الطالب استقلاليته باكتسابه لمهارات جديدة مع إشراف بسيط من قبل جهة التدريب . ٦- القدرة على إيجاد حلول مناسبة في حال حدوث أي تغيير في متطلبات العمل وقيوده ٧- القدرة على التواصل مع الكثير من الأشخاص المتواجدين في المجال العملي . ٨- تعلم المسؤوليات الأخلاقية والاحترافية.

Module Learning Outcomes مخرجات التعلم للمادة الدراسية	- Understanding of Computer Architecture: Gain knowledge of the components and operation of computer systems, including processors, memory, input/output devices, and the organization of data. - Programming Skills: Develop proficiency in programming languages commonly used in computer engineering, such as C, C++, Java, or Python. Learn to write efficient and well-structured code. - Problem-Solving and Algorithm Design: Learn techniques for problem-solving and algorithm development. Understand various algorithmic approaches and their efficiency. Develop skills to analyze and optimize algorithms.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: - Part 1- [7 hrs] : تعريف الطالب على اقسام وشعب الموقع التدريبي - Part 2- [7 hrs] : التعرف على اجزاء القرص الصلب - Part 3- [7 hrs] : التعرف الاعطال الشائعة في الاقراص الصلبة - Part 4- [8 hrs] : انواع الطابعات الالكترونية والاعطال التي تواجه هذه الطابعات

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	ان الإستراتيجية الرئيسية التي سيتم تبنيها في تقديم هذه الوحدة: - تشجيع طلبة هندسة الحاسوب على المشاركة بشكل مهني مع الأوساط العملية من خلال الممارسة الميدانية في دوائر الدولة . - إيجاد حلول مبتكرة ريادية لسد الاحتياجات المحلية. - بناء قيادة مهنية وأخلاقية وتعاونية في مكان العمل وفي المجتمع. - تطوير معرفتهم وخبراتهم
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	20	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	5
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	30	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	7.5
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	50		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes				
	Assignments	8	15% (10)	1, 2,3,4	LO # 1, 2, 3 and 4
	Projects / Lab.	4	20% (10)	Continuous	All
	Report	2	15% (10)	2,4	LO # 2 and 4
	Midterm Exam				

Summative assessment	Final Exam	1 hr	50% (50)	5	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week	Material Covered
Week 1	- تعريف الطالب على اقسام وشعب الموقع التدريبي مع اعطاء نبذة مختصرة عن اجزاء الحاسبة وكيفية عملها وامكانية صيانة بعض اجزائها.
Week 2	- التعرف على اجزاء القرص الصلب وكيفية تقسيمه وطريقة خزن البيانات ومقارنته مع قرص SSD و M2 والطرق المستخدمة لتصفير القرص (NTFS,FAT16,FAT32)
Week 3	- التعرف الاعطال الشائعة في الاقراص الصلبة وكيفية معالجتها والبرامج المستخدمة في عملية اصلاح الاقراص الصلبة واسترجاع المعلومات المحذوفة او المفقودة بعد عملية الاصلاح
Week 4	- اعطاء نبذة مختصرة عن انواع الطابعات الالكترونية والاعطال التي تواجه هذه الطابعات وكيفية معالجتها. - التعرف على طبيعة الاجهزة المسيطر عليها حاسوبياً وكيفية عملها في حال توفرها في الموقع التدريبي
Week 5	Final Exam

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts		
Recommended Texts		
Websites		

APPENDIX:

GRADING SCHEME

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



MODULE DESCRIPTOR FORM

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	CONTROL ENGINEERING		Module Delivery	
Module Type	Core		☒ Theory ☒ Lecture ☒ Lab ☒ Tutorial ☒ Practical ☒ Seminar	
Module Code	CTE300			
ECTS Credits	6			
SWL (hr/sem)	150			
Module Level	UGx11 3	Semester of Delivery		1
Administering Department	Department of Computer Techniques Engineering		College	Northern Technical University ENGINEERING TECHNICAL COLLEGE/MOSUL
Module Leader	Ziad Saeed Mohammed		e-mail	ziadsaeed@ntu.edu.iq
Module Leader's Acad. Title	Lecturer		Module Leader's Qualification	Ph.D.
Module Tutor	None		e-mail	None
Peer Reviewer Name	None		e-mail	None
Review Committee Approval	13/06/2023		Version Number	1.0

Relation with Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Aims أهداف المادة الدراسية	53. To learn the basic definitions of a control systems and their types. 54. Methodologies and modeling for several types of control systems. 55. Learn the control system performances and their applications. 56. Learn the control system stability and their calculations. 57. Apply the Root locus analysis and Routh stability in control system. 58. Apply the frequency analysis (Bode plots). 59. Design the suitable type of controllers for improving the system performances.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	93. Learning about the automatic control systems. 94. Learning the Modelling of control systems. 95. Learning the control system stability and method of calculations. 96. Learning the system compensations techniques. 97. Ability to design, and implement different prototypes of control systems.

	98. Ability to design, and analyze the several types of controllers and for suitable applications.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: <u>Part A – presents an introduction to control systems:</u> Classify the types of control systems, deals with mathematical modeling and transfer function of control systems. Derives mathematical models of mechanical systems and electrical systems. Block diagrams representation and reduction). Control system Stability. Definitions and method of stability. Routh's- Hurwitz stability criterion is presented for stability analysis of control systems. Applications for Routh stability in control system for calculating the range of gain to ensure the system stability [16hrs.]. <u>Part B- Control system performance:</u> Introduction to system steady- state response. Definition of system types, order, and rank. Study the time response analysis, (transient response and steady-state analyses of control systems). Steady state error calculations for several system inputs and types. [16hrs.]. <u>Part C- Graphical method for obtaining stability:</u> Discusses the Root-Locus analysis. Frequency response analysis (BODE PLOTS). [13hrs] <u>Part D – Compensations:</u> Definitions and classify of compensators. Design of the controller using root locus method. [12hrs.] - Revision problem classes [3 hrs.].

Learning and Teaching Strategies استراتيجيات التعلم والتعليم	
Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.

Student Workload (SWL) الحمل الدراسي للطالب			
Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	60	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	90	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	6
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	150		

Module Evaluation تقييم المادة الدراسية				
As	Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Quizzes	2	10% (10)	5, 10	LO #1, 2, 10 and 11

Formative assessment	Assignments	2	10% (10)	2, 12	LO # 3, 4, 6 and 7
	Projects / Lab.	1	10% (10)	Continuous	All
	Report	1	10% (10)	13	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	<u>Introduction to linear control system, open and closed loop system.</u>
Week 2	<u>Mathematical modeling of physical systems and transfer functions.</u>
Week 3	<u>Mathematical modeling of D.C. Servo Motor. State space representation and analysis.</u>
Week 4	<u>Transfer function, block diagram representation and reduction diagram.</u>
Week 5	<u>Transfer function, block diagram representation and reduction diagram.</u>
Week 6	<u>Time domain analysis, steady-state transient analysis. Part-1</u>
Week 7	<u>Time domain analysis, steady-state transient analysis. Part-2</u>
Week 8	<u>Stability analysis and Routh stability criteria.</u>
Week 9	<u>Root Locus Technique. Part-1</u>
Week 10	<u>Root Locus Technique. Part-2</u>
Week 11	<u>Frequency Response analysis, phase, gain margin and bode plots. Part-1</u>
Week 12	<u>Frequency Response analysis, phase, gain margin and bode plots. Part-2</u>
Week 13	<u>Compensation, phase-lag compensation lag-lead compensation.</u>
Week 14	<u>P, PI, PD, and PID Modes of Feedback Control, Realization of PID Controller Using Active and Passive Elements.</u>
Week 15	<u>Final Exam.</u>
Delivery Plan (Weekly Lab. Syllabus) المنهاج الاسبوعي للمختبر	
Week	Material Covered
Week 1	<u>Lab1: Explanation of CKL003 Board+ D.C. motor speed control.</u>
Week 2	<u>Lab2: The linear operational amplifier as computational element.</u>
Week 3	<u>Lab 3: Dimmer light control+ Temperature control.</u>
Week 4	<u>Lab 4: Three steps control.</u>
Week 5	<u>Lab 5: Alternating current control motors using (ON-OFF) switching.</u>
Week 6	<u>Lab 6: Control on operation of two motors in sequences+ Liquid level control system.</u>
Week 7	<u>Lab 7: Transfer function part1+ Transfer function part2</u>
Week 8	<u>Lab 8: Response of second order system.</u>
Week 9	<u>Lab 9: Frequency response (Bode plots).</u>

Week 10	<u>Lab 10: Frequency response (polar plots and Nyquist plots).</u>
Week 11	<u>Lab 11: Speed control and error correction for C-L using PD controller.</u>
Week 12	<u>Lab 12: Proportional-integral controller.</u>
Week 13	<u>Lab 13: Proportional plus integral plus derivative controller or PID.</u>
Week 14	<u>Lab 14: The Control Function of Time Delay Valve 3/2 Way. (NORMALY CLOSED)</u>

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	1.Modern control engineering: By OGATA. Pearson Education, Inc. fifth edition 2010. 2. Feedback and Control System. SCHAUM'S OUTLINE. JOSEPH J. DISTEFANO, McGraw-Hill Companies, Inc.1995.	Yes(and electronic version)
Recommended Texts	Automatic Control Systems: By BENJAMIN C. KUO, JOHN WILEY & SONS, INC.9 th edition2010.	No,(only electronic version-pdf)
Websites	https://www.tutorialspoint.com/control_systems/index.htm	

APPENDIX:

GRADING SCHEME				
مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	<u>Microprocessor Supported Chips</u>		Module Delivery	
Module Type	<u>Core</u>		✓ Theory ✓ Lecture ✓ Lab ✓ Tutorial ✓ Practical ✓ Seminar	
Module Code	<u>CTE301</u>			
ECTS Credits	<u>6</u>			
SWL (hr/sem)	<u>152</u>			
Module Level	UGx11 3	Semester of Delivery		1
Administering Department	<u>Department of Computer Techniques Engineering</u>		College	<u>Northern Technical University Engineering Technical College/Mosul</u>
Module Leader	Abdullah Mohammed A. Hamdoon		e-mail	abd_comp@ntu.edu.iq
Module Leader's Acad. Title		Lecturer	Module Leader's Qualification	M.Sc.
Module Tutor	None		e-mail	None
Peer Reviewer Name		None	e-mail	None
Review Committee Approval		21/06/2023	Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	Microprocessors	Semester	1
	Computer Architecture		2
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	The objective of studying the Microprocessor Supported Chips is to enable the student to connect the processor with various digital, analog, and serial devices. The student learns vary digital methods to input/output data by interrupt or polling methods, how to write/read data on RAM using Direct Memory Access. Also, for each chip has different inputs and outputs pins, the student learns the digital circuits that needed to connect it with the processor, in addition, the student is learned how to program Supported Chips to perform the function of the design.
Module Learning Outcomes	1- The student can design interface circuit for connecting processor with varies devices (digital, analog, and serial).

مخرجات التعلم للمادة الدراسية	2- The student can read and understand datasheets manuals for differences chips and programming their registers.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: • <u>Part A – Microprocessor interfacing Circuit Design</u> It contain the methods for design interface circuit to connect LEDs , Switches to microprocessor. [2 hrs] • <u>Part B- Programmable Peripheral Interface (PPI)</u> The PPI provides a flexible parallel interface which includes features such as single bit, 4 bits, and 8 bits (Byte) wide input and output ports, buffered input, latched output, input and output port with handshaking. [4 hrs] • <u>Part C Programmable Interval Timer(PIT)</u> Programmable Interval Timer contains three independent 16 bits counters that can be programmed to operate in a variety of ways to implement timing functions. For instance, they can be set up to work as a one-shot pulse generator, square-wave generator, or rate generator. [4 hrs] • <u>Part D Programmable Interrupt Controller(PIC)</u> The 8259A programmable interrupt controller (PIC) adds eight vectored priority encoded interrupts to the microprocessor. This controller can be expanded without additional hardware to accept up to 64 interrupt request inputs. This expansion requires a master 8259A and eight 8259A slaves. [6 hrs]. • <u>Part D Programming Direct Memory Access (DMA)</u> The DMA controller is capable to perform data transfer between either two sections of memory or between memory and I/O device. This controller bypass the μp during its data transfer. [4 hrs]. • <u>Programmable Communication Interface</u> is programmable communication interface designed to connect to virtually any type of serial interface. The 16550 is a Universal Asynchronous Receiver/Transmitter (UART) that is fully compatible with Intel microprocessors, and it is capable of operating at 0-1.5 M Baud. [4 hrs]. • <u>Analog-to-Digital Conversion</u> A common low-cost ADC is the ADC0804, which is compatible with a wide range of MPSs such as Intel family. There are faster ADCs available, & some with more resolution than 8-bits, but this device is ideal for many applications that do not required a high degree of accuracy. The ADC0804 requires a 100 μs to convert an analogy input into a digital output code. [2 hrs]. • <u>Digital-to-Analog Conversion</u> The ADC0808 data acquisition component is a monolithic CMOS device with an 8-bit analog-to-digital converter, 8-channel multiplexer and microprocessor

	compatible control logic. The 8-bit A/D converter uses successive approximation as the conversion technique. [2 hrs].
	• Revision problem classes [2 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	59	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	103	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	7
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	162		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5, 10	LO #1-5 and 6-10
	Assignments	5	10% (10)	3,5,8,10,12	LO #1-3, 4-5, 6-8,9-10 and 11-12
	Projects / Lab.	14	10% (10)	Continuous	All
	Report	8	10% (10)	1,3,5,7,9,11,13	All
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الأسبوعي النظري

Week	Material Covered
Week 1	Microprocessor interfacing Circuit Design
Week 2	8255 Programmable Peripheral Interface (PPI) I
Week 3	8255 Programmable Peripheral Interface (PPI) II
Week 4	8254 Programmable Interval Timer (PIT) I
Week 5	8254 Programmable Interval Timer (PIT) II
Week 6	Microprocessor Interrupts
Week 7	8259A Programmable Interrupt Controller(PIC) I
Week 8	8259a Programmable Interrupt Controller(PIC) II
Week 9	8237 Programming Direct Memory Access (DMA) Controller I

Week 10	8237 Programming Direct Memory Access (DMA) Controller II
Week 11	16550 Programmable Communication Interface
Week 12	16550 Programmable Communication Interface Applications
Week 13	Analog-to-Digital Conversion Application ADC0804
Week 14	Digital-to-Analog Conversion Application DAC0808
Week 15	Final Exam

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الأسبوعي للمختبر

Week	Material Covered
Week 1	Lab 1: Training on Proteus Program to Make Processor work
Week 2	Lab 2: Microprocessor interfacing Circuit Design in Protues (Leds and Switches)
Week 3	Lab 3: Microprocessor interfacing Circuit Design in Protues (Seven Segment Display (SSD))
Week 4	Lab 4: Programmable Peripheral Interface (Keypad and SSD)
Week 5	Lab 5: Programmable Peripheral Interface (Application)
Week 6	Lab 6: Programmable Interval Timer
Week 7	Lab 7: Programmable Interval Timer (Application)
Week 8	Lab 8: Microprocessor Interrupts Design
Week 9	Lab 9: Programmable Interrupt Controller
Week 10	Lab 10: Programmable Communication Interface
Week 11	Lab 11: Programmable Communication Interface Application
Week 12	Lab 12: Analog-to-Digital Conversion Application (ADC0804)
Week 13	Lab 13: Digital-to-Analog Conversion Application (DAC0808)
Week 14	Lab 14: Review

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	The Intel Microprocessors 8086/8088, 80186/80188, 80286, 80386, 80486, Pentium and Pentium Pro. Processor Architecture, Programming, and Interfacing. Eighth Edition, Barry B. Brey, Copyright © 2009	No
Recommended Texts	Advanced Microprocessors and Peripherals, Third Edition, K M Bhurchandi and A K Ray, Copyright © 2013	No
Websites	Data sheets of Chips on internet	

APPENDIX:

GRADING SCHEME

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings

	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information					
معلومات المادة الدراسية					
Module Title	<u>DIGITAL SIGNAL PROCESSING</u>			Module Delivery	
Module Type	Core			✓	Theory
Module Code	CTE302			✓	Lecture
ECTS Credits	4			✓	Lab
SWL (hr/sem)	100			x	Tutorial
				✓	Practical
				✓	Seminar
Module Level	UGx11 3		Semester of Delivery	1	
Administering Department	<u>Department of Computer Techniques Engineering</u>		College	<u>Northern Technical University Engineering Technical College/Mosul</u>	
Module Leader	Mohand lokman Ahmed		e-mail	mohandaldabag@ntu.edu.iq	
Module Leader's Acad. Title	Asst.Prof.		Module Leader's Qualification	PhD.	
Module Tutor	None		e-mail	None	
Peer Reviewer Name	None		e-mail	None	
Review Committee Approval	21/06/2023		Version Number	1.0	

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	The objectives of a course on digital signal processing (DSP): 1-To provide a comprehensive treatment of the important issues in design, implementation, and application of digital signal processing algorithms. 2-To make students familiar with the most important methods in DSP, including digital filter design, transform-domain processing, and importance 3-To describe signals mathematically and understand how to perform mathematical operations on signals 4-To introduce students to basic techniques in designing and implementing digital signal processing systems 5-To discover discrete-time signals and analyze them with the Fourier transform
Module Learning Outcomes	1-Understand the fundamental concepts and principles of digital signal processing, such as sampling, quantization, and digital filtering.

مخرجات التعلم للمادة الدراسية	2-Apply mathematical tools and techniques, such as Fourier analysis, z-transforms, and digital signal processing algorithms, to analyze and process digital signals. 3-Design and implement digital signal processing systems using software tools and programming languages, and evaluate their performance through simulation and experimentation. 4-Analyze and interpret the results of digital signal processing experiments, and communicate them effectively through written reports and oral presentations. 5-Apply digital signal processing techniques to specific engineering applications, such as audio and speech processing, image and video processing, and control systems.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: - Part A – Introduction to digital signal processing Analog versus digital signals, discrete-time signals and systems, sampling and quantization, signal representation and analysis .[8 hrs] - Part B- Time-domain analysis of discrete-time signals and systems: Convolution, difference equations, impulse response and frequency response, stability and causality.[12hrs] - Part C -: Frequency-domain analysis of discrete-time signals and systems: Discrete Fourier transform (DFT), properties of DFT, fast Fourier transform (FFT), power spectral density, windowing. [12 hrs] - Part D - Digital filtering: FIR filters, IIR filters, design and analysis of filters, filter structures, filter realization, frequency transformation. [14 hrs]: - Part E – Signal processing applications: Audio and speech processing, image and video processing, control systems, telecommunications and networking, biomedical signal processing.[10 hrs] - Revision problem classes [4 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be used in digital signal processing courses is introducing the fundamental concepts of digital signal processing, such as sampling, quantization, and digital filtering, provide real-world examples: Digital signal processing has many practical applications, such as audio and image processing, communications, and control systems and provide opportunities for students to work with digital signals using simulation software or laboratory experiments. This can help students develop a deeper understanding of the concepts and gain practical experience.
------------	---

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	61	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4.06
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	39	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	2.6
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	100		

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	9,8,12	LO #,8,9,10 and 11
	Assignments	4	10% (10)	2, 12	LO # 3, 4, 6, 10
	Projects / Lab.	16	10% (10)	Continuous	All
	Report	3	10% (10)	2,4,10	LO#2,4and10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	Introduction to Digital Signal Processing(DSP) system:
Week 2	What DSP,general block diagram, classification and its properties.
Week 3	A/D and D/A conversion :
Week 4	Understand how digital to analog (D/A) and analog to digital (A/D) converters operate on a
Week 5	signal and be able to model these operations mathematically
Week 6	Discrete-Time Signal processing:
Week 7	Define simple non-periodic discrete-time sequences such as the impulse and unit step, and
Week 8	perform time shifting and time-reversal operations on such sequences
Week 9	Discrete-Time Signal Transformations:
Week 10	Time –Reversal ,Time – Scaling and Time –Shifting
Week 11	Convolution of Discrete-Time Systems:
Week 12	Properties of Discrete-time systems with convolution .Convolution with impulse response and for definite discrete-time signals
Week 13	Time/Frequency Domain Representation of Signals:
Week 14	Perform time, frequency and Z-transform analysis on signals.
Week 15	Discrete-Time Fourier Transform :
Week 16	Final Exam

Delivery Plan (Weekly Lab. Syllabus) المنهاج الاسبوعي للمختبر	
Week	Material Covered
Week 1	Lab 1: Represent basic signals like: Unit Impulse, Ramp, Unit Step, Exponential.
Week 2	Lab 2:. Generate discrete sine and cosine signals with given sampling frequency
Week 3	Lab3:Illustrate the Nyquist sampling theorem
Week 4	Lab 4: Represent complex exponential as a function of real and imaginary part.
Week 5	Lab 5: Determine impulse and step response of two vectors.
Week 6	Lab 6: Perform convolution between two vectors .
Week 7	Lab7: Determine rational z-transform from the given poles and zeros .
Week 8	Lab 8: Compute DFT and IDFT of a given sequence .

Week 9	Lab 9: Compute the DFT of a sequence $x(n)$ using DIT and DIF algorithm.
Week 10	Lab 10: Perform linear convolution of two sequence using DFT .
Week 11	Lab 11: Design Band pass and Band reject FIR linear phase filter using Hamming and Hanning windows
Week 12	Lab 12: Design a Type 1 Chebyshev IIR highpass filter.
Week 13	Lab 13: Design an IIR Elliptic low pass filter.
Week 14	Lab14: design an IIR Butterworth bandpass filter
Week 15	Lab 15: To study coefficient quantization effects on the frequency response of a cascade form IIR filter .
Week 16	Final Exam

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	Digital Signal Processing 4th Edition by John Proakis (Author), Dimitris Manolakis (Author)	YES
Recommended Texts	Discrete-Time Signal Processing (Prentice-Hall Signal Processing Series) 3rd Edition by Alan Oppenheim (Author), Ronald Schafer (Author)	No
Websites		

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	<u>engineering analysis – Computer Communication and networking</u>		Module Delivery
Module Type	Core		✓ Theory ✓ Lecture ✓ Lab ✓ Tutorial ✓ Practical ✓ Seminar
Module Code	CTE303		
ECTS Credits	3		
SWL (hr/sem)	75		
Module Level	UGx11 3	Semester of Delivery	1
Administering Department	Department OF COMPUTER Techniques Engineering		College Northern Technical University Engineering Technical College/Mosul
Module Leader	Orjuwan M. Al-Jawadi		e-mail Arjuwan_m@ntu.edu.iq
Module Leader's Acad. Title	Lecturer		Module Leader's Qualification M.Sc.
Module Tutor	None		e-mail None
Peer Reviewer Name	None		e-mail None
Review Committee Approval	21/06/2023		Version Number 1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents		
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية		
Module Objectives أهداف المادة الدراسية	60.	To develop problem solving skills and understanding of engineering analysis mathematic concept.
	61.	To understand Laplace Transform, Z-transform and their applications.
	62.	This course deals with the basic concept of Mathematical Engineering.
	63.	This is the basic subject for solving complicated problems using engineering transforms.
	64.	To understand the concept of identity system in complex frequencies, as in S-domain and Z-domain.
	65.	To perform integration in complex plane
Module Learning Outcomes	99.	Recognize how Engineering mathematic can be applied.
	100.	Learn Laplace transform and its inverse.
	101.	Learn Z-Transform and its inverse.

مخرجات التعلم للمادة الدراسية	102. Probability: Basic terminology, probability and set notation, law of probability, independent events 103. Statistics: Graphical representation, and measurements
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: <u>Part A – Laplace Transform and Z-Transform</u> Laplace Transform definition, properties and its applications to solve integrations, mathematical problems, differential equations and electrical circuits and transform them between continuous-time and complex frequency in S-domain, Z-Transform definition, properties and its applications to solve mathematical problems, differential equations and transform them between discrete-time and complex frequency in Z-domain. [18hrs] <u>Part B- probability and Statics</u> Basic Terminology, set notation, law of probability independent events Statics with Graphical representation, measure of central tendency, measure of dispersion [14 hrs] <u>Part C Numerical Computations</u> Bisection method, false position method , Newton-Raphson method: solution of algebraic equations, Newton-Raphson method: transcendental equations [16 hrs] <u>Part D Solution of Linear Simultaneous Equations [12 hrs]</u> <u>Solution of linear simultaneous equations: 1) Direct methods (Gauss elimination, Gauss Jordan), 2) Iterative method (Jacobi's, Gauss-seidel iteration)</u> Numerical solution of ordinary differential equation (Picard's , Euler's method) NUMERICAL SOLUTION OF ORDINARY DIFFERENTIAL EQUATION (PICARD'S , EULER'S METHOD)

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.
------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	60	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	15	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	1
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	75		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	2	10% (10)	2, 12	LO # 3, 4, 6 and 7
	Projects / Lab.	28	10% (10)	Continuous	All

	Report	1	10% (10)	13	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week	Material Covered
Week 1	<u>Continues Unit Step Function and Impulse Function Definition</u>
Week 2	<u>Laplace transform, Properties, theorems and applications</u>
Week 3	<u>Laplace Inverse Transform, Properties, theorems and applications</u>
Week 4	<u>Z-transform, properties, theorems, and applications</u>
Week 5	<u>Z- Inverse Transform, properties, theorems, and applications</u>
Week 6	<u>Probability: Basic terminology, probability and set notation</u>
Week 7	<u>Probability: law of probability, independent events</u>
Week 8	<u>Statistics: Graphical representation, measure of central tendency</u>
Week 9	<u>Statistics: measure of dispersion</u>
Week 10	<u>Numerical computations: bisection method, false position method</u>
Week 11	<u>Newton-Raphson method: solution of algebraic equations, Newton-Raphson method: transcendental equations</u>
Week 12	<u>Solution of linear simultaneous equations: 1) Direct methods (Gauss elimination, Gauss Jordan)</u> <u>2) Iterative method (Jacobi's, Gauss-seidel iteration)</u>
Week 13	<u>Numerical solution of ordinary differential equation (Picard's , Euler's method)</u>
Week 14	<u>Solution of nonlinear equation (Newton-Raphson method)</u>
Week 15	Final Exam

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	<u>Lab 1: Introduction to Unit Step Function and Impulse Function</u>
Week 2	<u>Lab 2: Plotting and Control Flow in MATLAB</u>
Week 3	<u>Lab 3: Laplace Transform Definition</u>
Week 4	<u>Lab 4: Laplace Transform Properties</u>
Week 5	<u>Lab 5: Inverse Laplace Transform</u>
Week 6	<u>Lab 6: Inverse Laplace Transform by Partial Fractions</u>
Week 7	<u>Lab 7: Solving Complex Electrical Circuits using Laplace Transform</u>
Week 8	<u>Lab 8: Z-Transform</u>
Week 9	<u>Lab 9: Z-Transform Properties</u>
Week 10	<u>Lab 10: Inverse of Z-Transform</u>
Week 11	<u>Lab 11: Solution of Special DE function using Simulink</u>
Week 12	<u>Lab 12: System Stability using Z-Transform</u>
Week 13	<u>Lab 13: Newton –Raphson Method</u>

Week 14	Lab 14: Taylor Series in MATLAB
----------------	--

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	SIGNALS, SYSTEMS AND TRANSFORMS, 4 th EDITION, CHARLES L. PHILIPS, JHON M. PARR	Yes
Recommended Texts	ADVANCED ENGINEERING MATHEMATICS, ERWIN KREYSZIG, 9 th	No
Websites	http://www.wiley.com/college/kreyszig	

APPENDIX:

APPENDIX

GRADING SCHEME				
مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Ministry of Higher Education and
Scientific Research - Iraq
Northern Technical University
Engineering Technical College/Mosul
Department of Computer Techniques Engineering



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information معلومات المادة الدراسية			
Module Title	<u>Fundamentals of Digital Communication</u>	Module Delivery	
Module Type	<u>Core</u>	✓	Theory
Module Code	<u>CTE304</u>	✓	Lecture
ECTS Credits	<u>5</u>	✓	Lab
SWL (hr/sem)	<u>125</u>	✓	Tutorial
		✓	Practical
		✓	Seminar
Module Level	UGx11 3	Semester of Delivery	1
Administering Department	<u>Department of Computer Techniques Engineering</u>	College	<u>Northern Technical University Engineering Technical College/Mosul</u>
Module Leader	Hakam Marwan Zaidan	e-mail	Hakam_m_zaidan@ntu.edu.iq
Module Leader's Acad. Title	<u>Assistant Lecturer</u>	Module Leader's Qualification	<u>MSc.</u>
Module Tutor	None	e-mail	None
Peer Reviewer Name	None	e-mail	None
Review Committee Approval	21/06/2023	Version Number	1.0

Relation With Other Modules العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes, and Indicative Contents أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Aims أهداف المادة الدراسية	<u>To introduce students to the fundamental concepts, principles, and techniques underlying digital communication systems. The course aims to provide a solid foundation in understanding various components of digital communication, including modulation techniques, channel coding, multiplexing, and error detection and correction. By the end of the course, students should be able to analyze and design digital communication systems, evaluate their performance in different channel conditions, and make informed decisions regarding the selection of appropriate modulation and coding schemes. The course also emphasizes the practical aspects of digital communication through hands-on experiments and</u>

	<u>simulations. Overall, the course aims to equip students with a comprehensive understanding of digital communication principles and the skills necessary for the design and implementation of efficient and reliable digital communication systems.</u>
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	• UNDERSTAND THE BASIC PRINCIPLES AND CONCEPTS OF DIGITAL COMMUNICATION SYSTEMS. • GAIN KNOWLEDGE OF VARIOUS MODULATION TECHNIQUES USED IN DIGITAL COMMUNICATION, SUCH AS AMPLITUDE SHIFT KEYING (ASK), FREQUENCY SHIFT KEYING (FSK), AND PHASE SHIFT KEYING (PSK). • DEVELOP AN UNDERSTANDING OF DIGITAL MODULATION SCHEMES, INCLUDING QUADRATURE AMPLITUDE MODULATION (QAM) AND ORTHOGONAL FREQUENCY DIVISION MULTIPLEXING (OFDM). • LEARN ABOUT ERROR DETECTION AND CORRECTION CODES, SUCH AS PARITY CHECK, HAMMING CODES, AND CYCLIC REDUNDANCY CHECK (CRC). • ACQUIRE KNOWLEDGE OF DIFFERENT TYPES OF CHANNEL CODING, INCLUDING CONVOLUTIONAL CODES AND REED-SOLOMON CODES. • UNDERSTAND THE PRINCIPLES AND OPERATION OF DIGITAL COMMUNICATION SYSTEMS, INCLUDING DEMODULATION, EQUALIZATION, AND SYNCHRONIZATION. • EXPLORE THE CONCEPTS OF MULTIPLE ACCESS TECHNIQUES, SUCH AS TIME-DIVISION MULTIPLE ACCESS (TDMA) AND CODE-DIVISION MULTIPLE ACCESS (CDMA). • GAIN PRACTICAL SKILLS IN DESIGNING, ANALYZING, AND EVALUATING DIGITAL COMMUNICATION SYSTEMS THROUGH LABORATORY EXPERIMENTS AND PROJECTS. • UNDERSTAND THE EFFECTS OF NOISE, FADING, AND INTER-SYMBOL INTERFERENCE ON DIGITAL COMMUNICATION SYSTEM PERFORMANCE. • LEARN ABOUT CHANNEL CAPACITY AND INFORMATION THEORY, INCLUDING THE CONCEPTS OF ENTROPY, CHANNEL CODING THEOREM, AND CAPACITY-ACHIEVING CODES. • DEVELOP AN UNDERSTANDING OF SOURCE CODING TECHNIQUES, SUCH AS HUFFMAN CODING AND ARITHMETIC CODING, FOR EFFICIENT DATA COMPRESSION. • EXPLORE EMERGING TRENDS AND TECHNOLOGIES IN DIGITAL COMMUNICATION, SUCH AS SOFTWARE-DEFINED RADIOS (SDR) AND COGNITIVE RADIO. • APPLY THEORETICAL KNOWLEDGE TO SOLVE PRACTICAL PROBLEMS AND ANALYZE THE PERFORMANCE OF DIGITAL COMMUNICATION SYSTEMS USING MATHEMATICAL MODELING AND SIMULATION TOOLS.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: <u>Part 1 – General Introduction [16 hrs]</u> Introduction to the digital communication. <u>Part 2 – digital modulation [16 hrs]</u> <u>Part 3 – error detection and correction [16 hrs]</u> <u>Part 4 – coding techniques [8 hrs]</u> <u>Revision [4 hrs]</u>
Learning and Teaching Strategies استراتيجيات التعلم والتعليم	
Strategies	The main strategy that will be adopted in delivering this module is to encourage student's participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials, and by considering simple experiments involving interesting sampling activities for the students.

Student Workload (SWL) الحمل الدراسي للطالب			
Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	63	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4.2
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	62	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4.1
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	3, 7, 11, 14	LO #2, 4, 10 and 11
	Assignments	4	10% (10)	3, 7, 9, 14	LO # 3, 5, 6 and 7
	Projects / Lab.	14	10% (10)	Continuous	All
	Report	5	10% (10)	5, 6, 8, 10, 13	LO # 5, 8 and 12
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	15	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الأسبوعي النظري	
Week	Material Covered
Week 1	Introduction to Digital Communication: Overview of digital communication systems, Analog vs. digital communication, Elements of a digital communication system
Week 2	Signals and Systems: Signal representation and manipulation, Fourier analysis and frequency domain representation Time and frequency domain characteristics of signals.
Week 3	Modulation Techniques: - Introduction to digital modulation techniques (ASK, FSK, PSK), Performance metrics: modulation index, bandwidth, power efficiency
Week 4	Pulse Amplitude Modulation (PAM): - Introduction to PAM and its applications, PAM waveform generation and detection, Performance analysis of PAM in the presence of noise.
Week 5	Pulse Code Modulation (PCM): Sampling theorem and Nyquist criterion, Quantization and coding of PCM signals, Quantization noise and signal-to-noise ratio (SNR).
Week 6	Baseband Transmission: - Introduction to bandpass transmission, Analog and digital modulation schemes, Coherent and non-coherent detection techniques
Week 7	Bandpass Transmission: - Introduction to bandpass transmission, Analog and digital modulation schemes, Coherent and non-coherent detection techniques
Week 8	Error Detection and Correction:

	Introduction to error detection and correction codes, Parity check, Hamming codes, and cyclic redundancy check (CRC), Bit error rate (BER) and its calculation
Week 9	<u>Channel Capacity and Coding:</u> <u>Shannon's channel capacity theorem, Channel capacity limits for various communication channels, Channel coding techniques (Block codes, Convolutional codes)</u>
Week 10	<u>Multiple Access Techniques</u>
Week 11	<u>Equalization and Diversity</u>
Week 12	<u>Spread Spectrum Techniques</u>
Week 13	<u>Wireless Communication Systems</u>
Week 14	<u>Introduction to Digital Modulation</u>
Week 15	<u>Final Exam.</u>

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	<u>Lab 1: Introduction to MATLAB/Simulink</u>
Week 2	<u>Lab 2: Signal Analysis and Fourier Transform</u>
Week 3	<u>Lab 3: Digital Modulation Techniques</u>
Week 4	<u>Lab 4: Pulse Amplitude Modulation (PAM)</u>
Week 5	<u>Lab 5: Pulse Code Modulation (PCM)</u>
Week 6	<u>Lab 6: Line Coding Techniques</u>
Week 7	<u>Lab 7: Baseband Transmission</u>
Week 8	<u>Lab 8: Analog and Digital Modulation</u>
Week 9	<u>Lab 9: Error Detection and Correction</u>
Week 10	<u>Lab 10: Channel Capacity and Coding</u>
Week 11	<u>Lab 11: Multiple Access Techniques</u>
Week 12	<u>Lab 12: Equalization and Diversity</u>
Week 13	<u>Lab 13: Spread Spectrum Techniques</u>
Week 14	<u>Lab 14: Review</u>

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	"DIGITAL COMMUNICATIONS" BY JOHN G. PROAKIS AND MASOUD SALEHI.	No
Recommended Texts	"PRINCIPLES OF DIGITAL COMMUNICATION" BY ROBERT G. GALLAGER	No
Websites		

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Ministry of Higher Education and
Scientific Research - Iraq
Northern Technical University
Engineering Technical College/Mosul
Department of Computer Technology Engineering



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	<u>Computer Networks Fundamentals</u>		Module Delivery	
Module Type	<u>Core</u>		✓ Theory ✓ Lecture ✓ Lab ✓ Tutorial ✓ Practical ✓ Seminar	
Module Code	<u>CTE305-S1</u>			
ECTS Credits	<u>6</u>			
SWL (hr/sem)	<u>150</u>			
Module Level	UGX11 3	Semester of Delivery		1
Administering Department	<u>Department of Computer Techniques Engineering</u>		College	<u>Northern Technical University Engineering Technical College/Mosul</u>
Module Leader	Dr. Ziyad Khalaf Farej		e-mail	drziyad.farej@ntu.edu.iq
Module Leader's Acad. Title		Asst. Prof.	Module Leader's Qualification Ph.D.	
Module Tutor	None		e-mail	None
Peer Reviewer Name		None	e-mail	None
Review Committee Approval		2/06/2023	Version Number	1.0

Relation with Other Modules

العلاقة مع المواد الدراسية الأخرى

Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents

أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

Module Objectives أهداف المادة الدراسية	66. To understand network architecture: Learning the different components and layers of computer networks. 67. To analyze network protocols: Studying the various protocols used in computer networks. 68. To exploring network security: Understanding the concepts and techniques related to network security. 69. To investigating network technologies: Exploring various network technologies, such as LANs, WANs, wireless networks. 70. To understanding network services and applications: Studying network services and applications. 71. To enhancing network design and implementation skills: Developing skills in designing and implementing computer networks
---	--

	72. To examine network management: Learning about network management principles, tools, and techniques.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	104. Understanding Network Concepts 105. Differentiate between LAN (Local Area Network), WAN (Wide Area Network), and MAN (Metropolitan Area Network). 106. Explain the purpose and components of the OSI model and the TCP/IP model 107. Understand the hierarchical structure of the layers in both models 108. Explain the different types of transmission media, encoding, modulation and multiplexing techniques used for data transmission 109. Analyzing Network Communication 110. Compare and contrast different network models, such as the TCP/IP model and the OSI model. 111. Applying Network Protocols: understanding how protocols like TCP/IP enable reliable data transmission and how they are used in real-world scenarios. 112. Understanding the functions and responsibilities of the data link layer in the OSI model. 113. Identify and apply error detection mechanisms, such as checksums or cyclic redundancy checks (CRC).
Indicative Contents المحتويات الإرشادية	- Introduction to Computer Networks: [12 h] - Definition of computer networks - Network architectures and types (LAN, WAN, MAN) - Network components and their functions - Network protocols and standards - Network Models and Protocols: [14 h] - OSI (Open Systems Interconnection) model and TCP/IP model - Layers of the OSI and TCP/IP models - Protocols and services at each layer - Encapsulation and de-encapsulation processes - Physical Layer: [20 h] - Transmission media (copper, fiber-optic, wireless) - Data transmission techniques (analog vs. digital, modulation, multiplexing) - Signal encoding (NRZ, Manchester, etc.) - Switching (circuit, datagram and virtual). - Data Link Layer: [12 h] - Framing and error detection - Flow control and error control mechanisms - Media Access Control (MAC) protocols (Ethernet, Wi-Fi) - Local Area Networks (LAN) and Ethernet technologies
Learning and Teaching Strategies استراتيجيات التعلم والتعليم	
Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. When studying Computer Networks Fundamentals, there are several key strategies that can help you grasp

	the material effectively. These strategies include (Understanding the Fundamentals, Layered Approach, Analyze Network Protocols, Network Simulations: Visualize Networks and Practical Problem Solving. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.
--	---

Student Workload (SWL) الحمل الدراسي للطالب			
Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	58	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعيا	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	92	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعيا	6.5
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	150		

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	8	10% (10)	2, 12	LO # 3, 4, 6 and 7
	Projects / Lab.	15	10% (10)	Continuous	All
	Report	5	10% (10)	13	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الأسبوعي النظري	
Week	Material Covered
Week 1	Introduction to data communication and networks: <u>Data Representations, Data Flows, and classify the computer networks according to application, size, transmission technology</u>
Week 2	Network Models: <u>Protocol Layering, The ISO reference Model, and TCP/IP Reference Model</u>
Week 3	Connection-Oriented Versus Connectionless Service, and Service Primitives
Week 4	LANs Topologies: <u>CSMA/CD, Token Access protocols, and IP addressing</u>
Week 5	Metropolitan Area Networks, Wide Area Networks, Internetworks, and VPNs
Week 6	<u>performance metrics, Bandwidth, Throughput, Latency (Delay), Bandwidth-Delay, Jitter</u>
Week 7	Digital signals transmission: <u>impairment (attenuation, distortion, noise, data rate limits)</u> <u>Channel capacity and Shannon Formula</u>
Week 8	<u>Bandwidth-Limited Signals, The Maximum Data Rate of a Channel</u>
Week 9	<u>Guided transmission media (twisted-pair cable, coaxial cable, fiber-optic cable), and wireless transmission, transmission modes, Parallel and Serial Transmissions</u>
Week 10	<u>Digital Signals and Digital Transmission:</u> <u>Line Coding Baseband, Passband,</u>

Week 11	<u>Multiplexing and Demultiplexing:</u> <u>FDM, TDM, and CDM</u>
Week 12	<u>Public Switched Telephone Network:</u> <u>Structure of the Telephone System, DSL Trunks and Multiplexing</u>
Week 13	<u>Switching:</u> <u>Circuit and Datagram Networks, Virtual-Circuit Networks</u> <u>Circuit switching, packet switching & virtual switching</u>
Week 14	<u>Wired LANs:</u> <u>Ethernet Standards, Bridged Ethernet, Switched Ethernet, Fast Ethernet And Gigabit Ethernet</u>
Week 15	<u>Final Exam.</u>

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الأسبوعي للمختبر

Week	Material Covered
Week 1	Lab 1: Introduction to Network Lab
Week 2	Lab 2: Network Transmission media
Week 3	Lab 3: Cables and LAN tester
Week 4	Lab 4: Network Devices 1
Week 5	Lab 5: Network Devices 2
Week 6	Lab 6: Peer-to-peer Network
Week 7	Lab 7: Building LAN Network using Hub 1
Week 8	Lab 8: Building LAN Network using Hub 2
Week 9	Lab 9: Network Tools
Week 10	Lab 10: Network commands 1
Week 11	Lab 11: Network commands 2
Week 12	Lab 12: Introduction to Internet Protocol (IP)
Week 13	Lab 13: IP addressing 1
Week 14	Lab 14: IP addressing 2

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	Behrouz A. Forouzan. Data communication and Networking, fifth edition	Yes
Recommended Texts	Michael Duck and Richard Read" Communications and Computer Networks, 2 nd edition, Pearson Education 2003	No
Websites	Digital Systems: From Logic Gates to Processors: https://www.coursera.org/learn/computes-networks	

APPENDIX:

GRADING SCHEME

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors

	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Ministry of Higher Education and
Scientific Research - Iraq
Northern Technical University
Engineering Technical College/Mosul
Department of Computer Technology Engineering



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information					
معلومات المادة الدراسية					
Module Title	<u>Controllers</u>			Module Delivery	
Module Type	<u>Supplement</u>			✓ Theory ✓ Lecture ✓ Lab ✓ Tutorial ✓ Practical ✓ Seminar	
Module Code	<u>CTE306</u>				
ECTS Credits	<u>4</u>				
SWL (hr/sem)	<u>102</u>				
Module Level	UGx11 3		Semester of Delivery		2
Administering Department	<u>Department of Computer Technology Engineering</u>		College	<u>Northern Technical University Engineering Technical College/Mosul</u>	
Module Leader	Zaid G. Mohammed		e-mail	zaid_gh@ntu.edu.iq	
Module Leader's Acad. Title		Lecturer	Module Leader's Qualification		M.Sc.
Module Tutor	None		e-mail	None	
Peer Reviewer Name		None	e-mail	None	
Review Committee Approval		14/06/2023	Version Number		1.0

Relation With Other Modules العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	Introduce the student to learn the architecture of a PIC Microcontroller and get acquainted with their use for control purposes. In addition, it will provide the knowledge of applications and interfacing of microcontrollers used in the field of instrumentation & control. Thus, this course is very useful for instrumentation engineers working in the area of embedded systems.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	114. Understand the architecture and features of PIC microcontrollers. 115. Program PIC microcontrollers using appropriate programming languages. 116. Interface and control external devices using PIC microcontrollers. 117. Design and implement practical projects utilizing PIC microcontrollers. 118. Understand and implement interrupt handling in PIC microcontrollers. 119. Optimize PIC microcontroller programs for performance and efficiency.

	120.	Debug and troubleshoot PIC microcontroller-based systems.
	121.	Apply knowledge of PIC microcontrollers to solve specific engineering problems.
Indicative Contents المحتويات الإرشادية	10.	Introduction to PIC Microcontrollers: [2 hr]
	a.	Overview of microcontrollers and their applications
	b.	Introduction to PIC microcontrollers and their features
	c.	Comparison with other microcontroller families
	11.	PIC Microcontroller Architecture: [2 hr]
	a.	CPU architecture and instruction set overview
	b.	Memory organization (Flash, RAM, EEPROM)
	c.	Special Function Registers (SFRs) and their functionalities
	d.	Clock and reset systems
	12.	Development Tools and Environment: [4 hr]
	a.	Introduction to development boards and programmers
	b.	Integrated Development Environments (IDEs) for PIC microcontrollers
	c.	Compiler options and configurations
	d.	Debugging tools and techniques
	13.	Programming PIC Microcontrollers: [4 hr]
	a.	Overview of programming languages (C, Assembly) for PIC microcontrollers
	b.	Writing and compiling PIC microcontroller programs
	c.	Uploading programs to the microcontroller
	d.	Debugging and troubleshooting code
	14.	PIC Microcontroller Peripherals: [8 hr]
	a.	GPIO (General-Purpose Input/Output) ports and their usage
	b.	Timers and counters for timing and synchronization
	c.	Analog-to-Digital Conversion (ADC) for reading analog signals
	d.	Serial communication interfaces (UART, SPI, I2C)
	e.	PWM (Pulse Width Modulation) for generating analog signals
	15.	Interrupts and Interrupt Handling: [4 hr]
	a.	Introduction to interrupts and their importance
	b.	Configuring and enabling interrupts in PIC microcontrollers
	c.	Writing interrupt service routines (ISRs)
	d.	Prioritizing and handling multiple interrupts
	16.	Advanced Topics: [8 hr]
	a.	Power management techniques and low-power modes
	b.	External memory interfacing (SRAM, EEPROM, external Flash)
	c.	Implementing communication protocols (CAN, USB, Ethernet)
	d.	Real-time operating systems (RTOS) for PIC microcontrollers
	e.	Using peripheral libraries and frameworks
	17.	PIC Microcontroller Projects and Applications: [10 hr]
	a.	Building basic electronic projects using PIC microcontrollers
	b.	Sensor interfacing and data acquisition applications
	c.	Motor control and robotics projects
	d.	Human-Machine Interface (HMI) applications

	e. IoT (Internet of Things) projects with PIC microcontrollers				
Learning and Teaching Strategies					
استراتيجيات التعلم والتعليم					
Strategies		The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.			
Student Workload (SWL)					
الحمل الدراسي للطالب					
Structured SWL (h/sem)		61	Structured SWL (h/w)	4	
الحمل الدراسي المنتظم للطالب خلال الفصل			الحمل الدراسي المنتظم للطالب أسبوعيا		
Unstructured SWL (h/sem)		41	Unstructured SWL (h/w)	2.7	
الحمل الدراسي غير المنتظم للطالب خلال الفصل			الحمل الدراسي غير المنتظم للطالب أسبوعيا		
Total SWL (h/sem)		102			
الحمل الدراسي الكلي للطالب خلال الفصل					
Module Evaluation					
تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	2	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	2	10% (10)	2, 12	LO # 3, 4, 6 and 7
	Projects / Lab.	8	10% (10)	Continuous	All
	Report	6	10% (10)	13	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		
Delivery Plan (Weekly Syllabus)					
المنهاج الاسبوعي النظري					
Week	Material Covered				
Week 1	Introduction to Microcontroller, Types of Microcontrollers, difference between Microprocessor and Microcontroller.				
Week 2	PIC Microcontroller Architecture and PIC Family.				
Week 3	Microcontroller Programming Model and Its Instruction Set				
Week 4	Interface with Microcontroller Part1 (Push buttons, Switches and 7segment)				
Week 5	Interface with Microcontroller Part2 (LCD, GLCD, Relay and Keypad)				
Week 6	Interrupt				
Week 7	A/D (Analog to Digital interface)				
Week 8	Timers (Timer0 and Timer1)				
Week 9	Timers (Timer2 and WDT)				
Week 10	CCP (Capture and Compare)				
Week 11	CCP (PWM)				
Week 12	Memory				

Week 13	Communication (UART, SPI, I2C)
Week 14	Application and projects of Microcontroller
Week 15	Final Exam

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الأسبوعي للمختبر

Week	Material Covered
Week 1	Lab 1: Introduction to MikroC Platform (LED Blinking)
Week 2	Lab 2: Introduction to Proteus Platform (LED Blinking)
Week 3	Lab 3: Switches and Bush button Interface
Week 4	Lab 4: Interface to 7Segment
Week 5	Lab 5: Interface to LCD
Week 6	Lab 6: Interrupt
Week 7	Lab 7: Analog to Digital
Week 8	Lab 8: Interface to keypad
Week 9	Lab 9: Relay Interface
Week 10	Lab 10: Timers (Timer0 and Timer1)
Week 11	Lab 11: CCP (Capture and Compare)
Week 12	Lab 12: CCP (PWM)
Week 13	Lab 13: Communication (UART, SPI, I2C)
Week 14	Lab 14: Review

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	1. Microcontroller Fundamentals and Applications with PIC, 2009. 2. Embedded Systems Programming with PIC 16F877A, Timothy D. Green.	No
Recommended Texts	3. PIC Projects: A Practical Approach by H. Parchizadeh and B. Vuksanovic. Wiley 2009. 4. Designing Embedded Systems with PIC Microcontrollers, 2nd Edition: Principles and Applications, 2nd edition 2009.	No
Websites	https://www.coursera.org , https://www.udemy.com	

APPENDIX:

GRADING SCHEME

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors

Fail Group (0 – 49)	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Ministry of Higher Education and
Scientific Research - Iraq
Northern Technical University
Engineering Technical College/Mosul
Department of Computer Techniques Engineering



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information معلومات المادة الدراسية			
Module Title	<u>Operating Systems</u>	Module Delivery	
Module Type	<u>Core</u>	☒ Theory ☒ Lecture ☒ Lab ☒ Tutorial ☒ Practical ☒ Seminar	
Module Code	<u>CTE307</u>		
ECTS Credits	<u>5</u>		
SWL (hr/sem)	<u>125</u>		
Module Level	UGx11 3	Semester of Delivery	2
Administering Department	<u>Department of Computer Techniques Engineering</u>	College	<u>Northern Technical University Engineering Technical College/Mosul</u>
Module Leader	dr. Younis Anas Younis	e-mail	younis.alrozz@ntu.edu.iq
Module Leader's Acad. Title	<u>Lecturer</u>	Module Leader's Qualification	<u>PhD.</u>
Module Tutor	None	e-mail	None
Peer Reviewer Name	None	e-mail	None
Review Committee Approval	21/06/2023	Version Number	1.0

Relation With Other Modules العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes, and Indicative Contents أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Aims أهداف المادة الدراسية	Teaches students about the design and implementation of operating systems. The major components of most operating systems, include process management, memory management, and file systems. Students will learn about the tradeoffs between performance and functionality during the design and implementation of an operating system. The course may also cover the historical evolution of operating systems over the last fifty years.

Module Learning Outcomes مخرجات التعلم للمادة الدراسية	1. Grasping the basic concepts of operating systems together with the modules needed to manage the different computer resources. 2. Grasping implementations of operating systems in reasonable detail together with hands-on experience.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: Part 1 – General Introduction [16 hrs] Introduction to the operating system, Types of operating systems. Part 2 – Process management [16 hrs] Process management and scheduling, Processes, and Threads (address spaces, system calls, scheduling), Threads and concurrency. Part 3 – Resource management [16 hrs] Memory management, File systems, and storage management, Input/Output (I/O) management. Part 4 – Virtualization and Cloud computing [8 hrs] Revision [4 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage student's participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials, and by considering simple experiments involving interesting sampling activities for the students.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	63	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4.2
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	62	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4.1
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	3, 7, 11, 14	LO #2, 4, 10 and 11
	Assignments	4	10% (10)	3, 7, 9, 14	LO # 3, 5, 6 and 7
	Projects / Lab.	14	10% (10)	Continuous	All
	Report	5	10% (10)	5, 6, 8, 10, 13	LO # 5, 8 and 12
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	15	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week	Material Covered
Week 1	Introduction to operating system
Week 2	Types of operating systems (Windows, Linux, MacOS, Unix)
Week 3	Process management and scheduling
Week 4	Processes and Threads (address spaces, system calls, scheduling)
Week 5	Threads and concurrency
Week 6	Memory management
Week 7	File systems and storage management
Week 8	Input/Output (I/O) management
Week 9	Synchronization (algorithms and structures like locks, semaphores, and monitors)
Week 10	Virtual Memory (paging, page tables, eviction, segmentation)
Week 11	File Systems (the file abstraction, directory structures)
Week 12	File Systems (disk I/O)
Week 13	Virtualization and cloud computing
Week 14	Review
Week 15	Final Exam

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	Lab1: Getting Started.
Week 2	Lab2: Process Scheduling Simulation
Week 3	Lab 3: Building a Simple Operating System - bootstrapping
Week 4	Lab 4: Building a Simple Operating System - memory management
Week 5	Lab 5: Building a Simple Operating System - process management
Week 6	Lab 6: File System Implementation - FAT or EXT.
Week 7	Lab 7: File System Implementation – EXT.
Week 8	Lab 8: Virtual Memory Simulation - virtual memory management - page replacement algorithms.
Week 9	Lab 9: Kernel Debugging -Linux
Week 10	Lab 10: Kernel Debugging - Windows
Week 11	Lab 11: Concurrency Control - synchronization primitives, semaphores
Week 12	Lab 12: Concurrency Control - synchronization primitives, monitors
Week 13	Lab 13: Device Driver Development - keyboard or mouse driver
Week 14	Lab 14: Review

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	Operating System Concepts, Tenth Edition by Avi Silberschatz, Peter Baer Galvin, Greg Gagne	No

	John Wiley & Sons, Inc.	
Recommended Texts	Modern Operating Systems, 4th Edition by Andrew Tanenbaum, Herbert Bos Pearson; 4th edition (March 10, 2014)	No
Websites	You can visit the course page at the following link: https://youtube.com/playlist?list=PL6KMWPQP_DM-7tMNjUa7X2zGrc8jipPel	

APPENDIX:

GRADING SCHEME				
مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	<u>Signals and systems</u>		Module Delivery	
Module Type	<u>Core</u>		✓ Theory ✓ Lecture ✓ Lab x Tutorial ✓ Practical ✓ Seminar	
Module Code	<u>CTE308</u>			
ECTS Credits	4			
SWL (hr/sem)	<u>100</u>			
Module Level	UGx11 3		Semester of Delivery	1
Administering Department	<u>Department of Computer Techniques Engineering</u>		College	<u>Northern Technical University Engineering Technical College/Mosul</u>
Module Leader	Mohand lokman Ahmed		e-mail	mohandaldabag@ntu.edu.iq
Module Leader's Acad. Title	<u>Asst.Prof.</u>		Module Leader's Qualification	<u>PhD.</u>
Module Tutor	None		e-mail	None
Peer Reviewer Name	None		e-mail	None
Review Committee Approval	21/06/2023		Version Number	1.0

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	The main objectives of studying signals and systems are: 1-To understand the nature, properties, and representation of signals. 2-To analyze and design linear time-invariant systems , learn about linear time-invariant systems, which are widely used in engineering applications, and how to analyze their behavior and design them to meet specific requirements. 3-To understand the effects of system components on signals, behavior of a system is determined by its components and how they can be used to shape and manipulate signals for specific applications. 4-To study the frequency domain representation of signals and systems 5-To apply the concepts of signals and systems to practical engineering applications, in this module, you will learn about some practical applications of signals and systems and how to use these concepts to design and analyze engineering systems.

Module Learning Outcomes مخرجات التعلم للمادة الدراسية	1-Understand the nature, properties, and mathematical representation of signals, including continuous-time and discrete-time signals, periodic and aperiodic signals, and deterministic and random signals. 2-Analyze and design linear time-invariant systems, including their impulse response, frequency response, and transfer function. 3-Understand the effects of system components on signals, including amplifiers, filters, and modulators, and how these components can be used to shape and manipulate signals. 4-Analyze signals and systems in the frequency domain using Fourier analysis, including Fourier series and Fourier transforms. 5-Apply signals and systems concepts to practical engineering applications, such as communication systems, control systems, and signal processing. 6-Understand the basics of sampling, quantization, and digital signal processing. 7-Analyze and design digital filters, including finite impulse response (FIR) and infinite impulse response (IIR) filters.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: - Part A – Introduction to signals and systems: Definition of signals, classification of signals, signal processing, classification of systems, system properties..[8 hrs] - Part B- Time-domain analysis of continuous-time signals and systems: Signal operations, system properties, convolution, impulse response, step response, and frequency response..[12hrs] - Part C - Fourier analysis of signals and systems: Fourier series, Fourier transform, properties of Fourier transforms, convolution and correlation in the frequency domain. [12 hrs] - Part D - Sampling and quantization: Sampling theorem, anti-aliasing filters, quantization error, and quantization noise. [14 hrs]: - Part E - Applications of signals and systems: Communication systems, control systems, image processing, audio signal processing,..[10 hrs] - Revision problem classes [4 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be used in signals and systems courses to introducing the fundamental concepts of signals and systems, such as signal classification, system properties, and signal operations. assignments, and group projects, Provide real-world examples: Signals and systems are used in many engineering applications, so it is helpful to provide real-world examples to illustrate the concepts and show how they are used in practice.
-------------------	---

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	61	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعيا	4.06
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	39	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعيا	2.6
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	100		

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	9,8,12	LO #,8,9,10 and 11
	Assignments	4	10% (10)	2, 12	LO # 3, 4, 6, 10
	Projects / Lab.	16	10% (10)	Continuous	All
	Report	3	10% (10)	2,4,10	LO#2,4and10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الاسبوعي النظري	
Week	Material Covered
Week 1	Introduction to signals and systems: motivation, organization of the course. Examples of signal processing systems. Basic classification of signals - continuous/discrete time, periodic/non-periodic. Transformation of time.
Week 2	Continuous and discrete time periodic signals: sinusoids and complex exponentials. Overview of basic notions in complex numbers. Discrete and continuous time systems. Linear, time invariant systems (LTI). Representation of signals as series of pulses, convolution. Describing systems using differential and difference equations.
Week 3	Continuous time signals periodic Non-periodic signals and Fourier series
Week 4	Continuous time signals: Some examples of Fourier series - Signal energy - Parseval's theorem.
Week 5	Continuous time signals frequency analysis: Fourier transform, spectral function. Spectra of typical signals
Week 6	Continuous-time systems : Laplace transform, transfer function, frequency response, stability. Example of a simple analog circuit.
Week 7	Sampling and reconstruction: ideal sampling, aliasing, sampling theorem. Spectrum of sampled signal, ideal reconstruction. Normalized time and frequency quantization
Week 8	Discrete-time signals and their frequency analysis: Discrete Fourier series, Discrete-time Fourier transform. Circular convolution, fast convolution.
Week 9	Discrete-time signals and their frequency analysis: Fast fourier transform(FFT) .
Week 10	Discrete systems - z-transform: finite and infinite impulse response systems (FIR and IIR), transfer function, frequency response, stability. Example of a digital filter..
Week 11	Discrete systems cont'd: design of simple digital filters, sampling of frequency response, windowing. Links between continuous-time and discrete-time systems.
Week 12	Random signals: random variable, realization, distribution function, probability density function (PDF). Stationarity and ergodicity..
Week 13	Parameters of a random signal: mean, Estimation - ensemble and temporal.:
Week 14	Random signals cont'd: correlation function, power spectral density (PSD). Processing of random signals by LTI systems
Week 15	Review

Week 16	Final exam
----------------	-------------------

Delivery Plan (Weekly Lab. Syllabus) المناهج الاسبوعي للمختبر	
Week	Material Covered
Week 1	Lab 1: Represent basic signals like:Unit Impulse, Ramp, Unit Step,Exponential.
Week 2	Lab 2: Generating and plotting of continuous and discrete-time signals in MATLAB.
Week 3	Lab3:Illustrate the Nyquist sampling theorem
Week 4	Lab 4: Signals Transformations
Week 5	Lab 5: Perform convolution between two vectors
Week 6	Lab 6: Convolution between signals and sequences.
Week 7	Lab 7:signal correlation(Auto and cross correlation)
Week 8	Lab 8: Fourier Series
Week 9	Lab 9:. Fourier transforms and inverse fourier transform
Week 10	Lab 10: Prpperties of fourier transform.
Week 11	Lab 11: Discrete-time Signals in Frequency Domain.
Week 12	Lab 12: Compute DFT and IDFT of a given sequence .
Week 13	Lab 13: Fast fourier transform (FFT).
Week 14	Lab 14:. Laplace Transform
Week 15	Lab 15: Z-Transform and Inverse Z-Transform Analysis
Week 16	Final Exam

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	Signals and Systems 2nd Edition by Alan Oppenheim (Author)	YES
Recommended Texts	Signals, Systems, & Transforms 5th Edition, Kindle Edition by Charles L. Phillips (Author), John Parr (Author), Eve A. Riskin (Author) Format: Kindle Edition	No
Websites		

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded

(0 – 49)	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Ministry of Higher Education and
Scientific Research - Iraq
Northern Technical University
Engineering Technical College/Mosul
Department of Computer Techniques Engineering



Module Descriptor Form
نموذج وصف المادة الدراسية

Module Information معلومات المادة الدراسية			
Module Title	<u>Wireless Sensor Network and IoT</u>	Module Delivery	
Module Type	<u>Core</u>	✓	Theory
Module Code	<u>CTE309</u>	✓	Lecture
ECTS Credits	<u>5</u>	✓	Lab
SWL (hr/sem)	<u>125</u>	✓	Tutorial
		✓	Practical
		✓	Seminar
Module Level	UGx11 3	Semester of Delivery	1
Administering Department	<u>Department of Computer Techniques Engineering</u>	College	<u>Northern Technical University Engineering Technical College/Mosul</u>
Module Leader	<u>Dr. Ahmed Khazal Younis</u>	e-mail	<u>ahmedkhazal@ntu.edu.iq</u>
Module Leader's Acad. Title	<u>Assistant prof.</u>	Module Leader's Qualification	<u>Ph.D</u>
Module Tutor	None	e-mail	None
Peer Reviewer Name	None	e-mail	None
Review Committee Approval	21/06/2023	Version Number	1.0

Relation With Other Modules العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes, and Indicative Contents أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Aims أهداف المادة الدراسية	- wireless sensor networks aim to gather data from the physical environment using a network of wireless sensor nodes. these nodes are equipped with sensors to measure various parameters such as temperature, humidity, pressure, motion, etc. - wireless sensor networks focus on collecting and aggregating the sensor data from multiple nodes to provide a comprehensive view of the monitored environment.

	• wireless sensor networks establish wireless communication links between sensor nodes to transmit data to a central node, called a sink or gateway. • Environmental Monitoring: WSNs find applications in various domains, including environmental monitoring, habitat monitoring, agriculture, healthcare, industrial automation, and disaster management. • IoT aims to connect a wide range of physical objects, devices, and systems to the internet. This enables these objects to exchange data, interact, and be remotely monitored and controlled. • Data Analytics and Automation: IoT systems aim to leverage the data generated by connected devices to gain insights, perform analytics, and enable automation and intelligent decision-making. • IoT aims to enhance efficiency, productivity, and resource management in various domains, such as smart cities, transportation, healthcare, manufacturing, energy management, and home automation. • IoT enables real-time monitoring and control of connected devices and systems from anywhere, anytime, using web or mobile interfaces. and simulations.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	The outcomes of Wireless Sensor Networks (WSNs) and the Internet of Things (IoT) have significant implications in various domains. Here are some key outcomes of WSNs and IoT: • Data-driven Decision Making: WSNs and IoT generate a vast amount of real-time data from sensors and connected devices. This data can be analyzed to gain valuable insights and support data-driven decision making • Improved Efficiency and Resource Management: WSNs and IoT enable efficient resource management by providing real-time monitoring and control. In industrial settings, WSNs can be used to monitor equipment conditions, detect faults, and optimize maintenance schedules, leading to reduced downtime and improved productivity. In smart buildings, IoT systems can optimize energy usage by adjusting lighting, heating, and cooling based on occupancy and environmental conditions. • Enhanced Safety and Security: WSNs and IoT contribute to enhanced safety and security in various domains. • Environmental Monitoring and Conservation: WSNs and IoT play a crucial role in environmental monitoring and conservation efforts. They can be deployed in ecosystems to monitor pollution levels, track wildlife behavior, and detect natural disasters. This data helps researchers and policymakers make informed decisions to protect the environment and wildlife habitats. • Smart and Connected Infrastructure: WSNs and IoT contribute to the development of smart and connected infrastructure. • Innovation and Economic Growth: WSNs and IoT foster innovation and contribute to economic growth. They create new business opportunities, drive technological advancements,

	and stimulate entrepreneurship.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: Part 1 – Introduction to Wireless Sensor Networks [16 hrs] Hardware Platforms, Topologies of wireless sensor networks, and Types of wireless sensor networks. Part 2 – Routing and Localization [16 hrs] Routing protocols, fundamental of Localization techniques, and Embedded Operating Systems. Part 3 – Introduction to IOT [16 hrs] Elements of IOT, Controllers using in IOT, and IOT Hardware. Part 4 – IOT Protocols and Module [8 hrs] IOT Communication Protocols, and Communication Module. Revision [4 hrs]

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage student's participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials, and by considering simple experiments involving interesting sampling activities for the students.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	63	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4.2
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	62	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4.1
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	3, 7, 11, 14	LO #2, 4, 10 and 11
	Assignments	4	10% (10)	3, 7, 9, 14	LO # 3, 5, 6 and 7
	Projects / Lab.	14	10% (10)	Continuous	All
	Report	5	10% (10)	5, 6, 8, 10, 13	LO # 5, 8 and 12
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	15	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الاسبوعي النظري

Week	Material Covered
Week 1	OVERVIEW OF WIRELESS SENSOR NETWORKS: • Introduction to Wireless Sensor Network. • Network Characteristics. • Key Technologies for Wireless Sensor Networks. <u>Applications of Wireless Sensor Networks.</u>
Week 2	Hardware Platforms: • Hardware parameters. • sensor nodes of a WSN: ○ sensing unit, ○ computational unit, and <u>communication unit.</u>
Week 3	Topologies of wireless sensor networks: • Star Topology. • Tree Topologies. • Mesh Topologies.
Week 4	Types of Wireless Sensor Networks: • Terrestrial WSNs. • Underground WSNs. • Underwater WSNs. • Multimedia WSNs. • Mobile WSNs.
Week 5	Routing protocols: • Routing Challenges. • Data-centric. • Geographic Routing. • Broadcast, and Multicast. • MANET protocols. <u>Resource-aware routing.</u>
Week 6	Localization: • Overview of different localization techniques
Week 7	Embedded Operating Systems: • Operating Systems for Wireless Sensor Networks • Operating System Design • Examples of Operating Systems: TinyOS , Mate ,MagnetOS , and MANTIS
Week 8	<u>Introduction to IOT:</u> • <u>Network Architecture.</u> • <u>Device Architecture.</u> • <u>Embedded system in IOT.</u> • <u>Application of IOT.</u>
Week 9	Controller use in IOT: • Arduino, ESP, and Rasberry-Pi boards. • Comparison between Arduino, ESP, and Rasberry-Pi boards • Hardware and Software Description. <u>Programming Software.</u>
Week 10	<u>Hardware in IOT:</u> • Basic Electronics Components of IOT: • LED, Resistors, Capacitors, Transistors, Relay, Switch, Diode, Zener
Week 11	<u>IOT Communication Protocols:</u> • <u>Wireless Protocols (SPI, I2C, USART, UART, Modbus).</u> • <u>Networking Protocols (OSI Reference Model, TCP/IP, Ethernet).</u>

Week 12	IOT Communication Module: • RF Module. • Bluetooth module. • GSM Module. • LAN Module. Wifi Modul. •
Week 13	Cloud Platforms for IOT: • Virtualization concepts and Cloud Architecture. • Cloud computing, benefits. • Cloud providers & offerings. • Study of IOT Cloud platforms . • ThingSpeak API and MQTT. Interfacing IOT with Web services. •
Week 14	Applications: Application example of Wireless Sensor Network and IOT. •
Week 15	Final Exam.

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	<u>Lab 1: Introduction of Embedded platform: Arduino and Raspberry Pi</u>
Week 2	<u>Lab 2: Architecture of Raspberry Pi part1</u>
Week 3	<u>Lab 3: Architecture of Raspberry Pi part2</u>
Week 4	<u>Lab 4: Software for the Raspberry Pi</u>
Week 5	<u>Lab 5: Configuration of the Raspberry Pi part1</u>
Week 6	<u>Lab 6: Configuration of the Raspberry Pi part2</u>
Week 7	<u>Lab 7: Programming Raspberry Pi with Python</u>
Week 8	<u>Lab 8: Basic Led Blinking</u>
Week 9	<u>Lab 9: Controlling of Gas Detecting Sensor.</u>
Week 10	<u>Lab 10: Controlling the Pressure Sensor</u>
Week 11	<u>Lab 11: Controlling the operation of Servo Motor</u>
Week 12	<u>Lab 12: Servo Motor Control with Webpage</u>
Week 13	<u>Lab 13: Temperature and Humidity Monitoring in Cloud Platform</u>

Week 14	Lab 14: Review
----------------	-----------------------

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	FUNDAMENTALS OF WIRELESS SENSOR NETWORKS [2010] WALTENEGUS DARGIE AND CHRISTIAN POELLABAUER INTERNET OF THINGS (IoT): TECHNOLOGIES, APPLICATIONS, CHALLENGES AND SOLUTIONS [2017] BY BK TRIPATHY ,J ANURADHA	No
Recommended Texts		
Websites		

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				
NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.				



Ministry of Higher Education and
Scientific Research - Iraq
Northern Technical University
Engineering Technical College/Mosul
Department of Computer Techniques Engineering



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information معلومات المادة الدراسية			
Module Title	<u>Fundamentals of Digital Communication</u>	Module Delivery	
Module Type	<u>Core</u>	✓	Theory
Module Code	<u>CTE310</u>	✓	Lecture
ECTS Credits	<u>5</u>	✓	Lab
SWL (hr/sem)	<u>125</u>	✓	Tutorial
		✓	Practical
		✓	Seminar
Module Level	UGx11 3	Semester of Delivery	2
Administering Department	<u>Department of Computer Techniques Engineering</u>	College	<u>Northern Technical University Engineering Technical College/Mosul</u>
Module Leader	Hakam Marwan Zaidan	e-mail	Hakam_m_zaidan@ntu.edu.iq
Module Leader's Acad. Title	<u>Assistant Lecturer</u>	Module Leader's Qualification	<u>MSc.</u>
Module Tutor	None	e-mail	None
Peer Reviewer Name	None	e-mail	None
Review Committee Approval	21/06/2023	Version Number	1.0

Relation With Other Modules العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	<u>BCCTE305-S1</u>	Semester	1
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes, and Indicative Contents أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Aims أهداف المادة الدراسية	<u>The primary objectives of the course on Digital Communication Systems are to provide students with a comprehensive understanding of the fundamental principles, techniques, and technologies used in digital communication. The course aims to familiarize students with various modulation schemes, coding techniques, and signal processing algorithms used in modern communication systems. By the end of the course, students should be able to design, analyze, and evaluate digital communication systems, including the selection of appropriate modulation schemes, error control coding, and channel equalization methods. The course also emphasizes hands-on experience through practical labs to enhance students' skills in</u>

	<u>implementing and simulating digital communication systems using MATLAB or similar tools.</u> <u>Overall, the course aims to equip students with the knowledge and practical skills necessary for the design and optimization of reliable and efficient digital communication systems.</u>
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	1. UNDERSTANDING DIGITAL COMMUNICATION CONCEPTS: GAIN A SOLID UNDERSTANDING OF THE FUNDAMENTAL CONCEPTS AND PRINCIPLES OF DIGITAL COMMUNICATION SYSTEMS, INCLUDING THE REPRESENTATION, TRANSMISSION, AND PROCESSING OF DIGITAL SIGNALS. 2. KNOWLEDGE OF MODULATION TECHNIQUES: ACQUIRE KNOWLEDGE OF VARIOUS MODULATION TECHNIQUES USED IN DIGITAL COMMUNICATION, SUCH AS AMPLITUDE SHIFT KEYING (ASK), FREQUENCY SHIFT KEYING (FSK), AND PHASE SHIFT KEYING (PSK). UNDERSTAND HOW THESE TECHNIQUES ENCODE INFORMATION ONTO CARRIER SIGNALS. 3. CHANNEL CODING AND ERROR CONTROL: LEARN ABOUT ERROR DETECTION AND CORRECTION CODES USED IN DIGITAL COMMUNICATION SYSTEMS, SUCH AS PARITY CHECK, HAMMING CODES, AND CYCLIC REDUNDANCY CHECK (CRC). UNDERSTAND HOW THESE CODES ENHANCE DATA RELIABILITY AND ENABLE ERROR CONTROL. 4. DIGITAL DEMODULATION AND DETECTION: UNDERSTAND THE PRINCIPLES OF DIGITAL DEMODULATION AND DETECTION TECHNIQUES USED TO RECOVER DIGITAL INFORMATION FROM RECEIVED SIGNALS. LEARN ABOUT COHERENT AND NON-COHERENT DETECTION METHODS AND THE IMPACT OF NOISE AND CHANNEL IMPAIRMENTS. 5. MULTIPLE ACCESS TECHNIQUES: GAIN KNOWLEDGE OF MULTIPLE ACCESS TECHNIQUES USED IN DIGITAL COMMUNICATION SYSTEMS, SUCH AS TIME-DIVISION MULTIPLE ACCESS (TDMA), CODE-DIVISION MULTIPLE ACCESS (CDMA), AND FREQUENCY-DIVISION MULTIPLE ACCESS (FDMA). UNDERSTAND THEIR APPLICATIONS AND ADVANTAGES. 6. SYSTEM PERFORMANCE ANALYSIS: DEVELOP SKILLS IN ANALYZING AND EVALUATING THE PERFORMANCE OF DIGITAL COMMUNICATION SYSTEMS, INCLUDING ASSESSING BIT ERROR RATE (BER), SIGNAL-TO-NOISE RATIO (SNR), AND LINK BUDGET. UNDERSTAND THE FACTORS THAT AFFECT SYSTEM PERFORMANCE. 7. UNDERSTANDING CHANNEL MODELS: ACQUIRE KNOWLEDGE OF CHANNEL MODELS AND THEIR CHARACTERISTICS, INCLUDING ADDITIVE WHITE GAUSSIAN NOISE (AWGN), FADING CHANNELS (RAYLEIGH AND Rician), AND THE IMPACT OF INTER-SYMBOL INTERFERENCE (ISI) ON SIGNAL QUALITY. 8. SOURCE CODING AND DATA COMPRESSION: LEARN ABOUT SOURCE CODING TECHNIQUES USED TO COMPRESS DIGITAL DATA EFFICIENTLY, SUCH AS HUFFMAN CODING AND ARITHMETIC CODING. UNDERSTAND THE PRINCIPLES OF LOSSLESS AND LOSSY COMPRESSION AND THEIR APPLICATIONS. 9. DIGITAL COMMUNICATION SYSTEM DESIGN: GAIN THE ABILITY TO DESIGN DIGITAL COMMUNICATION SYSTEMS BY SELECTING APPROPRIATE MODULATION SCHEMES, CHANNEL CODING TECHNIQUES, AND ERROR CONTROL STRATEGIES BASED ON SYSTEM REQUIREMENTS AND CONSTRAINTS. 10. APPLICATION OF DIGITAL COMMUNICATION PRINCIPLES: APPLY DIGITAL COMMUNICATION PRINCIPLES TO REAL-WORLD SCENARIOS AND PRACTICAL APPLICATIONS, SUCH AS WIRELESS COMMUNICATION, SATELLITE COMMUNICATION, AND DATA TRANSMISSION OVER NETWORKS. 11. ETHICAL CONSIDERATIONS: DEVELOP AN UNDERSTANDING OF ETHICAL CONSIDERATIONS AND PROFESSIONAL RESPONSIBILITIES RELATED TO DIGITAL COMMUNICATION SYSTEMS, SUCH AS PRIVACY, SECURITY, AND INTELLECTUAL PROPERTY.
Indicative Contents المحتويات الإرشادية	Indicative content includes the following: <u>Part 1 – General Introduction [16 hrs]</u> Introduction to the digital communication systems.

	Part 2 – digital modulation [16 hrs] Part 3 – error detection and correction [16 hrs] Part 4 – coding techniques [8 hrs] Revision [4 hrs]
--	--

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage student's participation in the exercises, while at the same time refining and expanding their critical thinking skills. This will be achieved through classes, interactive tutorials, and by considering simple experiments involving interesting sampling activities for the students.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	63	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعياً	4.2
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	62	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعياً	4.1
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	3, 7, 11, 14	LO #2, 4, 10 and 11
	Assignments	4	10% (10)	3, 7, 9, 14	LO # 3, 5, 6 and 7
	Projects / Lab.	14	10% (10)	Continuous	All
	Report	5	10% (10)	5, 6, 8, 10, 13	LO # 5, 8 and 12
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	15	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المنهاج الأسبوعي النظري

Week	Material Covered
Week 1	<u>Review of Fundamentals</u> <u>Recap of digital communication fundamentals</u> • <u>Signal representation and manipulation</u> • <u>Fourier analysis and frequency domain representation</u> •
Week 2	<u>Baseband Digital Transmission</u> <u>Baseband transmission and its limitations</u> • <u>Pulse shaping and matched filtering</u> • <u>Eye diagrams and intersymbol interference (ISI)</u> •

Week 3	<u>Line Coding and Equalization</u> <u>Line coding techniques (Unipolar, Polar, Bipolar)</u> • <u>Nyquist ISI criterion and equalization techniques</u> • <u>Decision feedback equalization (DFE)</u> •
Week 4	<u>Bandpass Digital Transmission</u> <u>Introduction to bandpass transmission</u> • <u>Digital modulation schemes (ASK, FSK, PSK)</u> • <u>Coherent and non-coherent detection techniques</u> •
Week 5	<u>Error Detection and Correction</u> <u>Introduction to error detection and correction codes</u> • <u>Linear block codes (Hamming, Reed-Solomon)</u> • <u>Convolutional codes and Viterbi decoding</u> •
Week 6	<u>Channel Coding and Interleaving</u> <u>Introduction to channel coding techniques</u> • <u>Turbo codes and iterative decoding</u> • <u>Interleaving and de-interleaving techniques</u> •
Week 7	<u>Multiple Access Techniques</u> <u>Multiple access schemes (FDMA, TDMA, CDMA)</u> • <u>Random access protocols (ALOHA, slotted ALOHA)</u> • <u>Carrier Sense Multiple Access (CSMA) protocols</u> •
Week 8	<u>Spread Spectrum Techniques</u> <u>Direct Sequence Spread Spectrum (DSSS)</u> • <u>Frequency Hopping Spread Spectrum (FHSS)</u> • <u>Code Division Multiple Access (CDMA)</u> •
Week 9	<u>Digital Modulation Techniques</u> <u>Quadrature Amplitude Modulation (QAM)</u> • <u>Orthogonal Frequency Division Multiplexing (OFDM)</u> • <u>Bit and symbol error rate analysis</u> •
Week 10	<u>Wireless Communication Systems</u> <u>Cellular communication concepts</u> • <u>Multiple access in cellular networks</u> • <u>Wireless network architectures (2G, 3G, 4G, 5G)</u> •
Week 11	<u>MIMO Communication Systems</u> <u>Introduction to Multiple-Input Multiple-Output (MIMO)</u> • <u>Spatial multiplexing and diversity techniques</u> • <u>MIMO channel capacity and beamforming</u> •
Week 12	<u>Channel Estimation and Equalization</u> <u>Channel estimation techniques for MIMO systems</u> • <u>MIMO equalization algorithms</u> • <u>Performance analysis in MIMO channels</u> •
Week 13	<u>Digital Communication System Design</u> <u>System-level design considerations</u> • <u>Link budget analysis and system performance metrics</u> • <u>Design trade-offs and practical implementation challenges</u> •
Week 14	<u>Advanced Topics and Emerging Technologies</u> <u>Overview of advanced topics (Cognitive radio, Massive MIMO)</u> • <u>Introduction to emerging technologies (IoT, 5G+, etc.)</u> • <u>Discussion on current research trends and future prospects</u> •
Week 15	<u>Final Exam.</u>

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الأسبوعي للمختبر

Week	Material Covered
Week 1	<u>Lab1: Introduction to MATLAB</u> <u>Getting started with MATLAB environment</u> • <u>Basic operations, variables, and functions</u> •
Week 2	<u>Lab 2: Pulse Amplitude Modulation (PAM)</u> <u>Generation and demodulation of PAM signals</u> • <u>Performance analysis and comparison of PAM schemes</u> •
Week 3	<u>Lab 3: Pulse Code Modulation (PCM)</u> <u>Implementation of PCM encoding and decoding</u> • <u>Analysis of quantization noise and signal-to-noise ratio (SNR)</u> •
Week 4	<u>Lab 4: Line Coding Techniques</u> <u>Implementation and comparison of line coding schemes</u> • <u>Performance evaluation using eye diagrams</u> •
Week 5	<u>Lab 5: Digital Modulation Techniques</u> <u>Implementation of ASK, FSK, and PSK modulation schemes</u> • <u>Analysis of modulation performance in AWGN channel</u> •
Week 6	<u>Lab 6: Error Detection and Correction</u> <u>Implementation of error detection codes (CRC, Hamming)</u> • <u>Performance analysis using error detection probability</u> •
Week 7	<u>Lab 7: Channel Coding with Convolutional Codes</u> <u>Encoder and decoder implementation for convolutional codes</u> • <u>BER performance analysis with Viterbi decoding</u> •
Week 8	<u>Lab 8: Spread Spectrum Techniques</u> <u>Simulation of Direct Sequence Spread Spectrum (DSSS)</u> • <u>Performance analysis in the presence of interference</u> •
Week 9	<u>Lab 9: Orthogonal Frequency Division Multiplexing (OFDM)</u> <u>OFDM signal generation and demodulation</u> • <u>Analysis of frequency and timing synchronization</u> •
Week 10	<u>Lab 10: Multiple Access Techniques</u> <u>Simulation of FDMA, TDMA, and CDMA systems</u> • <u>Performance comparison under varying load conditions</u> •
Week 11	<u>Lab 11: MIMO Systems</u> <u>Simulation of MIMO transmission and reception</u> • <u>Analysis of capacity and diversity gains</u> •
Week 12	<u>Lab 12: Channel Estimation and Equalization in MIMO</u> <u>Channel estimation techniques in MIMO systems</u> • <u>Equalization algorithms and performance evaluation</u> •

Week 13	Lab 13: Wireless Channel Simulation Simulation of fading channels (Rayleigh, Rician) • Impact of channel conditions on system performance •
Week 14	Lab 14: Review

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	"DIGITAL COMMUNICATIONS" BY JOHN G. PROAKIS AND MASOUD SALEHI.	No
Recommended Texts	"PRINCIPLES OF DIGITAL COMMUNICATION" BY ROBERT G. GALLAGER	No
Websites		

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A – Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C – Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E – Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Ministry of Higher Education and
Scientific Research - Iraq
Northern Technical University
Engineering Technical College/Mosul
Department of Computer Technology Engineering



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information معلومات المادة الدراسية			
Module Title	<u>Computer Networks Systems</u>		Module Delivery
Module Type	<u>Core</u>		✓ Theory
Module Code	<u>CTE311</u>		✓ Lecture
ECTS Credits	<u>5</u>		✓ Lab
SWL (hr/sem)	<u>125</u>		✓ Tutorial
			✓ Practical
			✓ Seminar
Module Level	UGx11 3	Semester of Delivery	2
Administering Department	<u>Department of Computer Techniques Engineering</u>	College	<u>Northern Technical University Engineering Technical College/Mosul</u>
Module Leader	<u>Dr. Ziyad Khalaf Farej</u>	e-mail	<u>drziyad.farej@ntu.edu.iq</u>
Module Leader's Acad. Title	<u>Asst. Prof.</u>	Module Leader's Qualification	<u>Ph.D.</u>
Module Tutor	None	e-mail	None
Peer Reviewer Name	None	e-mail	None
Review Committee Approval	2/06/2023	Version Number	1.0

Relation with Other Modules العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	73. Medium Access Control (MAC): To Enhance MAC protocols and improve channel access efficiency. To Reduce collisions and increase throughput in wireless or shared media environments. 74. LAN Performance: To Improve LAN performance by optimizing protocols and reducing latency. To Enhance network switch and router performance to handle increasing data rates. To Enhance LAN security and mitigate potential threats to ensure data confidentiality. 75. Data Link Layer: To Develop efficient error detection and correction mechanisms and ensure data integrity. To Implement flow control mechanisms and regulate the rate of data transmission. 76. Transport Layer: To Enhance transport layer protocols such as TCP and UDP to improve reliability and efficiency.

	To Implement congestion control algorithms and prevent network congestion to ensure fair resource allocation. 77. Network Layer: To Develop routing protocols and enable efficient packet forwarding and path selection.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	122. Explain the concepts of framing, error detection, and error correction 123. Applying flow control mechanisms to regulate data transmission between sender and receiver. 124. Applying Network Protocols: understanding how protocols like TCP/IP enable reliable data transmission and how they are used in real-world scenarios. 125. Differentiate between connection-oriented and connectionless transport protocols. 126. Implement reliable data transfer mechanisms using protocols like TCP. 127. Managing Network Infrastructure and optimizing network performance. 128. Understanding the basics of wireless technologies and IoT.
Indicative Contents المحتويات الإرشادية	- Data Link Layer: [20 h] - Media Access Control (MAC) protocols (Ethernet, Wi-Fi) - Framing and error detection - Flow control and error control mechanisms - Local Area Networks (LAN) and Ethernet technologies - Network Layer: [14 h] - IP addressing and subnetting - Routing protocols (static routing, RIP, OSPF) - IP packet forwarding and (IPv4) and (IPv6) - Transport Layer: [12 h] - Transmission Control Protocol (TCP) and User Datagram Protocol (UDP) - Connection-oriented vs. connectionless protocols - Reliable data transfer and congestion control mechanisms - Emerging Network Technologies: [12 h] - Wireless networks (Wi-Fi, Bluetooth, cellular networks) - Internet of Things (IoT) and sensor networks

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. When studying Computer Networks Fundamentals, there are several key strategies that can help you grasp the material effectively. These strategies include (Understanding the Fundamentals, Layered Approach, Analyze Network Protocols, Network Simulations: Visualize Networks and Practical Problem Solving. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطالب

Structured SWL (h/sem)	58	Structured SWL (h/w)	4
-------------------------------	----	-----------------------------	---

الحمل الدراسي المنتظم للطالب خلال الفصل		الحمل الدراسي المنتظم للطالب أسبوعيا	
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	67	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعيا	5
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	8	10% (10)	2, 12	LO # 3, 4, 6 and 7
	Projects / Lab.	15	10% (10)	Continuous	All
	Report	5	10% (10)	13	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المناهج الأسبوعي النظري	
Week	Material Covered
Week 1	:Introduction to Local Area Networks Standards Medium Access Control, Ethernet (CSMA/CD) and Token MAC protocols
Week 2	LAN performance; Token ring and CSMA/CD performance Evaluation
Week 3	Introduction to Data-Link Layer; High-level Data Link Control (HDLC) and Point-to-Point Protocol (PPP)
Week 4	Data Link Layer Design Issues: Framing, Error Control, and Flow Control
Week 5	Automatic Repeat Request (ARQ), Stop-And-Wait Protocol and Sliding Window Protocols
Week 6	Link Throughput, Utilization and Efficiency
Week 7	Effect of Errors on Throughput Effect of Sliding Window and ARQ on Throughput
Week 8	Introduction, Error Detection and Correction, Types of Errors, Redundancy, Forward Error Correction
Week 9	Block Coding, Error detection, Cyclic Codes, Cyclic Redundancy Check, Hardware Implementation
Week 10	Transport layer; Process-la-Process Delivery UDP, TCP and SCTP, Features and Connection
Week 11	TCP Congestion Control, Timers Open loop and Close loop Congestion Control
Week 12	Network Layer: Delivery, Forwarding, and Routing
Week 13	Unicast Routing Protocols; Intra- and Interdomain Routing Multicast and Broadcast Routing Protocols
Week 14	Wireless LANs; IEEE 802.11 Standards and Bluetooth
Week 15	Final Exam.

Delivery Plan (Weekly Lab. Syllabus) المنهاج الاسبوعي للمختبر	
Week	Material Covered
Week 1	Lab 1: Introduction to wireless networks
Week 2	Lab 2: Wireless network characteristics
Week 3	Lab 3: IEEE 802.11 standards
Week 4	Lab 4: Wireless Access point
Week 5	Lab 5: Wireless Station
Week 6	Lab 6: Data Routing Algorithm
Week 7	Lab 7: Introduction to Network Management software
Week 8	Lab 8: Mikrotik system
Week 9	Lab 9: Mikrotik hardware devices
Week 10	Lab 10: Winbox software
Week 11	Lab 11: Firewall configuration
Week 12	Lab 12: Hotspot management
Week 13	Lab 13: User manager
Week 14	Lab 14: Network advanced tools

Learning and Teaching Resources مصادر التعلم والتدريس		
	Text	Available in the Library?
Required Texts	Behrouz A. Forouzan. Data communication and Networking, fifth edition	Yes
Recommended Texts	Michael Duck and Richard Read” Communications and Computer Networks, 2 nd edition, Pearson Education 2003	No
Websites	Digital Systems: From Logic Gates to Processors: https://www.coursera.org/learn/computes-networks	

APPENDIX:

GRADING SCHEME مخطط الدرجات				
Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required
Note:				

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Ministry of Higher Education and
Scientific Research - Iraq
Northern Technical University
Engineering Technical College/Mosul
Department of Computer Technology Engineering



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	Computer Networks Systems		Module Delivery	
Module Type	Core		☒ Theory ☒ Lecture ☒ Lab ☒ Tutorial ☒ Practical ☒ Seminar	
Module Code	CTE311			
ECTS Credits	5			
SWL (hr/sem)	125			
Module Level	UGX11 3	Semester of Delivery		2
Administering Department	Department of Computer Techniques Engineering		College	Northern Technical University Engineering Technical College/Mosul
Module Leader	Dr. Ziyad Khalaf Farej		e-mail	drziyad.farej@ntu.edu.iq
Module Leader's Acad. Title	Asst. Prof.		Module Leader's Qualification	Ph.D.
Module Tutor	None		e-mail	None
Peer Reviewer Name	None		e-mail	None
Review Committee Approval	2/06/2023		Version Number	1.0

Relation with Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	78. Medium Access Control (MAC): To Enhance MAC protocols and improve channel access efficiency. To Reduce collisions and increase throughput in wireless or shared media environments. 79. LAN Performance: To Improve LAN performance by optimizing protocols and reducing latency. To Enhance network switch and router performance to handle increasing data rates. To Enhance LAN security and mitigate potential threats to ensure data confidentiality. 80. Data Link Layer:

	To Develop efficient error detection and correction mechanisms and ensure data integrity. To Implement flow control mechanisms and regulate the rate of data transmission. 81. Transport Layer: To Enhance transport layer protocols such as TCP and UDP to improve reliability and efficiency. To Implement congestion control algorithms and prevent network congestion to ensure fair resource allocation. 82. Network Layer: To Develop routing protocols and enable efficient packet forwarding and path selection.
Module Learning Outcomes مخرجات التعلم للمادة الدراسية	129. Explain the concepts of framing, error detection, and error correction 130. Applying flow control mechanisms to regulate data transmission between sender and receiver. 131. Applying Network Protocols: understanding how protocols like TCP/IP enable reliable data transmission and how they are used in real-world scenarios. 132. Differentiate between connection-oriented and connectionless transport protocols. 133. Implement reliable data transfer mechanisms using protocols like TCP. 134. Managing Network Infrastructure and optimizing network performance. 135. Understanding the basics of wireless technologies and IoT.
Indicative Contents المحتويات الإرشادية	- Data Link Layer: [20 h] - Media Access Control (MAC) protocols (Ethernet, Wi-Fi) - Framing and error detection - Flow control and error control mechanisms - Local Area Networks (LAN) and Ethernet technologies - Network Layer: [14 h] - IP addressing and subnetting - Routing protocols (static routing, RIP, OSPF) - IP packet forwarding and (IPv4) and (IPv6) - Transport Layer: [12 h] - Transmission Control Protocol (TCP) and User Datagram Protocol (UDP) - Connection-oriented vs. connectionless protocols - Reliable data transfer and congestion control mechanisms - Emerging Network Technologies: [12 h] - Wireless networks (Wi-Fi, Bluetooth, cellular networks) - Internet of Things (IoT) and sensor networks
Learning and Teaching Strategies استراتيجيات التعلم والتعليم	
Strategies	The main strategy that will be adopted in delivering this module is to encourage students' participation in the exercises, while at the same time refining and expanding their critical thinking skills. When studying Computer Networks Fundamentals, there are several key strategies that can help you grasp the material effectively. These strategies include (Understanding the Fundamentals, Layered Approach, Analyze Network Protocols, Network Simulations: Visualize Networks and Practical Problem Solving. This will be achieved through classes, interactive tutorials and by considering type of simple experiments involving some sampling activities that are interesting to the students.

Student Workload (SWL) الحمل الدراسي للطالب			
Structured SWL (h/sem) الحمل الدراسي المنتظم للطالب خلال الفصل	58	Structured SWL (h/w) الحمل الدراسي المنتظم للطالب أسبوعيا	4
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطالب خلال الفصل	67	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطالب أسبوعيا	5
Total SWL (h/sem) الحمل الدراسي الكلي للطالب خلال الفصل	125		

Module Evaluation تقييم المادة الدراسية					
As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	5, 10	LO #1, 2, 10 and 11
	Assignments	8	10% (10)	2, 12	LO # 3, 4, 6 and 7
	Projects / Lab.	15	10% (10)	Continuous	All
	Report	5	10% (10)	13	LO # 5, 8 and 10
Summative assessment	Midterm Exam	2 hr	10% (10)	7	LO # 1-7
	Final Exam	3 hr	50% (50)	16	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus) المنهاج الأسبوعي النظري	
Week	Material Covered
Week 1	:Introduction to Local Area Networks Standards Medium Access Control, Ethernet (CSMA/CD) and Token MAC protocols
Week 2	LAN performance; Token ring and CSMA/CD performance Evaluation
Week 3	Introduction to Data-Link Layer; High-level Data Link Control (HDLC) and Point-to-Point Protocol (PPP)
Week 4	Data Link Layer Design Issues: Framing, Error Control, and Flow Control
Week 5	Automatic Repeat Request (ARQ), Stop-And-Wait Protocol and Sliding Window Protocols
Week 6	Link Throughput, Utilization and Efficiency
Week 7	Effect of Errors on Throughput Effect of Sliding Window and ARQ on Throughput
Week 8	Introduction, Error Detection and Correction, Types of Errors, Redundancy, Forward Error Correction
Week 9	Block Coding, Error detection, Cyclic Codes, Cyclic Redundancy Check, Hardware Implementation
Week 10	Transport layer; Process-la-Process Delivery UDP, TCP and SCTP, Features and Connection
Week 11	TCP Congestion Control, Timers Open loop and Close loop Congestion Control
Week 12	Network Layer: Delivery, Forwarding, and Routing
Week 13	Unicast Routing Protocols; Intra- and Interdomain Routing Multicast and Broadcast Routing Protocols

Week 14	Wireless LANs; IEEE 802.11 Standards and Bluetooth
Week 15	Final Exam.

Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

Week	Material Covered
Week 1	Lab 1: Introduction to wireless networks
Week 2	Lab 2: Wireless network characteristics
Week 3	Lab 3: IEEE 802.11 standards
Week 4	Lab 4: Wireless Access point
Week 5	Lab 5: Wireless Station
Week 6	Lab 6: Data Routing Algorithm
Week 7	Lab 7: Introduction to Network Management software
Week 8	Lab 8: Mikrotik system
Week 9	Lab 9: Mikrotik hardware devices
Week 10	Lab 10: Winbox software
Week 11	Lab 11: Firewall configuration
Week 12	Lab 12: Hotspot management
Week 13	Lab 13: User manager
Week 14	Lab 14: Network advanced tools

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts	Behrouz A. Forouzan. Data communication and Networking, fifth edition	Yes
Recommended Texts	Michael Duck and Richard Read” Communications and Computer Networks, 2 nd edition, Pearson Education 2003	No
Websites	Digital Systems: From Logic Gates to Processors: https://www.coursera.org/learn/computes-networks	

APPENDIX:

GRADING SCHEME

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 – 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.



Module Descriptor Form

نموذج وصف المادة الدراسية

Module Information				
معلومات المادة الدراسية				
Module Title	<u>Summer Training 2</u>		Module Delivery	
Module Type	<u>Supplement</u>		Theory Lecture ✓ Lab Tutorial ✓ Practical ✓ Seminar	
Module Code	<u>CTE312</u>			
ECTS Credits	<u>2</u>			
SWL (hr/sem)	<u>50</u>			
Module Level	UGx11 2	Semester of Delivery		2
Administering Department	<u>Department of Computer Techniques Engineering</u>	College	<u>Northern Technical University Engineering Technical College/Mosul</u>	
Module Leader	All Acadimic staf	e-mail		
Module Leader's Acad. Title			Module Leader's Qualification	
Module Tutor	None	e-mail	None	
Peer Reviewer Name		None	e-mail	None
Review Committee Approval		21/06/2023	Version Number 1.0	

Relation With Other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

Module Aims, Learning Outcomes and Indicative Contents	
أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية	
Module Objectives أهداف المادة الدراسية	٩- القدرة على ربط المعرفة المكتسبة من الطالب خلال دراسته الأكاديمية بالمشاكل الحقيقية على أرض الواقع ١٠- اكتشاف بيئة العمل واحتياجاتها وقبورها ١١- القدرة على تحديد المتطلبات المطلوبة لإيجاد حلول مناسبة وفعالة للمشاكل الحقيقية على أرض الواقع مع وجود قيود فنية مختلفة ١٢- القدرة على تكوين رؤية واضحة حول الأهداف والمعوقات والعمل بشكل فعال ١٣- إيجاد الطالب استقلاليته باكتسابه لمهارات جديدة مع إشراف بسيط من قبل جهة التدريب . ١٤- القدرة على إيجاد حلول مناسبة في حال حدوث أي تغيير في متطلبات العمل وقبوره ١٥- القدرة على التواصل مع الكثير من الأشخاص المتواجدين في المجال العملي . ١٦- تعلم المسؤوليات الأخلاقية والاحترافية.

Module Learning Outcomes مخرجات التعلم للمادة الدراسية	- Networking Fundamentals: Gain knowledge of computer networks, protocols, and network architecture. Understand concepts like IP addressing, routing, switching, and network security. - Operating Systems: Learn about the basic principles and components of operating systems. Understand process management, memory management
Indicative Contents المحتويات الإرشادية	- Part 1- [5 hrs] : تشغيل وصيانة شبكات الحاسبات - Part 2- [5 hrs] : انواع شبكات الحاسبات - Part 3- [5 hrs] : البروتوكولات المتبعة في شبكات الحاسبات - Part 4- [5 hrs] : مراجعة

Learning and Teaching Strategies

استراتيجيات التعلم والتعليم

Strategies	ان الإستراتيجية الرئيسية التي سيتم تبنيها في تقديم هذه الوحدة: - تشجيع طلبة هندسة الحاسوب على المشاركة بشكل مهني مع الأوساط العملية من خلال الممارسة الميدانية في دوائر الدولة . - إيجاد حلول مبتكرة ريادية لسد الاحتياجات المحلية. - بناء قيادة مهنية وأخلاقية وتعاونية في مكان العمل وفي المجتمع. - تطوير معرفتهم وخبراتهم
-------------------	--

Student Workload (SWL)

الحمل الدراسي للطلاب

Structured SWL (h/sem) الحمل الدراسي المنتظم للطلاب خلال الفصل	20	Structured SWL (h/w) الحمل الدراسي المنتظم للطلاب أسبوعيا	5
Unstructured SWL (h/sem) الحمل الدراسي غير المنتظم للطلاب خلال الفصل	30	Unstructured SWL (h/w) الحمل الدراسي غير المنتظم للطلاب أسبوعيا	7.5
Total SWL (h/sem) الحمل الدراسي الكلي للطلاب خلال الفصل	50		

Module Evaluation

تقييم المادة الدراسية

As		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes				
	Assignments	8	15% (10)	1, 2,3,4	LO # 1, 2, 3 and 4
	Projects / Lab.	4	20% (10)	Continuous	All
	Report	2	15% (10)	2,4	LO # 2 and 4
Summative assessment	Midterm Exam				
	Final Exam	1 hr	50% (50)	5	All
Total assessment			100% (100 Marks)		

Delivery Plan (Weekly Syllabus)

المناهج الاسبوعي النظري

Week	Material Covered
Week 1	تعريف الطالب على اقسام الموقع التدريبي والاطلاع على سير العمل وطبيعة ادارة شبكات الحاسبات داخل الموقع التدريبي
Week 2	اعطاء نبذة مختصرة عن اجزاء شبكات الحاسبات والية عملها والبروتوكولات التي تنظم عمل هذه الشبكات وتعريف الطالب على انواع شبكات الحاسبات من حيث النطاق الجغرافي
Week 3	تعريف الطالب على كيفية انشاء شبكات محلية وبرمجة الاجهزة الشبكية مثل (switchs, router, access point)
Week 4	تعريف الطالب على كيفية انشاء شبكات الواسعة النطاق (WAN) وبرمجة الاجهزة الشبكية مثل (power beam, sector ...etc)
Week 5	Final Exam

Learning and Teaching Resources

مصادر التعلم والتدريس

	Text	Available in the Library?
Required Texts		
Recommended Texts		
Websites		

APPENDIX:

GRADING SCHEME

مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
Success Group (50 - 100)	A - Excellent	امتياز	90 - 100	Outstanding Performance
	B - Very Good	جيد جدا	80 - 89	Above average with some errors
	C - Good	جيد	70 - 79	Sound work with notable errors
	D - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	E - Sufficient	مقبول	50 - 59	Work meets minimum criteria
Fail Group (0 - 49)	FX – Fail	مقبول بقرار	(45-49)	More work required but credit awarded
	F – Fail	راسب	(0-44)	Considerable amount of work required

Note:

NB Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.

نموذج وصف المقرر

١. اسم المقرر:					
لغة انكليزية ٤					
٢. رمز المقرر:					
NTU401					
٣. الفصل الدراسي / السنة					
الفصل الاول/المستوى الرابع					
٤. تاريخ إعداد الوصف					
٢٠٢٥/٦/١٠					
٥. أشكال الحضور المتاحة					
حضور					
٦. عدد الساعات المعتمدة (الإجمالي) / عدد الوحدات (الإجمالي)					
2					
٧. اسم مسؤول المقرر (اذكر جميع الأسماء، إذا كان هناك أكثر من اسم واحد)					
الاسم: د. ثبات فخري يونس البريد الإلكتروني: thabet.tfy@ntu.edu.iq					
٨. أهداف المقرر					
الأهداف ١- الالمام بقواعد اللغة الانكليزية ٢- اتقان التحدث باللغة الانكليزية ٣- اتقان كتابة اللغة الانكليزية					
٩. استراتيجيات التدريس والتعلم					
الاستراتيجية المحاضرات النظرية					
١٠. بنية المقرر					
الأسبوع	الساعات	مخرجات التعلم المطلوبة	اسم الوحدة / أو الموضوع	طريقة التعليم	طريقة التقييم
1	2	ان يكون الطالب قادرا على الالمام بقواعد اللغة الانكليزية	Simple present	نظري	الامتحانات المكتوبة والشفوية
2	2	ان يكون الطالب قادرا على الالمام بقواعد اللغة الانكليزية	Simple past	نظري	الامتحانات المكتوبة والشفوية

3	2	ان يكون الطالب قادرا على الامام بقواعد اللغة الانكليزية	Present continuous	نظري	الامتحانات المكتوبة والشفوية
4	2	ان يكون الطالب قادرا على الامام بقواعد اللغة الانكليزية	Past perfect	نظري	الامتحانات المكتوبة والشفوية
5	2	ان يكون الطالب قادرا على الامام بقواعد اللغة الانكليزية	Nouns	نظري	الامتحانات المكتوبة والشفوية
6	2	ان يكون الطالب قادرا على الامام بقواعد اللغة الانكليزية	Noun qualifiers	نظري	الامتحانات المكتوبة والشفوية
7	2	ان يكون الطالب قادرا على الامام بقواعد اللغة الانكليزية	Negative sentences	نظري	الامتحانات المكتوبة والشفوية
8	2	ان يكون الطالب قادرا على التحدث باللغة الانكليزية	Formal speech	نظري	الامتحانات المكتوبة والشفوية
9	2	ان يكون الطالب قادرا على التحدث باللغة الانكليزية	Formal speech	نظري	الامتحانات المكتوبة والشفوية

الامتحانات المكتوبة والشفوية	نظري	Formal speech	ان يكون الطالب قادرا على التحدث باللغة الانكليزية	2	10
الامتحانات المكتوبة والشفوية	نظري	Informal speech	ان يكون الطالب قادرا على التحدث باللغة الانكليزية	2	11
الامتحانات المكتوبة والشفوية	نظري	Formal letters	ان يكون الطالب قادرا على اتقان الكتابة باللغة الانكليزية	2	12
الامتحانات المكتوبة والشفوية	نظري	Formal letters	ان يكون الطالب قادرا على اتقان الكتابة باللغة الانكليزية	2	13
الامتحانات المكتوبة والشفوية	نظري	Formal Email	ان يكون الطالب قادرا على اتقان الكتابة باللغة الانكليزية	2	14
الامتحانات المكتوبة والشفوية	نظري	Scientific paper	ان يكون الطالب قادرا على اتقان الكتابة باللغة الانكليزية	2	15
١١. تقييم المقرر					
١٠ درجات امتحان يومي و ٤٠ درجة امتحان شهري و ٥٠ درجلا امتحان فصلي					
١٢. موارد التعلم والتعليم					
			Liz and John Soars “headway” 2 nd edition		

	https://www.cambridgeenglish.org/learning-english/free-resources/

نموذج وصف المقرر

١. اسم المقرر:
نظرية المعلومات
٢. رمز المقرر:

CCT404					
٣. الفصل الدراسي / السنة					
الفصل الاول/المستوى الرابع					
٤. تاريخ إعداد الوصف					
٢٠٢٥/٦/١٠					
٥. أشكال الحضور المتاحة					
حضور					
٦. عدد الساعات المعتمدة (الإجمالي) / عدد الوحدات (الإجمالي)					
٢ ساعة نظري و ٢ ساعة عملي عدد الوحدات ٣					
٧. اسم مسؤول المقرر (اذكر جميع الأسماء، إذا كان هناك أكثر من اسم واحد)					
الاسم: د. عماد احمد محمد البريد الإلكتروني: e.a.mohammed@ntu.edu.iq					
٨. اهداف المقرر					
الاهداف ١- التعرف على انواع المتغيرات العشوائية ٢- التعامل مع المعلومات ٣- تحليل قنوات الاتصالات					
٩. استراتيجيات التدريس والتعلم					
الاستراتيجية المحاضرات النظرية والعملية					
١٠. بنية المقرر					
الأسبوع	الساعات	مخرجات التعلم المطلوبة	اسم الوحدة / أو الموضوع	طريقة التعليم	طريقة التقييم
1	4	ان يكون الطالب قادرا على معرفة انواع المتغيرات العشوائية	Probability	نظري و عملي	الامتحانات النظرية والعملية
2	4	ان يكون الطالب قادرا على معرفة انواع المتغيرات العشوائية	Joint probability	نظري و عملي	الامتحانات المكتوبة والشفوية

الامتحانات النظرية والعملية	نظري وعلمي	Conditional probability	ان يكون الطالب قادرا على معرفة انواع المتغيرات العشوائية	4	3
الامتحانات النظرية والعملية	نظري وعلمي	Random variables	ان يكون الطالب قادرا على معرفة انواع المتغيرات العشوائية	4	4
الامتحانات النظرية والعملية	نظري وعلمي	Random variables	ان يكون الطالب قادرا على معرفة انواع المتغيرات العشوائية	4	5
الامتحانات النظرية والعملية	نظري وعلمي	Gaussian distribution	ان يكون الطالب قادرا على معرفة انواع المتغيرات العشوائية	4	6
الامتحانات النظرية والعملية	نظري وعلمي	Poisson distribution	ان يكون الطالب قادرا على معرفة انواع المتغيرات العشوائية	4	7
الامتحانات النظرية والعملية	نظري وعلمي	Uniform distribution	ان يكون الطالب قادرا على معرفة انواع المتغيرات العشوائية	4	8
الامتحانات النظرية والعملية	نظري وعلمي	Chi-square distribution	ان يكون الطالب قادرا على معرفة انواع المتغيرات العشوائية	4	9

الامتحانات النظرية والعملية	نظري وعلمي	Types of information	ان يكون الطالب قادرا على التعامل مع المعلومات	4	10
الامتحانات النظرية والعملية	نظري وعلمي	Digital Information	ان يكون الطالب قادرا على التعامل مع المعلومات	4	11
الامتحانات النظرية والعملية	نظري وعلمي	Joint Information	ان يكون الطالب قادرا على التعامل مع المعلومات	4	12
الامتحانات النظرية والعملية	نظري وعلمي	Conditional information	ان يكون الطالب قادرا على التعامل مع المعلومات	4	13
الامتحانات النظرية والعملية	نظري وعلمي	Channels Type	ان يكون الطالب قادرا على تحليل قنوات الاتصالات	4	14
الامتحانات النظرية والعملية	نظري وعلمي	Channel capacity	ان يكون الطالب قادرا على تحليل قنوات الاتصالات	4	15

١١. تقييم المقرر	
١٠ درجات امتحان يومي نظري و ١٠ درجة امتحان يومي عملي و ٢٠ درجة امتحان شهري نظري و ١٠ درجة امتحان شهري عملي و ١٠ درجات امتحان فصلي عملي و ٤٠ درجة امتحان فصلي نظري	
١٢. موارد التعلم والتعليم	
	Stremler “Introduction to communication system” 9 th edition
	Lathi “Digital communication” 7th edition
	https://www.coursera.org/learn/information-theory

نموذج وصف المقرر

١. اسم المقرر:					
الاتصالات الرقمية ٢					
٢. رمز المقرر:					
CCT405					
٣. الفصل الدراسي / السنة					
الفصل الاول/المستوى الرابع					
٤. تاريخ إعداد الوصف					
٢٠٢٥/٦/١٠					
٥. أشكال الحضور المتاحة					
حضور					
٦. عدد الساعات المعتمدة (الإجمالي) / عدد الوحدات (الإجمالي)					
٢ ساعة نظري و ٢ ساعة عملي عدد الوحدات ٣					
٧. اسم مسؤول المقرر (اذكر جميع الأسماء، إذا كان هناك أكثر من اسم واحد)					
الاسم: حكم مروان زيدان البريد الإلكتروني: hakam_m_zaidan@ntu.edu.iq					
٨. أهداف المقرر					
الأهداف ١- معرفة طبيعة الإشارة الرقمية ٢- معرفة كيفية تحويل الإشارة التناظرية الى إشارة رقمية ٣- معرفة أهمية نظام الاتصالات الرقمي و تطبيقاته					
٩. استراتيجيات التدريس والتعلم					
الاستراتيجية المحاضرات النظرية والعملية					
١٠. بنية المقرر					
الأسبوع	الساعات	مخرجات التعلم المطلوبة	اسم الوحدة / أو الموضوع	طريقة التعليم	طريقة التقييم
1	4	ان يكون الطالب قادرا على معرفة طبيعة الإشارة الرقمية	Signal and systems	نظري وعلمي	الامتحانات النظرية والعملية
2	4	ان يكون الطالب قادرا على معرفة طبيعة الإشارة الرقمية	Deterministic signal	نظري وعلمي	الامتحانات النظرية والعملية

الامتحانات النظرية والعملية	نظري وعلمي	Non-deterministic signal	ان يكون الطالب قادرا على معرفة طبيعة الإشارة الرقمية	4	3
الامتحانات النظرية والعملية	نظري وعلمي	Continuous signals	ان يكون الطالب قادرا على معرفة طبيعة الإشارة الرقمية	4	4
الامتحانات النظرية والعملية	نظري وعلمي	Signal spectrum	ان يكون الطالب قادرا على معرفة طبيعة الإشارة الرقمية	4	5
الامتحانات النظرية والعملية	نظري وعلمي	Discrete signals	ان يكون الطالب قادرا على تحويل الإشارة التناظرية الى رقمية	4	6
الامتحانات النظرية والعملية	نظري وعلمي	Analog to digital converter	ان يكون الطالب قادرا على تحويل الإشارة التناظرية الى رقمية	4	7
الامتحانات النظرية والعملية	نظري وعلمي	Digital quantization	ان يكون الطالب قادرا على تحويل الإشارة التناظرية الى رقمية	4	8
الامتحانات النظرية والعملية	نظري وعلمي	Digital Communication system	ان يكون الطالب قادرا على معرفة اهمية نظام الاتصالات ال و تطبيقاته	4	9

الامتحانات النظرية والعملية	نظري وعلمي	Digital modulation	ان يكون الطالب قادرا على معرفة اهمية نظام الاتصالات ال و تطبيقاته	4	10
الامتحانات النظرية والعملية	نظري وعلمي	ASK	ان يكون الطالب قادرا على معرفة اهمية نظام الاتصالات ال و تطبيقاته	4	11
الامتحانات النظرية والعملية	نظري وعلمي	PSK	ان يكون الطالب قادرا على معرفة اهمية نظام الاتصالات ال و تطبيقاته	4	12
الامتحانات النظرية والعملية	نظري وعلمي	CDMA	ان يكون الطالب قادرا على معرفة اهمية نظام الاتصالات ال و تطبيقاته	4	13
الامتحانات النظرية والعملية	نظري وعلمي	FHSS	ان يكون الطالب قادرا على معرفة اهمية نظام الاتصالات ال و تطبيقاته	4	14
الامتحانات النظرية والعملية	نظري وعلمي	DSSS	ان يكون الطالب قادرا على معرفة اهمية نظام الاتصالات ال و تطبيقاته	4	15

١١. تقييم المقرر	
١٠ درجات امتحان يومي نظري و ١٠ درجة امتحان يومي عملي و ٢٠ درجة امتحان شهري نظري و ١٠ درجة امتحان شهري عملي و ١٠ درجات امتحان فصلي عملي و ٤٠ درجة امتحان فصلي نظري	
١٢. موارد التعلم والتعليم	
	Gallager - Principles of Digital Communications 2008
	Digital Communications/ Bernard Sklar
	https://www.coursera.org/

نموذج وصف المقرر

١. اسم المقرر:
معالجة الإشارة الرقمية
٢. رمز المقرر:

CCT406					
٣. الفصل الدراسي / السنة					
الفصل الاول/المستوى الرابع					
٤. تاريخ إعداد الوصف					
٢٠٢٥/٦/١٠					
٥. أشكال الحضور المتاحة					
حضور					
٦. عدد الساعات المعتمدة (الإجمالي) / عدد الوحدات (الإجمالي)					
٢ ساعة نظري و ٢ ساعة عملي عدد الوحدات ٣					
٧. اسم مسؤول المقرر (اذكر جميع الأسماء، إذا كان هناك أكثر من اسم واحد)					
الاسم: عمر زياد طارق البريد الإلكتروني: omar.zyad@ntu.edu.iq					
٨. أهداف المقرر					
الاهداف ١ فهم تحليل الاشارات الرقمية ٢- تحويل الاشارات من محور الزمن الى محاور اخرى ٣- معرفة تطبيقات معالجة الاشارة					
٩. استراتيجيات التدريس والتعلم					
الاستراتيجية المحاضرات النظرية والعملية					
١٠. بنية المقرر					
الأسبوع	الساعات	مخرجات التعلم المطلوبة	اسم الوحدة / أو الموضوع	طريقة التعليم	طريقة التقييم
1	4	ان يكون الطالب قادرا على فهم تحليل الاشارات الرقمية	Digital signals	نظري وعلمي	الامتحانات النظرية والعملية
2	4	ان يكون الطالب قادرا على فهم تحليل الاشارات الرقمية	Unit step signal	نظري وعلمي	الامتحانات النظرية والعملية

3	4	ان يكون الطالب قادرا على فهم تحليل الاشارات الرقمية	Ramp signal	نظري وعلمي	الامتحانات النظرية والعملية
4	4	ان يكون الطالب قادرا على فهم تحليل الاشارات الرقمية	Exponential signals	نظري وعلمي	الامتحانات النظرية والعملية
5	4	ان يكون الطالب قادرا على تحويل الاشارات من محور الزمن الى محاور اخرى	Time domain to Frequency domain conversion	نظري وعلمي	الامتحانات النظرية والعملية
6	4	ان يكون الطالب قادرا على تحويل الاشارات من محور الزمن الى محاور اخرى	DFT	نظري وعلمي	الامتحانات النظرية والعملية
7	4	ان يكون الطالب قادرا على تحويل الاشارات من محور الزمن الى محاور اخرى	FFT	نظري وعلمي	الامتحانات النظرية والعملية
8	4	ان يكون الطالب قادرا على تحويل الاشارات من محور الزمن الى محاور اخرى	Time domain to Z Domain conversion	نظري وعلمي	الامتحانات النظرية والعملية
9	4	ان يكون الطالب قادرا على تحويل الاشارات من محور الزمن الى محاور اخرى	Laplace transform	نظري وعلمي	الامتحانات النظرية والعملية

الامتحانات النظرية والعملية	نظري وعلمي	Filters	ان يكون الطالب قادرا على معرفة تطبيقات معالجة الإشارة	4	10
الامتحانات النظرية والعملية	نظري وعلمي	Chebyshev filter	ان يكون الطالب قادرا على معرفة تطبيقات معالجة الإشارة	4	11
الامتحانات النظرية والعملية	نظري وعلمي	Butterworth filter	ان يكون الطالب قادرا على معرفة تطبيقات معالجة الإشارة	4	12
الامتحانات النظرية والعملية	نظري وعلمي	1 st order filters	ان يكون الطالب قادرا على معرفة تطبيقات معالجة الإشارة	4	13
الامتحانات النظرية والعملية	نظري وعلمي	2 nd order filters	ان يكون الطالب قادرا على معرفة تطبيقات معالجة الإشارة	4	14
الامتحانات النظرية والعملية	نظري وعلمي	3 rd order filters	ان يكون الطالب قادرا على معرفة تطبيقات معالجة الإشارة	4	15

١١. تقييم المقرر	
١٠ درجات امتحان يومي نظري و ١٠ درجة امتحان يومي عملي و ٢٠ درجة امتحان شهري نظري و ١٠ درجة امتحان شهري عملي و ١٠ درجات امتحان فصلي عملي و ٤٠ درجة امتحان فصلي نظري	
١٢. موارد التعلم والتعليم	
	Digital Signal Processing John G. Proakis, 2007
	https://www.coursera.org/

نموذج وصف المقرر

١. اسم المقرر:
ذكاء اصطناعي ١

٢. رمز المقرر:					
CCT407					
٣. الفصل الدراسي / السنة					
الفصل الاول/المستوى الرابع					
٤. تاريخ إعداد الوصف					
٢٠٢٥/٦/١٠					
٥. أشكال الحضور المتاحة					
حضور					
٦. عدد الساعات المعتمدة (الإجمالي) / عدد الوحدات (الإجمالي)					
٢ ساعة نظري و ٢ ساعة عملي عدد الوحدات ٣					
٧. اسم مسؤول المقرر (اذكر جميع الأسماء، إذا كان هناك أكثر من اسم واحد)					
الاسم: د. احمد خزعل يونس البريد الإلكتروني: a.k.younis@ntu.edu.iq					
٨. اهداف المقرر					
الاهداف ١- معرفة انواع البيانات ٢- معرفة انواع الشبكات ٣- تعلم لغة الاله					
٩. استراتيجيات التدريس والتعلم					
الاستراتيجية المحاضرات النظرية والعملية					
١٠. بنية المقرر					
الأسبوع	الساعات	مخرجات التعلم المطلوبة	اسم الوحدة / أو الموضوع	طريقة التعليم	طريقة التقييم
1	4	ان يكون الطالب قادرا على معرفة انواع البيانات	Data types	نظري وعلمي	الامتحانات النظرية والعملية
2	4	ان يكون الطالب قادرا على معرفة انواع البيانات	Data types	نظري وعلمي	الامتحانات المكتوبة والشفوية

الامتحانات النظرية والعملية	نظري وعلمي	Structured data	ان يكون الطالب قادرا على معرفة انواع البيانات	4	3
الامتحانات النظرية والعملية	نظري وعلمي	Unstructured data	ان يكون الطالب قادرا على معرفة انواع البيانات	4	4
الامتحانات النظرية والعملية	نظري وعلمي	Images type	ان يكون الطالب قادرا على معرفة انواع البيانات	4	5
الامتحانات النظرية والعملية	نظري وعلمي	Random data	ان يكون الطالب قادرا على معرفة انواع البيانات	4	6
الامتحانات النظرية والعملية	نظري وعلمي	unsupervised networks	ان يكون الطالب قادرا على معرفة انواع الشبكات	4	7
الامتحانات النظرية والعملية	نظري وعلمي	unsupervised networks	ان يكون الطالب قادرا على معرفة انواع الشبكات	4	8
الامتحانات النظرية والعملية	نظري وعلمي	supervised networks	ان يكون الطالب قادرا على معرفة انواع الشبكات	4	9

الامتحانات النظرية والعملية	نظري وعلمي	supervised networks	ان يكون الطالب قادرا على معرفة انواع الشبكات	4	10
الامتحانات النظرية والعملية	نظري وعلمي	Supervised network	ان يكون الطالب قادرا على معرفة انواع الشبكات	4	11
الامتحانات النظرية والعملية	نظري وعلمي	Machine learning	ان يكون الطالب قادرا على اتقان لغة الاله	4	12
الامتحانات النظرية والعملية	نظري وعلمي	Machine learning	ان يكون الطالب قادرا على اتقان لغة الاله	4	13
الامتحانات النظرية والعملية	نظري وعلمي	Machine learning	ان يكون الطالب قادرا على اتقان لغة الاله	4	14
الامتحانات النظرية والعملية	نظري وعلمي	Machine learning	ان يكون الطالب قادرا على اتقان لغة الاله	4	15

١١. تقييم المقرر	
١٠ درجات امتحان يومي نظري و ١٠ درجة امتحان يومي عملي و ٢٠ درجة امتحان شهري نظري و ١٠ درجة امتحان شهري عملي و ١٠ درجات امتحان فصلي عملي و ٤٠ درجة امتحان فصلي نظري	
١٢. موارد التعلم والتعليم	
	Artificial Intelligence by Yun Peng, 2022
	https://www.coursera.org

نموذج وصف المقرر

١. اسم المقرر:					
الترميز					
٢. رمز المقرر:					
CCT408					
٣. الفصل الدراسي / السنة					
الفصل الثاني/المستوى الرابع					
٤. تاريخ إعداد الوصف					
٢٠٢٥/٦/١٠					
٥. أشكال الحضور المتاحة					
حضور					
٦. عدد الساعات المعتمدة (الإجمالي) / عدد الوحدات (الإجمالي)					
٢ ساعة نظري و ٢ ساعة عملي عدد الوحدات ٣					
٧. اسم مسؤول المقرر (اذكر جميع الأسماء، إذا كان هناك أكثر من اسم واحد)					
الاسم: د. عماد احمد محمد البريد الإلكتروني: e.a.mohammed@ntu.edu.iq					
٨. اهداف المقرر					
الاهداف ١-الامام بانواع الترميز ٢- انشاء ترميز اكتشاف الخطأ ٣- انشاء ترميز تصحيح الخطأ					
٩. استراتيجيات التدريس والتعلم					
الاستراتيجية المحاضرات النظرية والعملية					
١٠. بنية المقرر					
الأسبوع	الساعات	مخرجات التعلم المطلوبة	اسم الوحدة / أو الموضوع	طريقة التعليم	طريقة التقييم
1	4	ان يكون الطالب قادرا على الامام بانواع الترميز	Coding	نظري وعلمي	الامتحانات النظرية والعملية
2	4	ان يكون الطالب قادرا على الامام بانواع الترميز	Line coding	نظري وعلمي	الامتحانات المكتوبة والشفوية

الامتحانات النظرية والعملية	نظري وعلمي	Source coding	ان يكون الطالب قادرا على الالمام بانواع الترميز	4	3
الامتحانات النظرية والعملية	نظري وعلمي	Data compression	ان يكون الطالب قادرا على الالمام بانواع الترميز	4	4
الامتحانات النظرية والعملية	نظري وعلمي	Huffman code	ان يكون الطالب قادرا على الالمام بانواع الترميز	4	5
الامتحانات النظرية والعملية	نظري وعلمي	Shannon code	ان يكون الطالب قادرا على الالمام بانواع الترميز	4	6
الامتحانات النظرية والعملية	نظري وعلمي	Shannon-Fano code	ان يكون الطالب قادرا على الالمام بانواع الترميز	4	7
الامتحانات النظرية والعملية	نظري وعلمي	RLE code	ان يكون الطالب قادرا على الالمام بانواع الترميز	4	8
الامتحانات النظرية والعملية	نظري وعلمي	Channel coding	ان يكون الطالب قادرا على انشاء ترميز اكتشاف الخطأ	4	9

الامتحانات النظرية والعملية	نظري وعلمي	Error detection code	ان يكون الطالب قادرا على انشاء ترميز اكتشاف الخطأ	4	10
الامتحانات النظرية والعملية	نظري وعلمي	VRC code	ان يكون الطالب قادرا على انشاء ترميز اكتشاف الخطأ	4	11
الامتحانات النظرية والعملية	نظري وعلمي	LRC code	ان يكون الطالب قادرا على انشاء ترميز اكتشاف الخطأ	4	12
الامتحانات النظرية والعملية	نظري وعلمي	Error Correction code	ان يكون الطالب قادرا على انشاء ترميز تصحيح الخطأ	4	13
الامتحانات النظرية والعملية	نظري وعلمي	Hamming code	ان يكون الطالب قادرا على انشاء ترميز تصحيح الخطأ	4	14
الامتحانات النظرية والعملية	نظري وعلمي	Turbo code	ان يكون الطالب قادرا على انشاء ترميز تصحيح الخطأ	4	15

١١. تقييم المقرر	
١٠ درجات امتحان يومي نظري و ١٠ درجة امتحان يومي عملي و ٢٠ درجة امتحان شهري نظري و ١٠ درجة امتحان شهري عملي و ١٠ درجات امتحان فصلي عملي و ٤٠ درجة امتحان فصلي نظري	
١٢. موارد التعلم والتعليم	
	Information Theory: From Coding to Learning ,Yury Poliansky, 2021
	https://www.coursera.org

نموذج وصف المقرر

١. اسم المقرر:					
امنية الحاسبات					
٢. رمز المقرر:					
CCT409					
٣. الفصل الدراسي / السنة					
الفصل الثاني/المستوى الرابع					
٤. تاريخ إعداد الوصف					
٢٠٢٥/٦/١٠					
٥. أشكال الحضور المتاحة					
حضور					
٦. عدد الساعات المعتمدة (الإجمالي) / عدد الوحدات (الإجمالي)					
٢ ساعة نظري و ٢ ساعة عملي عدد الوحدات ٣					
٧. اسم مسؤول المقرر (اذكر جميع الأسماء، إذا كان هناك أكثر من اسم واحد)					
الاسم: د. فرقد حامد عبد الرحيم البريد الإلكتروني: frqhamid@ntu.edu.iq					
٨. اهداف المقرر					
الاهداف ١- معرفة انواع التشفير ٢- معرفة انواع التهديدات ٣- معرفة انواع الحماية					
٩. استراتيجيات التدريس والتعلم					
الاستراتيجية المحاضرات النظرية والعملية					
١٠. بنية المقرر					
الأسبوع	الساعات	مخرجات التعلم المطلوبة	اسم الوحدة / أو الموضوع	طريقة التعليم	طريقة التقييم
1	4	ان يكون الطالب قادرا على معرفة انواع التشفير	cryptography	نظري وعلمي	الامتحانات النظرية والعملية
2	4	ان يكون الطالب قادرا على معرفة انواع التشفير	cryptography	نظري وعلمي	الامتحانات المكتوبة والشفوية

الامتحانات النظرية والعملية	نظري وعلمي	Symmetric key cipher	ان يكون الطالب قادرا على معرفة انواع التشفير	4	3
الامتحانات النظرية والعملية	نظري وعلمي	Cesar cipher	ان يكون الطالب قادرا على معرفة انواع التشفير	4	4
الامتحانات النظرية والعملية	نظري وعلمي	Transposition cipher	ان يكون الطالب قادرا على معرفة انواع التشفير	4	5
الامتحانات النظرية والعملية	نظري وعلمي	Hell cipher	ان يكون الطالب قادرا على معرفة انواع التشفير	4	6
الامتحانات النظرية والعملية	نظري وعلمي	Asymmetric key cipher	ان يكون الطالب قادرا على معرفة انواع التشفير	4	7
الامتحانات النظرية والعملية	نظري وعلمي	Public key	ان يكون الطالب قادرا على معرفة انواع التشفير	4	8
الامتحانات النظرية والعملية	نظري وعلمي	RSA cipher	ان يكون الطالب قادرا على معرفة انواع التشفير	4	9

الامتحانات النظرية والعملية	نظري وعلمي	AES cipher	ان يكون الطالب قادرا على معرفة انواع التشفير	4	10
الامتحانات النظرية والعملية	نظري وعلمي	ADS cipher	ان يكون الطالب قادرا على معرفة انواع التشفير	4	11
الامتحانات النظرية والعملية	نظري وعلمي	viruses	ان يكون الطالب قادرا على معرفة انواع التهديدات	4	12
الامتحانات النظرية والعملية	نظري وعلمي	Trojan virus	ان يكون الطالب قادرا على معرفة انواع التهديدات	4	13
الامتحانات النظرية والعملية	نظري وعلمي	One time pad	ان يكون الطالب قادرا على معرفة انواع الحماية	4	14
الامتحانات النظرية والعملية	نظري وعلمي	Salt passwords	ان يكون الطالب قادرا على معرفة انواع الحماية	4	15

١١. تقييم المقرر	
١٠ درجات امتحان يومي نظري و ١٠ درجة امتحان يومي عملي و ٢٠ درجة امتحان شهري نظري و ١٠ درجة امتحان شهري عملي و ١٠ درجات امتحان فصلي عملي و ٤٠ درجة امتحان فصلي نظري	
١٢. موارد التعلم والتعليم	
	Security Engineering, Ross Anderson, 2 nd edition
	https://www.coursera.org

نموذج وصف المقرر

١. اسم المقرر:					
معالج دقيق ٢					
٢. رمز المقرر:					
CCT410					
٣. الفصل الدراسي / السنة					
الفصل الثاني/المستوى الرابع					
٤. تاريخ إعداد الوصف					
٢٠٢٥/٦/١٠					
٥. أشكال الحضور المتاحة					
حضور					
٦. عدد الساعات المعتمدة (الإجمالي) / عدد الوحدات (الإجمالي)					
٢ ساعة نظري و ٢ ساعة عملي عدد الوحدات ٣					
٧. اسم مسؤول المقرر (اذكر جميع الأسماء، إذا كان هناك أكثر من اسم واحد)					
الاسم: د. احمد فالح محمود البريد الإلكتروني: ahmed.faleh@ntu.edu.iq					
٨. اهداف المقرر					
الاهداف ١- معرفة انواع المعالجات ٢- معرفة انواع المتحكمات ٣- التعامل مع اجهزة التشبيك					
٩. استراتيجيات التدريس والتعلم					
الاستراتيجية المحاضرات النظرية والعملية					
١٠. بنية المقرر					
الأسبوع	الساعات	مخرجات التعلم المطلوبة	اسم الوحدة / أو الموضوع	طريقة التعليم	طريقة التقييم
1	4	ان يكون الطالب قادرا على معرفة انواع المعالجات	Microprocessors	نظري وعلمي	الامتحانات النظرية والعملية
2	4	ان يكون الطالب قادرا على معرفة انواع المعالجات	Microprocessors	نظري وعلمي	الامتحانات المكتوبة والشفوية

الامتحانات النظرية والعملية	نظري وعلمي	8088 processor	ان يكون الطالب قادرا على معرفة انواع المعالجات	4	3
الامتحانات النظرية والعملية	نظري وعلمي	386 processor	ان يكون الطالب قادرا على معرفة انواع المعالجات	4	4
الامتحانات النظرية والعملية	نظري وعلمي	Pentium processor	ان يكون الطالب قادرا على معرفة انواع المعالجات	4	5
الامتحانات النظرية والعملية	نظري وعلمي	Dual core	ان يكون الطالب قادرا على معرفة انواع المعالجات	4	6
الامتحانات النظرية والعملية	نظري وعلمي	Quad-core	ان يكون الطالب قادرا على معرفة انواع المعالجات	4	7
الامتحانات النظرية والعملية	نظري وعلمي	Octal-core	ان يكون الطالب قادرا على معرفة انواع المعالجات	4	8
الامتحانات النظرية والعملية	نظري وعلمي	microcontrollers	ان يكون الطالب قادرا على معرفة انواع المتحكمات	4	9

الامتحانات النظرية والعملية	نظري وعلمي	microcontrollers	ان يكون الطالب قادرا على معرفة انواع المتحكمات	4	10
الامتحانات النظرية والعملية	نظري وعلمي	counters	ان يكون الطالب قادرا على التعامل مع اجهزة التشبيك	4	11
الامتحانات النظرية والعملية	نظري وعلمي	Serial ports	ان يكون الطالب قادرا على التعامل مع اجهزة التشبيك	4	12
الامتحانات النظرية والعملية	نظري وعلمي	latch	ان يكون الطالب قادرا على التعامل مع اجهزة التشبيك	4	13
الامتحانات النظرية والعملية	نظري وعلمي	buffers	ان يكون الطالب قادرا على التعامل مع اجهزة التشبيك	4	14
الامتحانات النظرية والعملية	نظري وعلمي	Parallel ports	ان يكون الطالب قادرا على التعامل مع اجهزة التشبيك	4	15

١١. تقييم المقرر	
١٠ درجات امتحان يومي نظري و ١٠ درجة امتحان يومي عملي و ٢٠ درجة امتحان شهري نظري و ١٠ درجة امتحان شهري عملي و ١٠ درجات امتحان فصلي عملي و ٤٠ درجة امتحان فصلي نظري	
١٢. موارد التعلم والتعليم	
	Microprocessors systems, R.J.Mitchell, 2022
	https://www.coursera.org

نموذج وصف المقرر

١. اسم المقرر:					
ذكاء اصطناعي 2					
٢. رمز المقرر:					
CCT411					
٣. الفصل الدراسي / السنة					
الفصل الثاني/المستوى الرابع					
٤. تاريخ إعداد الوصف					
٢٠٢٥/٦/١٠					
٥. أشكال الحضور المتاحة					
حضور					
٦. عدد الساعات المعتمدة (الإجمالي) / عدد الوحدات (الإجمالي)					
٢ ساعة نظري و ٢ ساعة عملي عدد الوحدات ٣					
٧. اسم مسؤول المقرر (اذكر جميع الأسماء، إذا كان هناك أكثر من اسم واحد)					
الاسم: د. احمد خزعل يونس البريد الإلكتروني: a.k.younis@ntu.edu.iq					
٨. اهداف المقرر					
الاهداف					
١- معرفة انواع التعلم ٢- إتقان التعلم العميق ٣- معرفة تطبيقات الذكاء الاصطناعي					
٩. استراتيجيات التدريس والتعلم					
الاستراتيجية					
المحاضرات النظرية والعملية					
١٠. بنية المقرر					
الأسبوع	الساعات	مخرجات التعلم المطلوبة	اسم الوحدة / أو الموضوع	طريقة التعليم	طريقة التقييم
1	4	ان يكون الطالب قادرا على معرفة انواع التعلم	Machine learning	نظري وعلمي	الامتحانات النظرية والعملية

الامتحانات المكتوبة والشفوية	نظري وعلمي	Deep learning	ان يكون الطالب قادرا على اتقان التعلم العميق	4	2
الامتحانات النظرية والعملية	نظري وعلمي	Deep learning algorithms	ان يكون الطالب قادرا على اتقان التعلم العميق	4	3
الامتحانات النظرية والعملية	نظري وعلمي	Resnet algorithm	ان يكون الطالب قادرا على اتقان التعلم العميق	4	4
الامتحانات النظرية والعملية	نظري وعلمي	CNN Algorithm	ان يكون الطالب قادرا على اتقان التعلم العميق	4	5
الامتحانات النظرية والعملية	نظري وعلمي	ANN algorithm	ان يكون الطالب قادرا على اتقان التعلم العميق	4	6
الامتحانات النظرية والعملية	نظري وعلمي	RNN algorithm	ان يكون الطالب قادرا على اتقان التعلم العميق	4	7
الامتحانات النظرية والعملية	نظري وعلمي	Activation functions	ان يكون الطالب قادرا على اتقان التعلم العميق	4	8

الامتحانات النظرية والعملية	نظري وعلمي	ReLu function	ان يكون الطالب قادرا على اتقان التعلم العميق	4	9
الامتحانات النظرية والعملية	نظري وعلمي	Log sigmoid function	ان يكون الطالب قادرا على اتقان التعلم العميق	4	10
الامتحانات النظرية والعملية	نظري وعلمي	Soft max	ان يكون الطالب قادرا على اتقان التعلم العميق	4	11
الامتحانات النظرية والعملية	نظري وعلمي	Images training	ان يكون الطالب قادرا على اتقان لغة الاله	4	12
الامتحانات النظرية والعملية	نظري وعلمي	Video training	ان يكون الطالب قادرا على معرفة تطبيقات الذكاء الاصطناعي	4	13
الامتحانات النظرية والعملية	نظري وعلمي	Classification	ان يكون الطالب قادرا على معرفة تطبيقات الذكاء الاصطناعي	4	14
الامتحانات النظرية والعملية	نظري وعلمي	prediction	ان يكون الطالب قادرا على معرفة تطبيقات الذكاء الاصطناعي	4	15

١١. تقييم المقرر					
١٠ درجات امتحان يومي نظري و ١٠ درجة امتحان يومي عملي و ٢٠ درجة امتحان شهري نظري و ١٠ درجة امتحان شهري عملي و ١٠ درجات امتحان فصلي عملي و ٤٠ درجة امتحان فصلي نظري					
١٢. موارد التعلم والتعليم					
			Artificial Intelligence by Yun Peng, 2022		
			https://www.coursera.org		